

TABLE OF CONTENTS - CONTINUED FROM PAGE 1

<u>DRAWING NUMBER</u>	<u>NO. OF SHEETS</u>	<u>DESCRIPTION</u>
D-UA-5411036-0-0 D-CS-5411036-0-1 K-PL-5411036-0-DBP	(SHT 1)	POWER LINE MONITOR 15V REG POWER LINE MONITOR 15V REG (CS) POWER LINE MONITOR 15V REG (PL)
D-UA-H7440-0-0 D-UA-5411793-0-0 D-CS-5411793-0-1 A-PL-5411793-0-0	(SHT 1) (SHT 1)	H7440 POWER SUPPLY ASSY. +5V REGULATOR POWER SUPPLY +5V REGULATOR POWER SUPPLY (CS) +5V REGULATOR POWER SUPPLY (PL)
D-CS-H730-0-1 D-AD-7009938-0-0		MASS BUS TERMINATOR TERMINATOR PACK ASSY.
D-UA-5413762-0-0 D-CS-5413762-0-1 K-PL-5413762-0-DBP		RM02/03 DUAL PORT SWITCH RM02/03 DUAL PORT SWITCH (CS) RM02/03 DUAL PORT SWITCH (PL)
D-AD-7017748-0-0 K-PL-7017748-0-DBP		CABINET AND MBA ASSY. DIAL PORT CABINET AND MBA ASSY. DIAL PORT (PL)
D-AD-7017747-0-0 K-PL-7017747-0-DRP		MBA CHASSIS ASSY. (UPPER) MBA CHASSIS ASSY. (UPPER) (PL)

TITLE	SIZE CODE	NUMBER	REV
RM05 DISK DRIVE	B TC	RM05-0-1	B

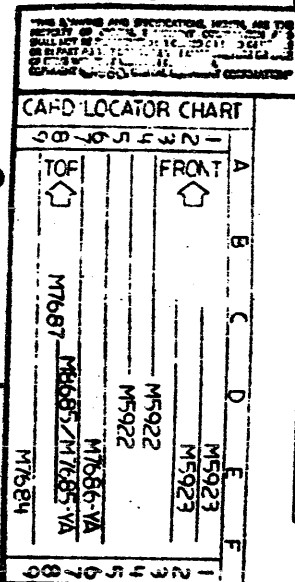


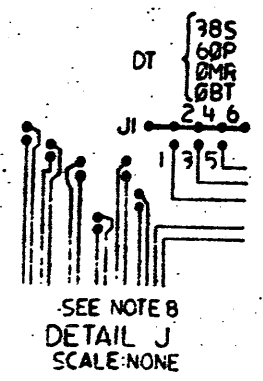
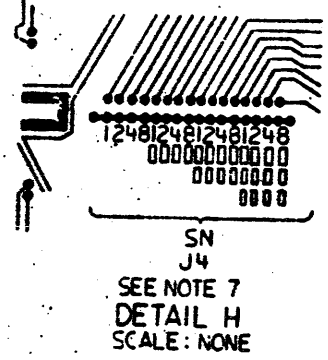
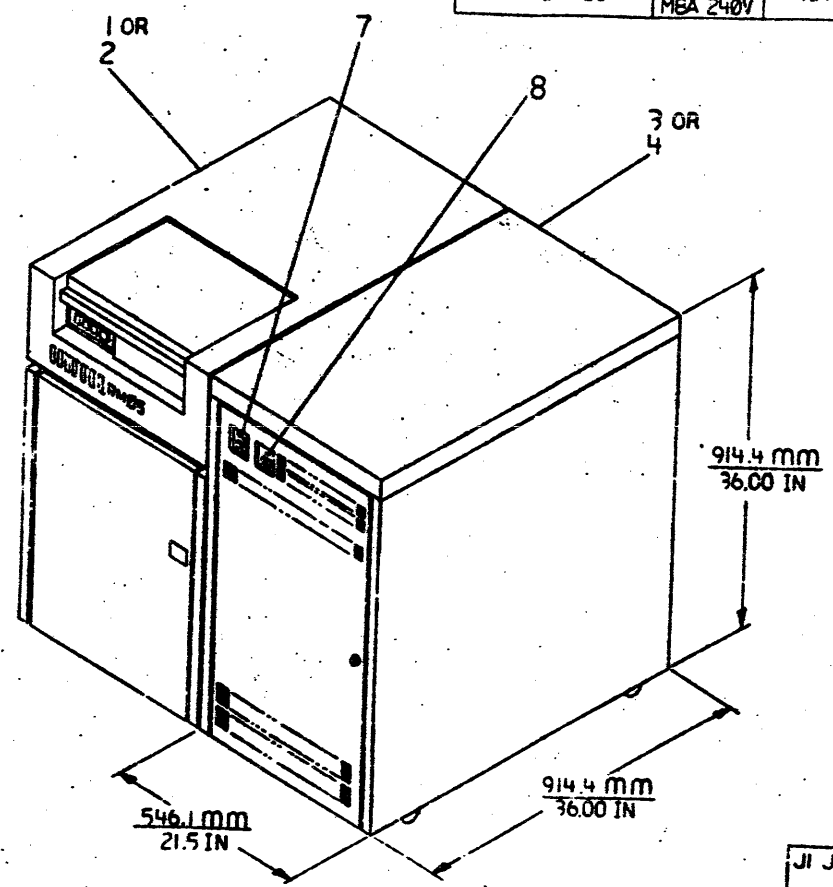
TABLE A		
X INDICATES INSTALLING A JUMPER		
S N BIT	JUMPER	RESULT S N BIT
1	X	
2		2
4	X	
8	X	
10		10
20		20
40		40
60	X	
100		100
200	X	
400	X	
800	X	
1000		1000
2000		2000
4000	X	
6000	X	
		3172 TOTAL

120 VOLT 1 PH MODEL RM05-AA
60 HZ 2+G WIRE CAB.
24 AMP S.N. CX-XXXXX

DETAIL G
SCALE: NONE
SEE NOTE 6

LEGEND			
MODEL/OPTION NO.	VOLTAGE	FREQUENCY	DESCRIPTION
RM05-AA	DRIVE 208V MBA 120V	60 HZ	256 MBYTE DRIVE W/ SINGLE PORT MBA AND CABINET
RM05-AB	DRIVE 220V MBA 240V	50 HZ	256 MBYTE DRIVE W/ SINGLE PORT MBA CABINET
RM05-AC	DRIVE 208V MBA 120V	60 HZ	256 MBYTE DRIVE W/ SINGLE PORT MBA (SECOND MBA)
RM05-AD	DRIVE 220V MBA 240V	50 HZ	256 MBYTE DRIVE W/ SINGLE PORT MBA (SECOND MBA)
RM05-BA	DRIVE 205V MBA 120V	60 HZ	256 MBYTE DRIVE W/ DUAL PORT MBA AND CABINET
RM05-BB	DRIVE 220V MBA 240V	50 HZ	256 MBYTE DRIVE W/ DUAL PORT MBA AND CABINET
RM05-BC	DRIVE 208V MBA 120V	60 HZ	256 MBYTE DRIVE W/ DUAL PORT MBA (SECOND MBA)
RM05-BD	DRIVE 220V MBA 240V	50 HZ	256 MBYTE DRIVE W/ DUAL PORT MBA (SECOND MBA)

- NOTES:
- REMOVE PART FROM MBA AND SET ASIDE. IT IS NOT USED IN THIS CONFIGURATION.
 - DELETED
 - DIAGRAM
 - DELETED
 - DELETED
 - MANUFACTURING ORGANIZATION TO COMPLETE LABEL INFORMATION ACCORDING TO DETAIL G. (DETAIL G SHOWS MODEL RM05-AA AS EXAMPLE ONLY).
 - THIRTY TWO WIREWRAP POSTS, IDENTIFIED AS J4 OF THE MASS BUS ADAPTER W/ ASSEMBLY. ALLOW THE SERIAL NO. TO BE WIRED INTO THE BACKPLANE. THE 16 POST PAIRS ARE IDENTIFIED AS 1, 2, 4, 2000, 4000, 8000 PROVIDING A BCD REPRESENTATION OF THE SERIAL NO.. THE LAST FOUR DIGITS OF THE DEC SERIAL NO. ARE TO BE USED AS THE WIRED SERIAL NO.. A JUMPER (1214314-00) MUST BE INSTALLED FOR EACH LOGIC ZERO IN THE WIRED SERIAL NO.. FOR EXAMPLE IF THE SERIAL NO. IS 3172, IT IS WIRED PER DETAIL "H" AND TABLE "A".
 - THE MASS BUS ADAPTER MUST HAVE THE FOLLOWING JUMPERS INSTALLED ON J1 OF THE WIRED ASSY. SEE DETAIL "J" AND JUMPER CONFIGURATION.
 - DELETED
 - DELETED
 - TERMINATORS ARE ALWAYS INSTALLED IN J2 AND J6 OF THE MBA BACKPLANE ONLY ON THE LAST MBA IN A SERIES.
 - THIS ITEM SUPPLIED WITH CABINET AND MBA ASSY, D.P. (7017748-00) OR CABINET AND MBA ASSY, S.P. (7017619-00).
 - TO UPGRADE A SINGLE PORT MBA IN THE FIELD, ORDER 1 DUAL ACCESS KIT (C-AD-RM05-C-0) PER EACH MBA.
- CAUTION: OFF-SHEET PARTS LIST EXISTS. SEE K-PL- RM05-0-DBP (21187).



SEE NOTE 8

J1 JUMPER CONFIGURATION	
JUMPER INSERTED	
SINGLE PORT DRIVE TYPE REG	5-6 20027
DUAL PORT DRIVE TYPE REG	24027

ITEM	A	B	C	D
1	150 CONTAINERS TO 1000'S			
2	150 CONTAINERS TO 1000'S			
3	150 CONTAINERS TO 1000'S			
4	150 CONTAINERS TO 1000'S			
5	150 CONTAINERS TO 1000'S			
6	150 CONTAINERS TO 1000'S			
7	150 CONTAINERS TO 1000'S			
8	150 CONTAINERS TO 1000'S			
9	150 CONTAINERS TO 1000'S			
10	150 CONTAINERS TO 1000'S			
11	150 CONTAINERS TO 1000'S			
12	150 CONTAINERS TO 1000'S			
13	150 CONTAINERS TO 1000'S			
14	150 CONTAINERS TO 1000'S			
15	150 CONTAINERS TO 1000'S			
16	150 CONTAINERS TO 1000'S			
17	150 CONTAINERS TO 1000'S			
18	150 CONTAINERS TO 1000'S			
19	150 CONTAINERS TO 1000'S			
20	150 CONTAINERS TO 1000'S			
21	150 CONTAINERS TO 1000'S			
22	150 CONTAINERS TO 1000'S			
23	150 CONTAINERS TO 1000'S			
24	150 CONTAINERS TO 1000'S			
25	150 CONTAINERS TO 1000'S			
26	150 CONTAINERS TO 1000'S			
27	150 CONTAINERS TO 1000'S			
28	150 CONTAINERS TO 1000'S			
29	150 CONTAINERS TO 1000'S			
30	150 CONTAINERS TO 1000'S			
31	150 CONTAINERS TO 1000'S			
32	150 CONTAINERS TO 1000'S			

DESCRIPTION	QTY	UNIT	REVISION
RM05	1	EA	1

QUANTITY & VARIATION

DATE: 10/10/83

REV: 1

SCALE: 1:1

SHEET: 1 OF 4

TITLE: RM05 DISK DRIVE ASSEMBLY

SIZE: D

NO. OF SHEETS: 4

NO. OF SHEETS: 4

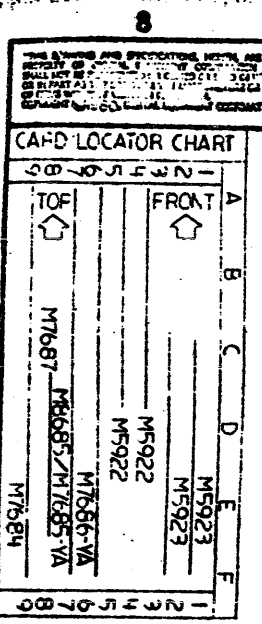
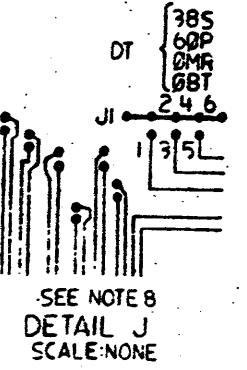
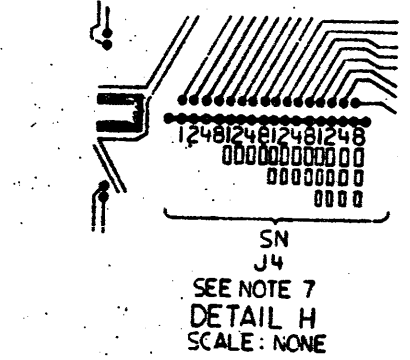
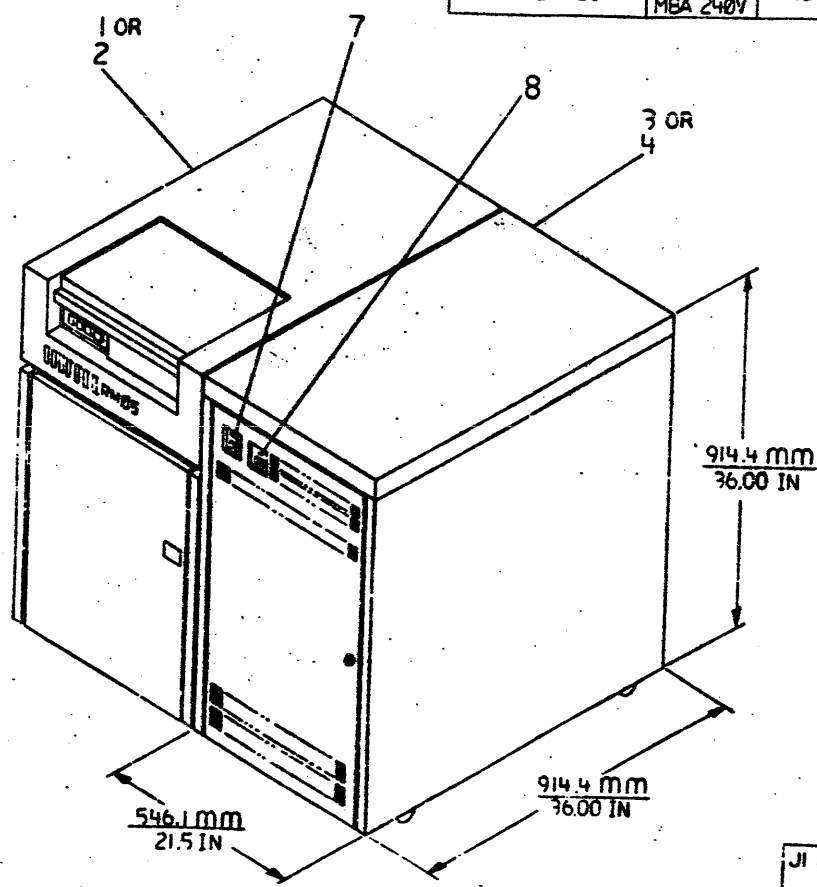


TABLE A		
X INDICATES INSTALLING A JUMPER		
S N BIT	JUMPER	RESULT S N BIT
1	X	
2		2
4	X	
8	X	
10		10
20		20
40		40
80	X	
100		100
200	X	
400	X	
600	X	
1000		1000
2000		2000
4000	X	
6000	X	
		3172 TOTAL

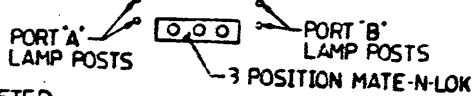
120 VOLT 60 HZ 1 PH 2+G WIRE 24 AMP MODEL RM05-AA CAB. S.N. CX-XXXX

DETAIL G
SCALE: NONE
SEE NOTE 6

LEGEND			
MODEL/OPTION NO	VOLTAGE	FREQUENCY	DESCRIPTION
RM05-AA	DRIVE 208V MBA 120V	60 HZ	256 MBYTE DRIVE W/ SINGLE PORT MBA AND CABINET
RM05-AB	DRIVE 220V MBA 240V	50 HZ	256 MBYTE DRIVE W/ SINGLE PORT MBA CABINET
RM05-AC	DRIVE 208V MBA 120V	60 HZ	256 MBYTE DRIVE W/ SINGLE PORT MBA (SECOND MBA)
RM05-AD	DRIVE 220V MBA 240V	50 HZ	256 MBYTE DRIVE W/ SINGLE PORT MBA (SECOND MBA)
RM05-BA	DRIVE 208V MBA 120V	60 HZ	256 MBYTE DRIVE W/ DUAL PORT MBA AND CABINET
RM05-BB	DRIVE 220V MBA 240V	50 HZ	256 MBYTE DRIVE W/ DUAL PORT MBA AND CABINET
RM05-BC	DRIVE 208V MBA 120V	60 HZ	256 MBYTE DRIVE W/ DUAL PORT MBA (SECOND MBA)
RM05-BD	DRIVE 220V MBA 240V	50 HZ	256 MBYTE DRIVE W/ DUAL PORT MBA (SECOND MBA)



- NOTES:
- REMOVE PART FROM MBA AND SET ASIDE. IT IS NOT USED IN THIS CONFIGURATION.
 - DELETED
 - DIAGRAM
 - DELETED
 - DELETED
 - MANUFACTURING ORGANIZATION TO COMPLETE LABEL INFORMATION ACCORDING TO DETAIL G. (DETAIL G SHOWS MODEL RM05-AA AS EXAMPLE ONLY).
 - THIRTY TWO WIREWRAP POSTS, IDENTIFIED AS J4 OF THE MASS BUS ADAPTER WIRED ASSEMBLY. ALLOW THE SERIAL NO. TO BE WIRED INTO THE BACKPLANE. THE 16 POST PAIRS ARE IDENTIFIED AS 1, 2, 4, 2000, 4000, 8000 PROVIDING A BCD REPRESENTATION OF THE SERIAL NO. THE LAST FOUR DIGITS OF THE DEC SERIAL NO. ARE TO BE USED AS THE WIRED SERIAL NO. A JUMPER (1214314-00) MUST BE INSTALLED FOR EACH LOGIC ZERO IN THE WIRED SERIAL NO. FOR EXAMPLE IF THE SERIAL NO. IS 3172, IT IS WIRED PER DETAIL "H" AND TABLE "A".
 - THE MASS BUS ADAPTER MUST HAVE THE FOLLOWING JUMPERS INSTALLED ON J1 OF THE WIRED ASSY. SEE DETAIL "J" AND JUMPER CONFIGURATION.
 - DELETED
 - DELETED
 - TERMINATORS ARE ALWAYS INSTALLED IN J2 AND J6 OF THE MBA BACKPLANE ONLY ON THE LAST MBA IN A SERIES.
 - THIS ITEM SUPPLIED WITH CABINET AND MBA ASSY. D.P. (7017748-00) OR CABINET AND MBA ASSY. S.P. (7017613-00).
 - TO UPGRADE A SINGLE PORT MBA IN THE FIELD, ORDER 1 DUAL ACCESS KIT (C-AD-RM05-C-0) PER EACH MBA.
- CAUTION: OFF-SHEET PARTS LIST EXISTS.
SEE K-PL-RM05-0-D8P (21187).



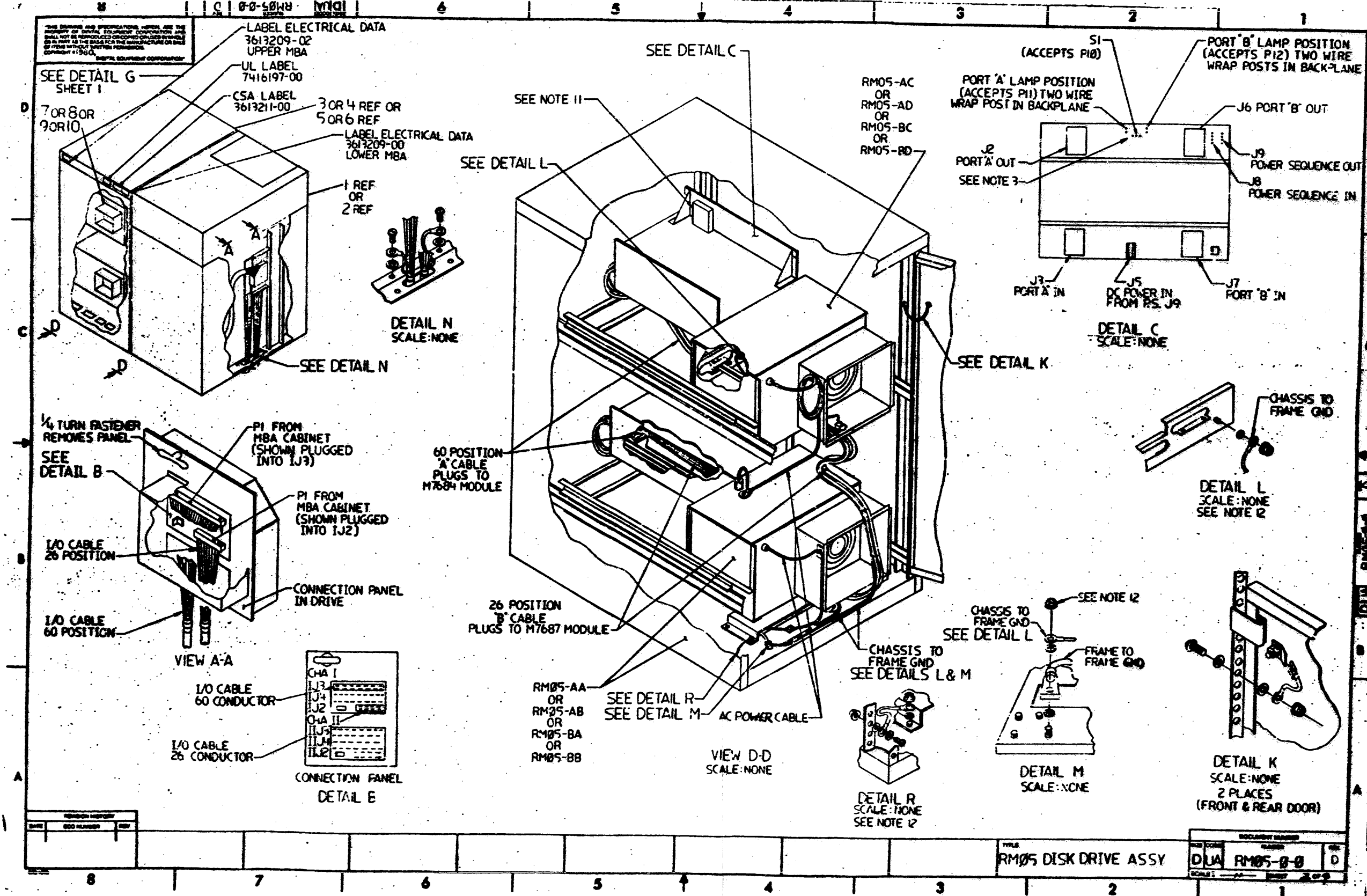
CARD LOCATOR CHART				
ROW	A	B	C	D
1				
2				
3				
4				
5				
6				
7				
8				
9				
10				

SEE NOTE 8

J1 JUMPER CONFIGURATION	
JUMPER INSERTED	
SINGLE PORT DRIVE TYPE REG	5-6 20027
DUAL PORT DRIVE TYPE REG	24027

DESCRIPTION			DWG. PART NO.		ITEM NO.
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES					
APPROX. DIMENSIONS	CLASS. OF ACCURACY	CHECK ONE	DIMENSIONS RANGE IN INCHES		
SURFACE QUALITY IN	MICRONS	AS SHOWN	0.125	0.25	0.50
		PREFERRED	0.125	0.25	0.50
QUANTITY & VARIATION	SYMBOLS	PREFERRED	0.125	0.25	0.50
THIRD ANGLE PROJECTION			FIRST USED FOR		
REVISE DIMS AND BREAK DIMS CORNERS			PART USED FOR		
DO NOT SCALE DIMS			TITLE		
MATERIALS SEE PARTS LIST			RM05		
SHEET			RM05 DISK DRIVE ASSEMBLY		
OF 4			REV.		
Dwg. No. RM05-0-0			DIA RM05-0-0		

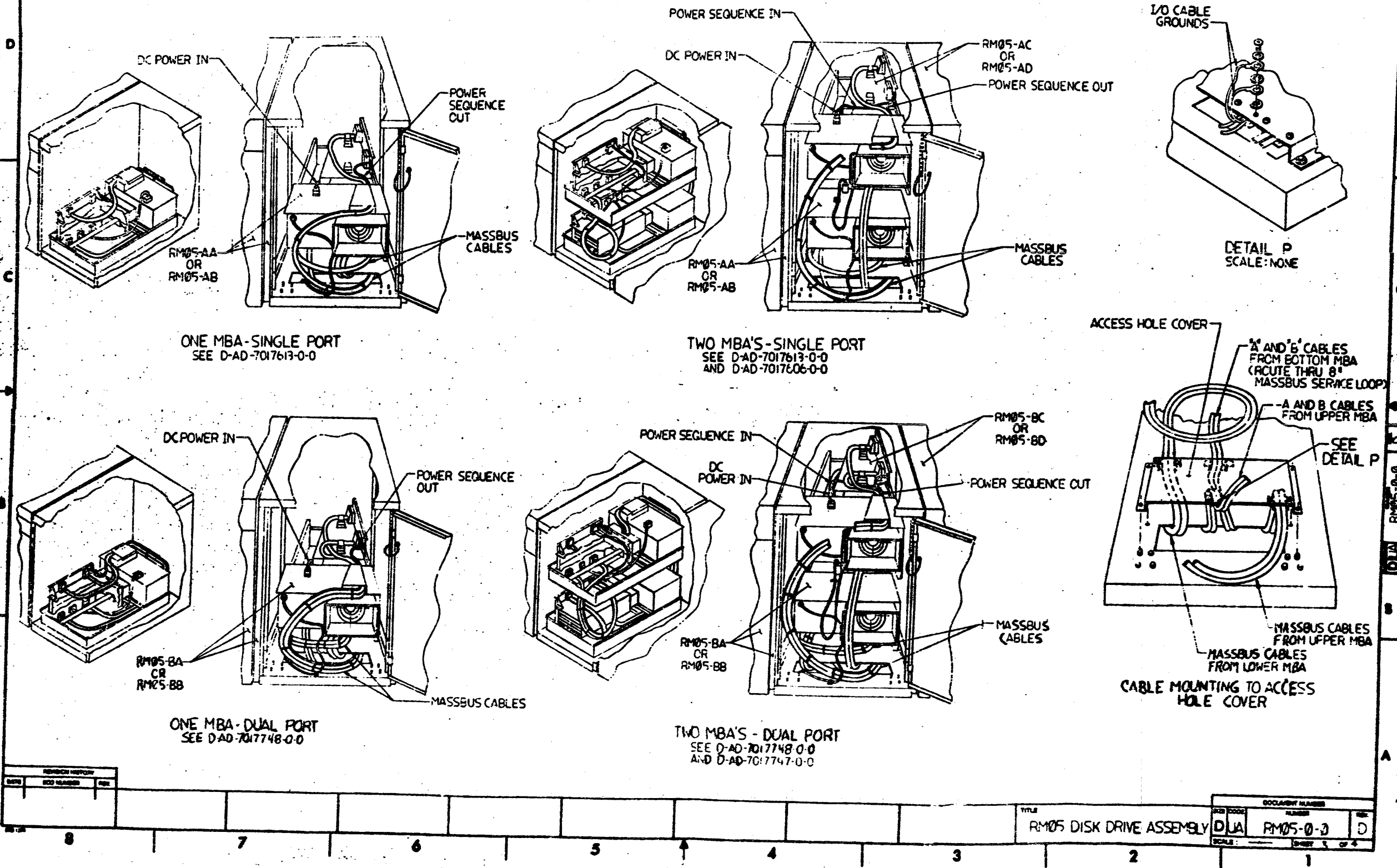
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REV	DESCRIPTION	DATE

TITLE		DRAWING NUMBER	
RM05 DISK DRIVE ASSY		DJA RM05-0-0	
SCALE: 1			

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 DOCUMENT # 17-00



REVISION HISTORY		
DATE	REV. NUMBER	REV.

TITLE		DOCUMENT NUMBER	
RM05 DISK DRIVE ASSEMBLY		DJA	RM05-0-0
SCALE: 1/4" = 1"		PAGE 1 OF 4	

8

0-0-50WB MJD

6

5

4

3

2

1

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CABLE CHART RM05-AC/AD

CABLE CHART RM05-BC/BD

CABLE APPEARS ON DRAWING NO.	DESCRIPTION	FROM	TO	REMARKS
D-AD-7018322-0-0	JUMPER, POWER SEQUENCE			SEE NOTE 1
D-AD-7017605-0-0	POWER SEQUENCE CABLE	J9 LOWER MBA	J8 UPPER MBA	
D-AD-7017747-0-0	MASS BUS CABLE, PORT 'A'	J2 LOWER MBA	J3 UPPER MBA	
D-AD-7018322-0-0	60 CONDUCTOR I/O CABLE	J1 ON M7684	IJ3 ON DISK DRIVE	
D-AD-7018322-0-0	26 CONDUCTOR I/O CABLE	J1 ON M7687	IJ2 ON DISK DRIVE	
D-AD-7018322-0-0	LINE CORD, AC INPUT	BACK OF POWER SUPPLY, UPPER MBA	J1 ON POWER SUPPLY, LOWER MBA	

CABLE APPEARS ON DRAWING NO.	DESCRIPTION	FROM	TO	REMARKS
D-AD-7018322-0-0	JUMPER, POWER SEQUENCE			SEE NOTE 1
D-AD-7017747-0-0	POWER SEQUENCE CABLE	J9 LOWER MBA	J8 UPPER MBA	
D-AD-7017748-0-0	MASS BUS CABLE, PORT 'A'	J2 LOWER MBA	J3 UPPER MBA	
D-AD-7018322-0-0	60 CONDUCTOR I/O CABLE	J1 ON M7684	IJ3 ON DISK DRIVE	
D-AD-7018322-0-0	26 CONDUCTOR I/O CABLE	J1 ON M7687	IJ2 ON DISK DRIVE	
D-AD-7018322-0-0	LINE CORD, AC INPUT	BACK OF POWER SUPPLY, UPPER MBA	J1 ON POWER SUPPLY, LOWER MBA	
D-AD-7017747-0-0	MASS BUS CABLE, PORT 'B'	J6 LOWER MBA	J7 UPPER MBA	

CABLE CHART RM05-BA/BB

CABLE APPEARS ON DRAWING NO.	DESCRIPTION	FROM	TO	REMARKS
D-AD-7018322-0-0	JUMPER, POWER SEQUENCE		PLUGS INTO J8 ON LOWER MBA	
D-AD-7017748-0-0	MASS BUS CABLE, PORT 'A'	HOST SYSTEM OR PREVIOUS MBA	J3 LOWER MBA	
D-AD-7018322-0-0	60 CONDUCTOR I/O CABLE	J1 ON M7684	IJ3 ON DISK DRIVE	
D-AD-7018322-0-0	26 CONDUCTOR I/O CABLE	J1 ON M7687	IJ2 ON DISK DRIVE	
D-AD-7018322-0-0	LINE CORD, AC INPUT	BACK OF POWER SUPPLY, LOWER MBA	WALL SOCKET	
D-AD-7017748-0-0	MASS BUS CABLE, PORT 'B'	HOST SYSTEM	J7 LOWER MBA	
D-AD-7017605-0-0	DUAL PORT HARNESS ASSY.	J1 SWITCH PANEL ASSEMBLY	P11 TO PORT 'A' LAMP POSTS P12 TO PORT 'B' LAMP POSTS P10 TO 3PIN MATE-IN-LOK, MOUNTED IN SWITCH POSITION	SEE NOTE 3

CABLE CHART RM05-AA/AB

CABLE APPEARS ON DRAWING NO.	DESCRIPTION	FROM	TO	REMARKS
D-AD-7018322-0-0	JUMPER, POWER SEQUENCE		PLUG INTO J8 ON LOWER MBA	
D-AD-7017619-0-0	MASS BUS CABLE, PORT 'A'	HOST SYSTEM OR PREVIOUS MBA	J3 LOWER MBA	
D-AD-7018322-0-0	60 CONDUCTOR I/O CABLE	J1 ON M7684	IJ3 ON DISK DRIVE	
D-AD-7018322-0-0	26 CONDUCTOR I/O CABLE	J1 ON M7687	IJ2 ON DISK DRIVE	
D-AD-7018322-0-0	LINE CORD, AC INPUT	BACK OF POWER SUPPLY, LOWER MBA	WALL SOCKET	

DATE	ECO NUMBER	BY

RM05 DISK DRIVE ASSEMBLY

DUA RM05-0-0 D

8

7

6

5

4

3

2

1

LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION							
					AA	AB	AC	AD	BA	BB	BC	BD
1	1	D-AD-7017618-0-0	7017618-00	REWORK DRAWING, RM05 DISK DRIVE	1	-	1	-	1	-	1	-
2	2	D-AD-7017618-0-1	7017618-01	REWORK DRAWING, RM05 DISK DRIVE	-	1	-	1	-	1	-	1
3	3	D-AD-7017613-0-0	7017613-00	CABINET & MBA ASSY S.P. (60 HZ)	1	-	-	-	-	-	-	-
4	4	D-AD-7017613-0-0	7017613-01	CABINET & MBA ASSY S. P. (50 HZ)	-	1	-	-	-	-	-	-
5	5		7017748-00	CABINET & MBA ASSY D. P. (60HZ)	-	-	-	-	1	-	-	-
6	6		7017748-01	CABINET & MBA ASSY D. P. (50 HZ)	-	-	-	-	-	1	-	-
7	7		7017747-00	MBA ASSY, DUAL PORT (60 HZ)	-	-	-	-	-	-	1	-
8	8		7017747-01	MBA ASSY, DUAL PORT (50 HZ)	-	-	-	-	-	-	-	1
9	9		7017606-00	MBA ASSY, SINGLE PORT (60HZ)	-	-	1	-	-	-	-	-
10	10		7017606-01	MBA ASSY, SINGLE PORT (50HZ)	-	-	-	1	-	-	-	-
11	11		3017107-00	DISK PACK RM05P DATA	1	1	1	1	1	1	1	1

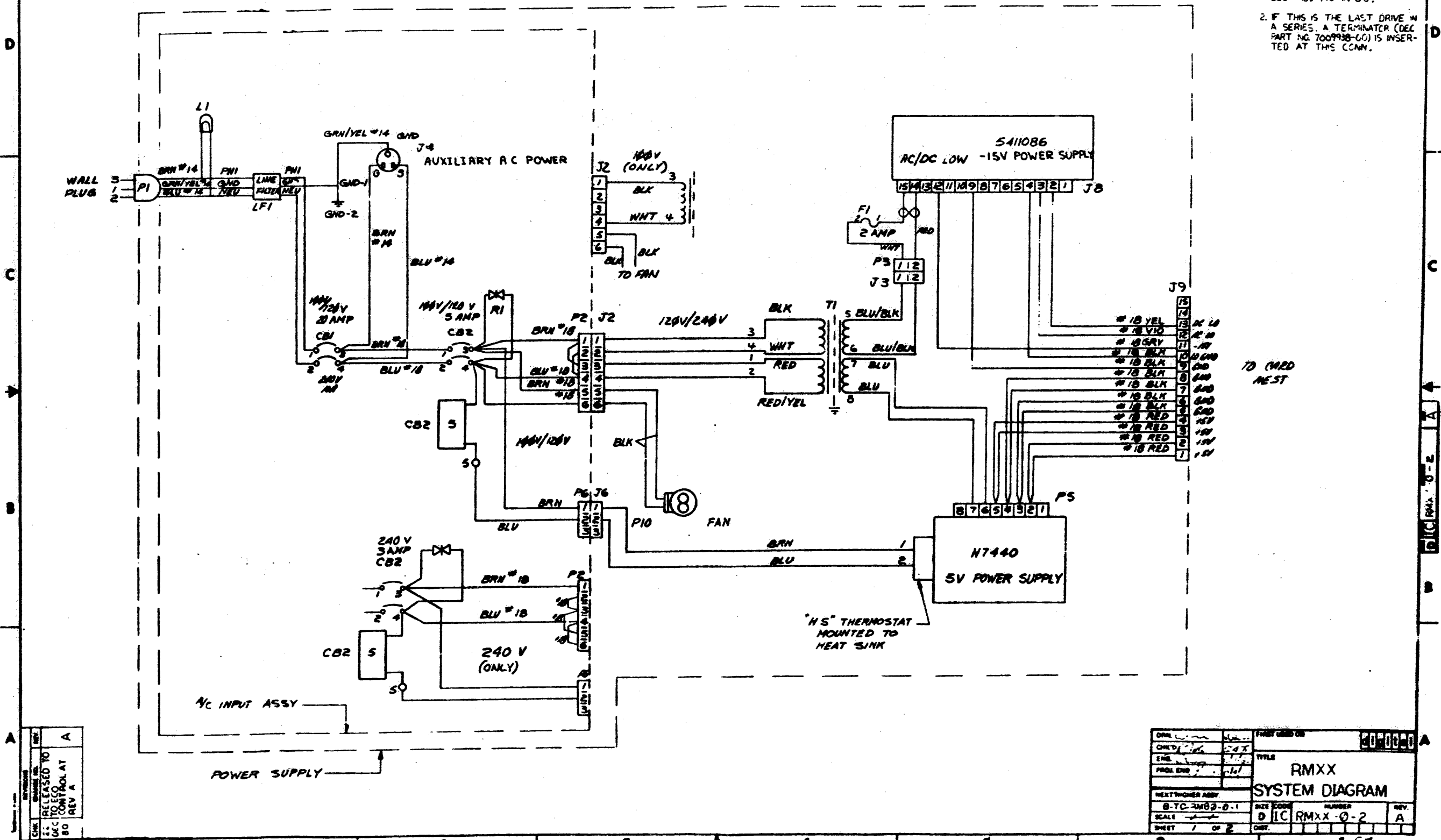
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ENG	ECO NUMBER	REV	SECTION A OF A	CHK'D: B. NICHOLE	DATE: 05-FEB-80	TITLE PARTS LIST	
	INITIAL	*	SECTION, VARIATION INDEX	DES.ENG.: A. CLARK	DATE: 05-FEB-80	RM05 DISK DRIVE ASSEMBLY	
ME	RM05-CX002	A	[A] AA,AB,AC,AD,BA,BB,				
ME	RM05-CX003	B	BC,BD				
ME	RM05-CX004	C	[B]	RESP.ENG.: B. MONTERO	DATE: 05-FEB-80	DOCUMENT NUMBER	
WH	RM05-CX05A	D	[C]				
	<i>Handwritten</i>		[D]	MFG.ENG.: B. BASSETT	DATE: 05-FEB-80	SIZE: K	CODE: PL
			[E]	ASSEMBLY NUMBER: B-UA-RM05-0-0	TOP DOCUMENT NUMBER: B-DD-RM05-0	NUMBER: RM05-0-DBP	REV: D
			[F]			FILE NAME: Z1187D.PLS	EDIT: 35

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Handwritten notes:
R.D.M.
12/11/80
15/1/80

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- NOTES:
1. IF THIS IS THE FIRST DRIVE IN A SERIES, INSTALL JUMPER DEC #7009490 IN J8.
 2. IF THIS IS THE LAST DRIVE IN A SERIES, A TERMINATOR (DEC PART NO. 7009938-00) IS INSERTED AT THIS CONN.



TO (M2D NEST

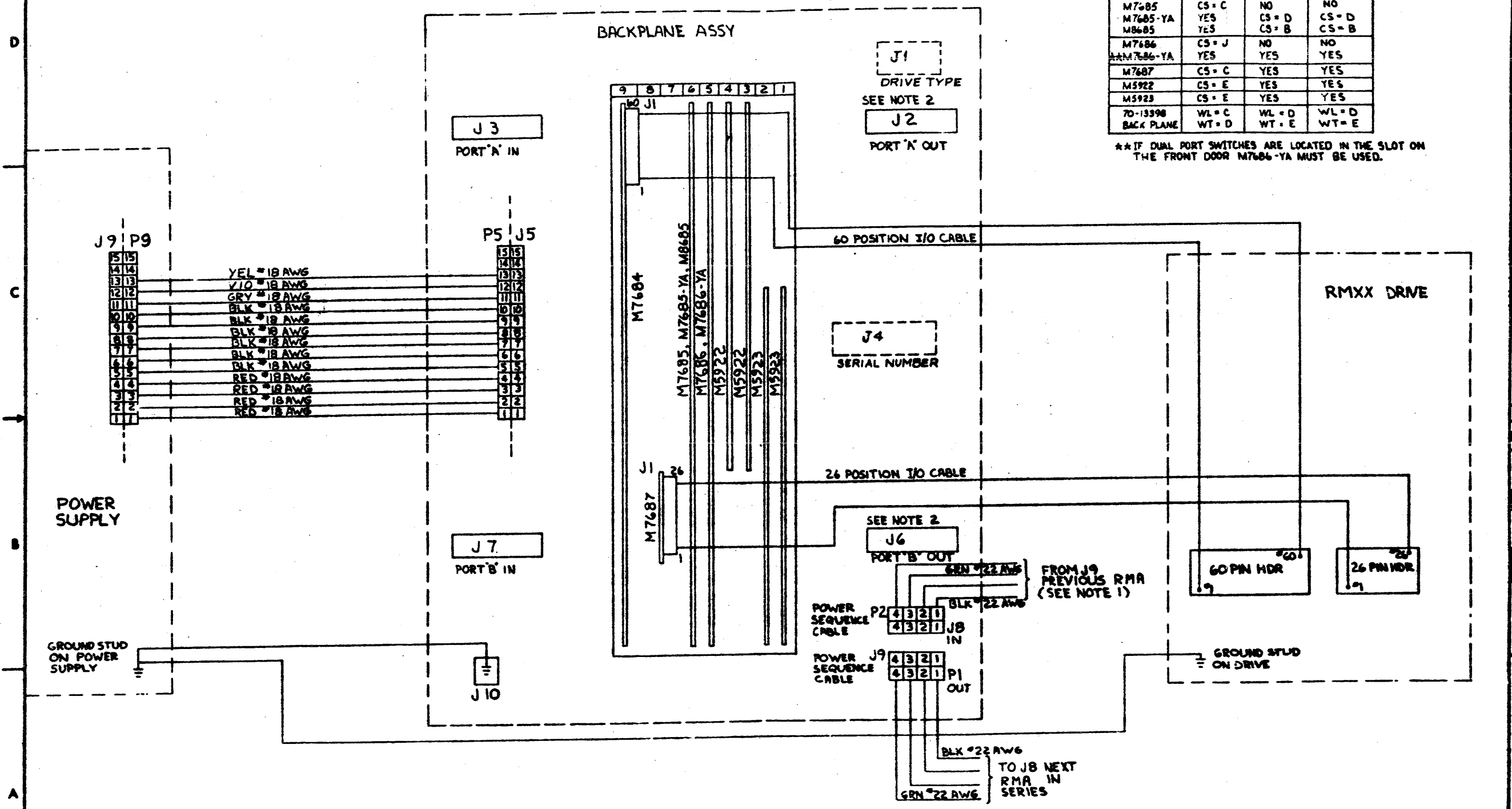
RELEASED TO THE PUBLIC BY THE NATIONAL ARCHIVES AND RECORDS ADMINISTRATION ON 08-12-2014

DATE	2-4-7	DESIGNED BY	20-0000
CHK'D BY		TITLE	RMXX SYSTEM DIAGRAM
ENG. NO.		SCALE	DIC RMXX-0-2
PROJ. ENG.		SHEET	1 OF 2
NEXT DRAWING ASSY		REV.	A
8-TC-2003-0-1			

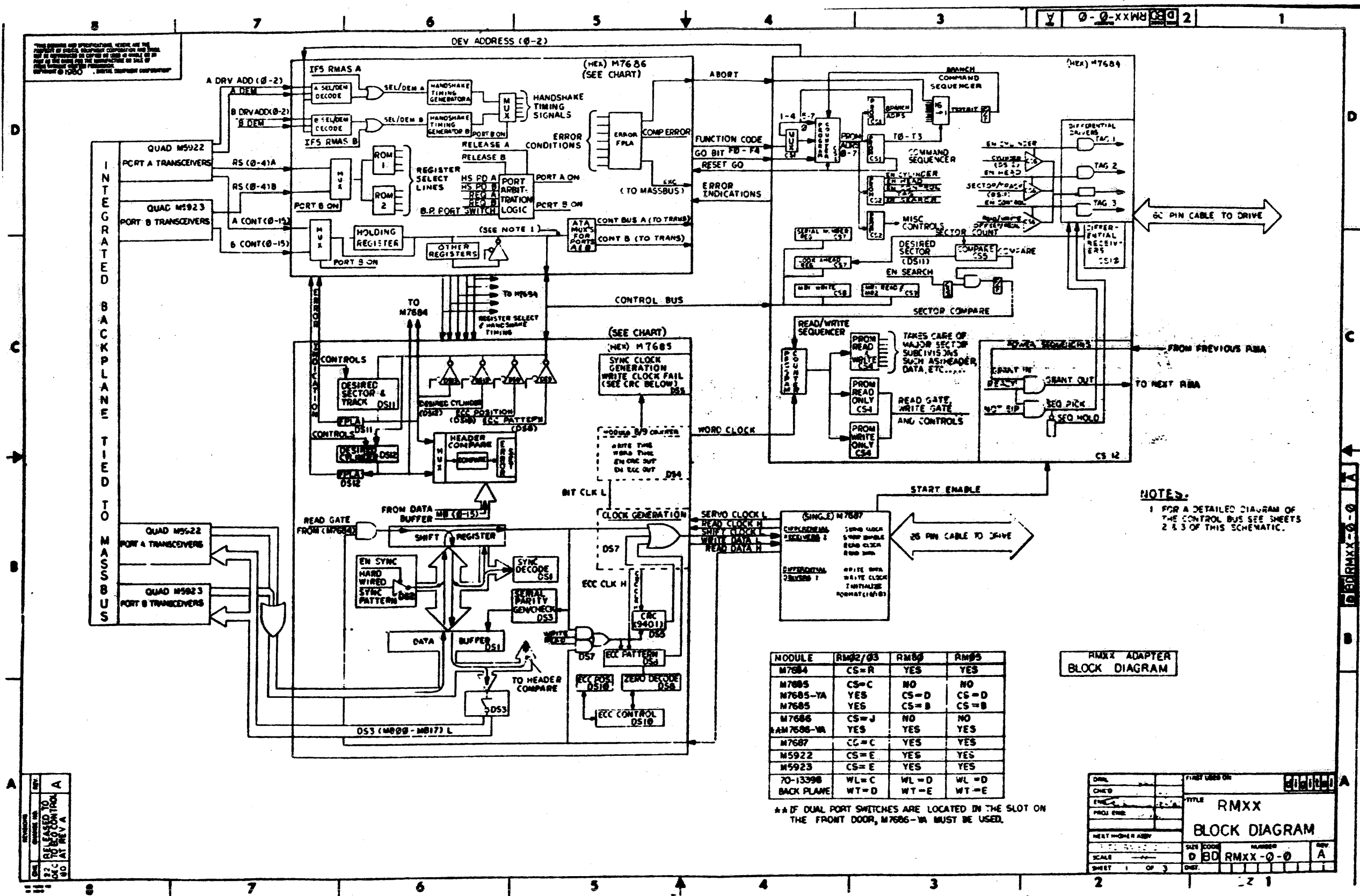
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MODULE	RM82/83	RM86	RM85
M7684	CS = R	YES	YES
M7685	CS = C	NO	NO
M7685-YA	YES	CS = D	CS = D
M8685	YES	CS = B	CS = B
M7686	CS = J	NO	NO
M7686-YA	YES	YES	YES
M7687	CS = C	YES	YES
M5922	CS = E	YES	YES
M5923	CS = E	YES	YES
70-13398	WL = C	WL = D	WL = D
BACK PLANE	WT = D	WT = E	WT = E

** IF DUAL PORT SWITCHES ARE LOCATED IN THE SLOT ON THE FRONT DOOR M7686-YA MUST BE USED.



REVISED		
CHK	CHANGE NO.	REV.



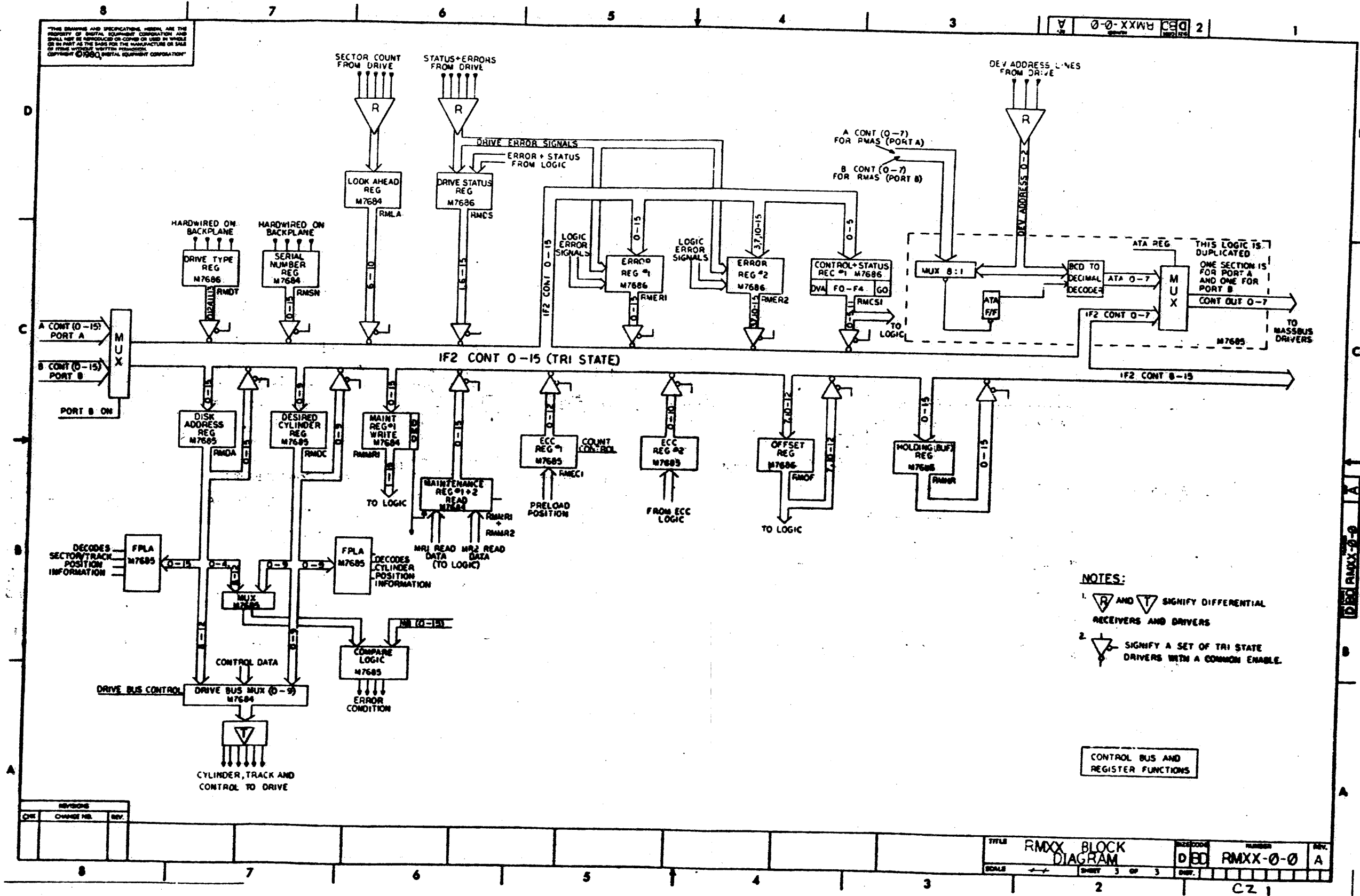
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RELEASING TO CONTROL
 22 DEC 80 AT REV A

DATE	12/22/80	FIRST USED ON	30100000
CHKD		TITLE	RMXX BLOCK DIAGRAM
ENGR		SCALE	D BD RMXX-0-0 A
PROJ ENG		SHEET	1 OF 3
HEAT NUMBER			

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0-0-RMXX-0-0



- NOTES:**
1. ∇ AND ∇ SIGNIFY DIFFERENTIAL RECEIVERS AND DRIVERS
 2. ∇ SIGNIFY A SET OF TRI STATE DRIVERS WITH A COMMON ENABLE.

CONTROL BUS AND REGISTER FUNCTIONS

REVISED		
CHG	CHANGE NO.	REV.

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DIGITAL EQUIPMENT CORPORATION
 0-0-RMXX-0-A 2

INDEX

SHEET

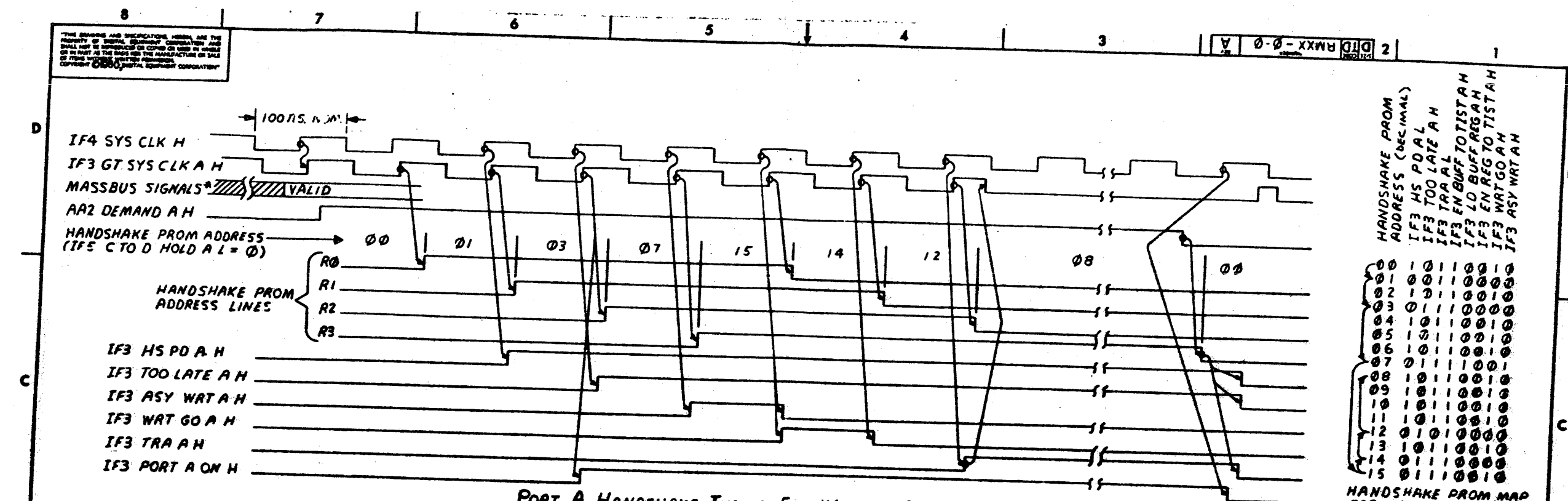
- 1 INDEX
- 2 HANDSHAKE TIMING
- 3 TIMING FOR ALL COMMANDS (COMMAND SEQUENCER), (SH 1 OF 3)
- 4 TIMING FOR ALL COMMANDS (COMMAND SEQUENCER), (SH 2 OF 3)
- 5 TIMING FOR ALL COMMANDS (COMMAND SEQUENCER/DATA), (SH 3 OF 3)
- 6 START OF DATA COMMAND ON SECTOR X+1 FOLLOWING DATA COMMAND ON SECTOR X
- 7 BASIC DATA TIMING (CLOCK GENERATION)
- 8 DATA TIMING FOR WRITE HEADER AND DATA (FORMAT)
- 9 READ HEADER AND DATA AND READ DATA TIMING
- 10 TIMING FOR WRITE DATA OPERATION
- 11 TIMING FOR ECC DURING READ
- 12 TIMING FOR ECC CORRECTION

INDEX

RELEASABLE TO
 CONTROL
 AT REV A

DATE	BY	FIRST USED ON
CHK'D		
ENGR		TITLE
PROJ. ENGR		INTERFACE
		TIMING DIAGRAM
NEXT NUMBER A/W		
3-TC-2433-3-1	REV CODE	NUMBER
	DIG	0-0-RMXX-0-0
SHEET 1	OF 12	REV. A
		CZ 1

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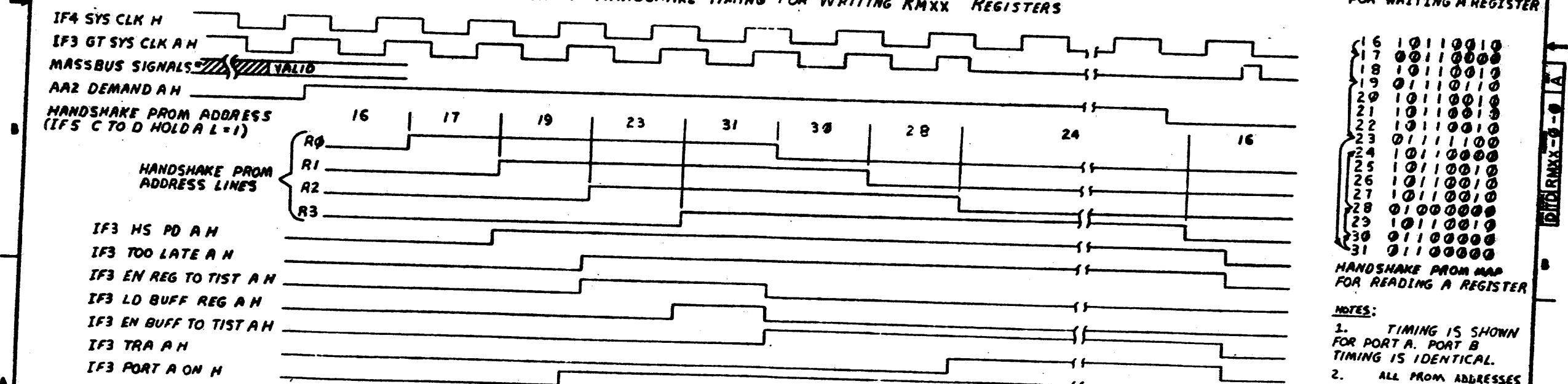
PORT A HANDSHAKE TIMING FOR WRITING RMX REGISTERS

HANDSHAKE FROM ADDRESS (DECIMAL)

IF3 HS PD A H	00	01	03	07	15	14	12	08	00
IF3 TOO LATE A H	00	00	00	00	00	00	00	00	00
IF3 ASY WRT A H	00	00	00	00	00	00	00	00	00
IF3 WRT GO A H	00	00	00	00	00	00	00	00	00
IF3 TRA A H	00	00	00	00	00	00	00	00	00
IF3 PORT A ON A H	00	00	00	00	00	00	00	00	00

HANDSHAKE FROM MAP FOR WRITING A REGISTER

16	10	11	00	10
17	00	11	00	00
18	10	11	00	00
19	01	11	00	10
20	10	11	00	10
21	10	11	00	10
22	10	11	00	10
23	01	11	10	00
24	10	11	00	00
25	10	11	00	10
26	10	11	00	10
27	10	11	00	10
28	01	00	00	00
29	10	11	00	10
30	01	10	00	00
31	01	10	00	00



PORT A HANDSHAKE TIMING FOR READING RMX REGISTERS

HANDSHAKE FROM MAP FOR READING A REGISTER

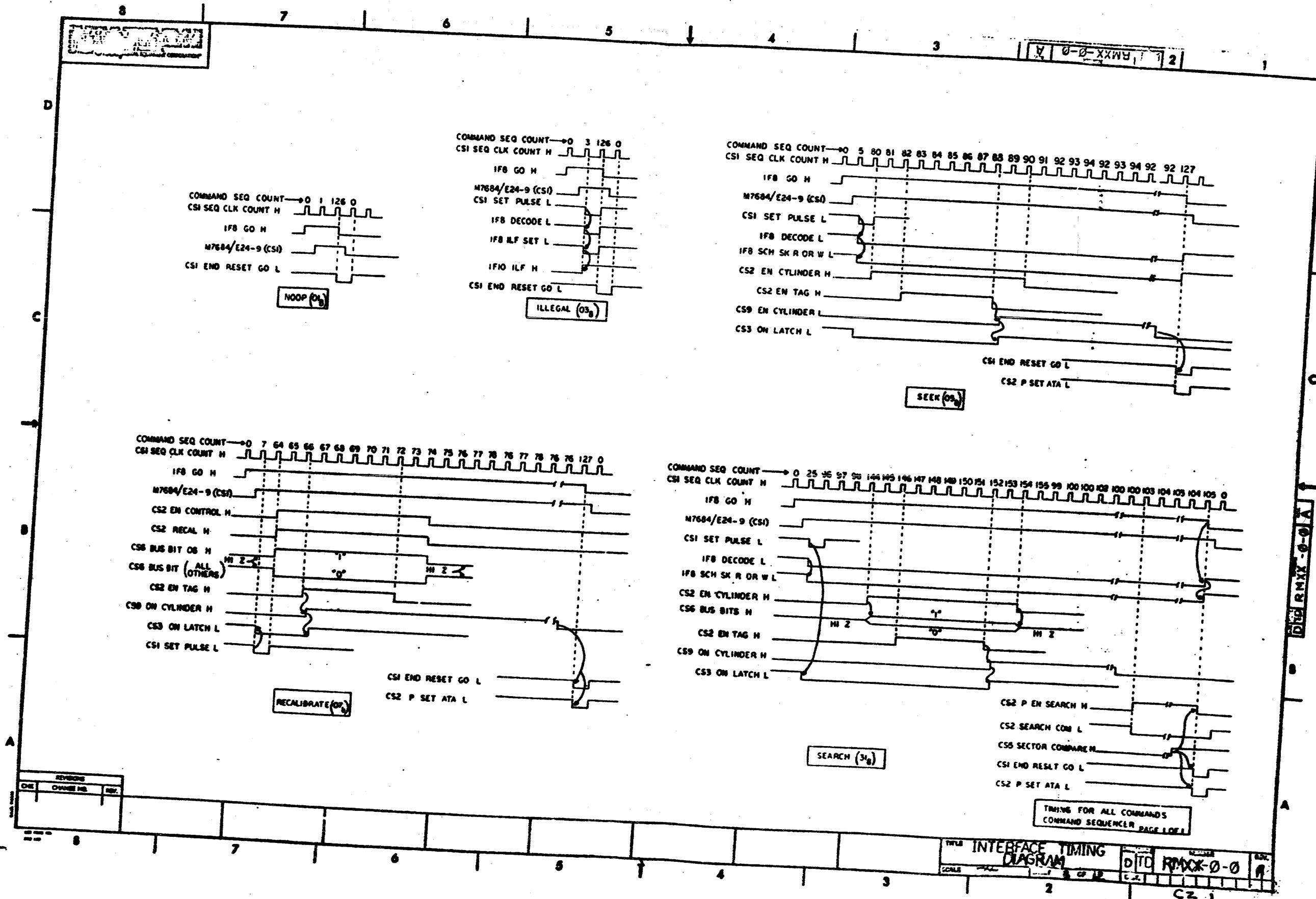
16	10	11	00	10
17	00	11	00	00
18	10	11	00	00
19	01	11	00	10
20	10	11	00	10
21	10	11	00	10
22	10	11	00	10
23	01	11	10	00
24	10	11	00	00
25	10	11	00	10
26	10	11	00	10
27	10	11	00	10
28	01	00	00	00
29	10	11	00	10
30	01	10	00	00
31	01	10	00	00

NOTES:
 1. TIMING IS SHOWN FOR PORT A. PORT B TIMING IS IDENTICAL.
 2. ALL FROM ADDRESSES NOT SHOWN ABOVE ARE UNUSED.

*MASSBUS SIGNALS REPRESENTED ARE AA1 ACONT 00-15, AA2 C TO D A H, AA2 DRV ADR 1H-2H-4H, AND AA2 REG SEL 1H-2H-4H-8H-16H.

TIME DURING WHICH CONTROLLER SEES ASSERTION OF TRANSFER (IF3 TRA A H) AND NEGATES DEMAND (AA2 DEMAND A H).

HANDSHAKE TIMING SHEET 1 OF 1

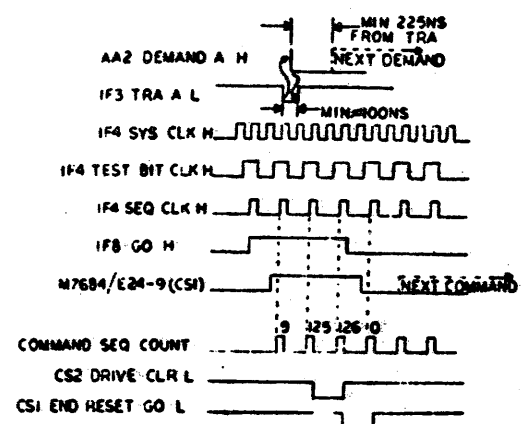


REVISIONS		
CHK	CHANGE NO.	REV.

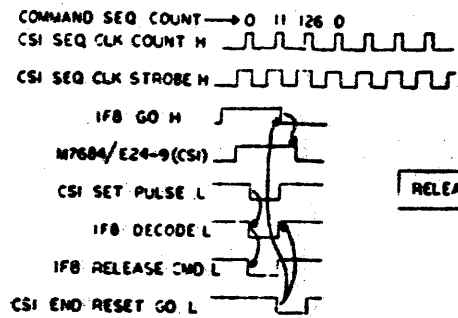
TRYING FOR ALL COMMANDS
 COMMAND SEQUENCER PAGE 1 OF 1

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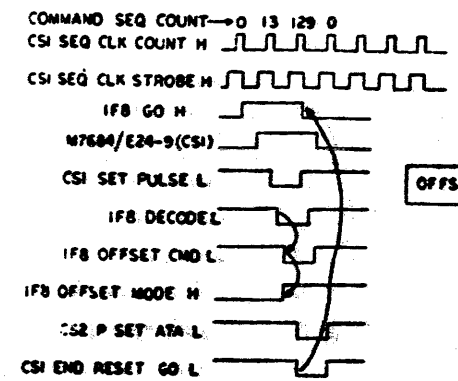
D
C
B
A



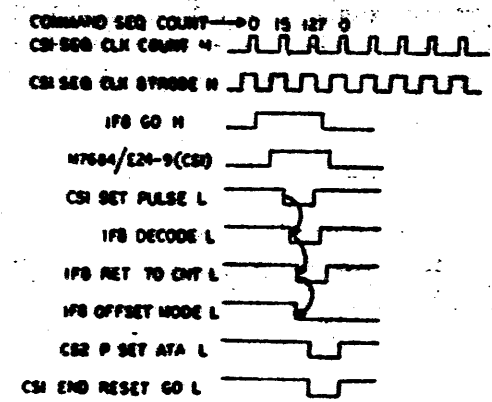
DRIVE CLEAR (11)



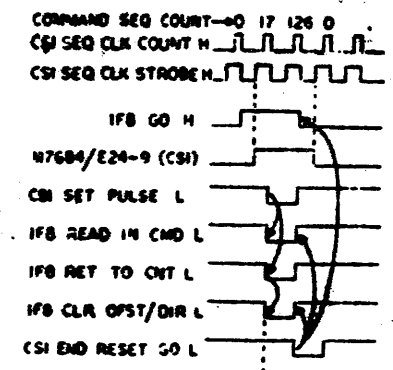
RELEASE (13)



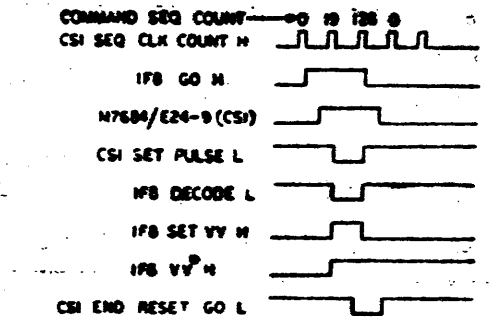
OFFSET (19)



RETURN TO CENTERLINE (17)



READ IN PRESET (21)



PACK ACKNOWLEDGE (23)

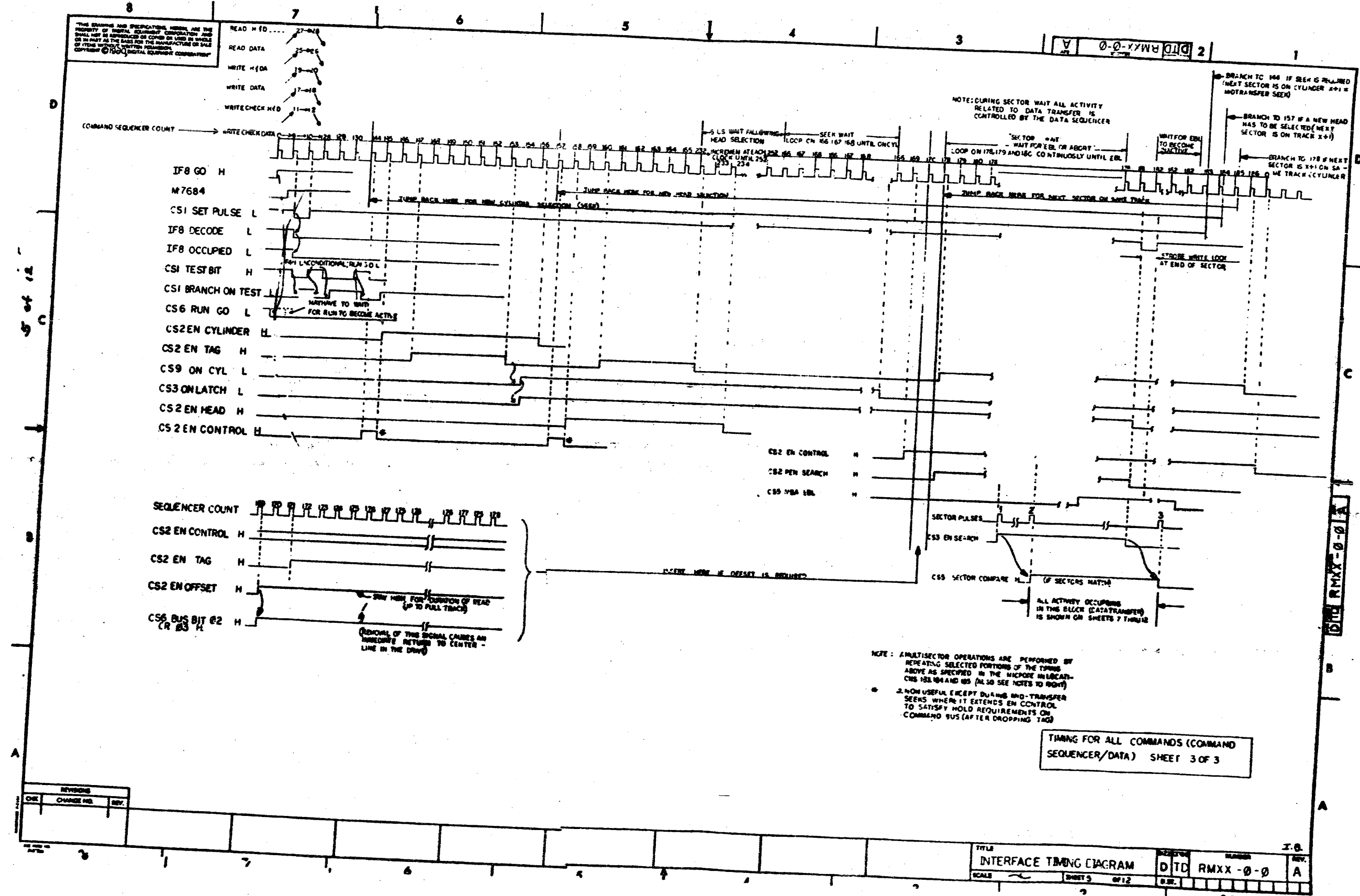
* FOR WHICHEVER PORT IS ON

FOR WHICHEVER PORT IS ON

TIMING FOR ALL COMMANDS
COMMAND SEQUENCER PAGE 1 OF 1

REV	CHG	CHG	REV

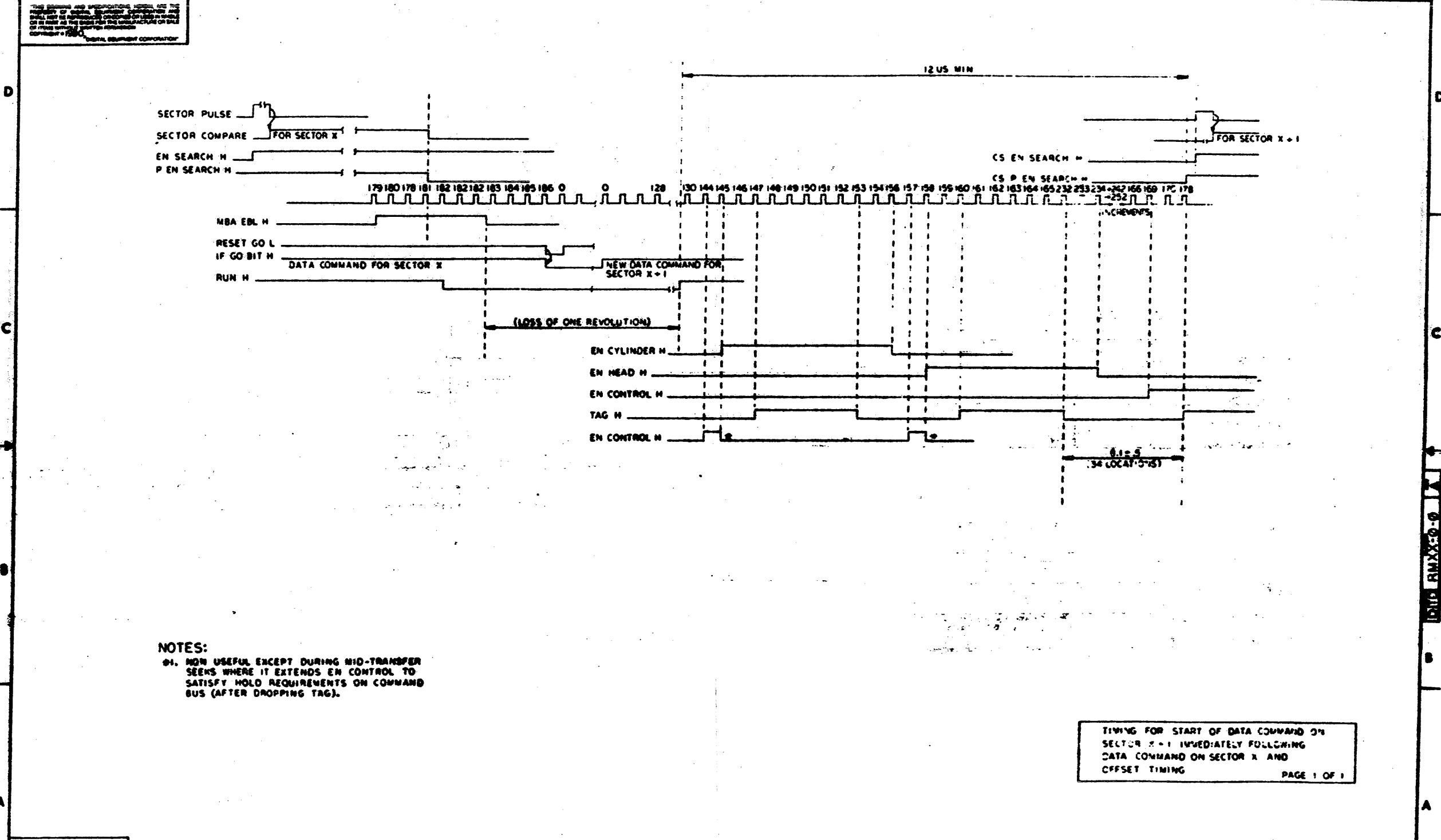
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NOTE: 1. MULTISECTOR OPERATIONS ARE PERFORMED BY REPEATING SELECTED PORTIONS OF THE TIMING ABOVE AS SPECIFIED IN THE MICROSECTOR INDICATORS 184 AND 185 (ALSO SEE NOTES TO RIGHT)
 2. NON-USEFUL EMPTY DURING AND-TRANSFER SEEKS WHERE IT EXTENDS EN CONTROL TO SATISFY HOLD REQUIREMENTS ON COMMAND BUS (AFTER DROPPING TAG)

TIMING FOR ALL COMMANDS (COMMAND SEQUENCER/DATA) SHEET 3 OF 3

REV.	CHANGED BY	REV.

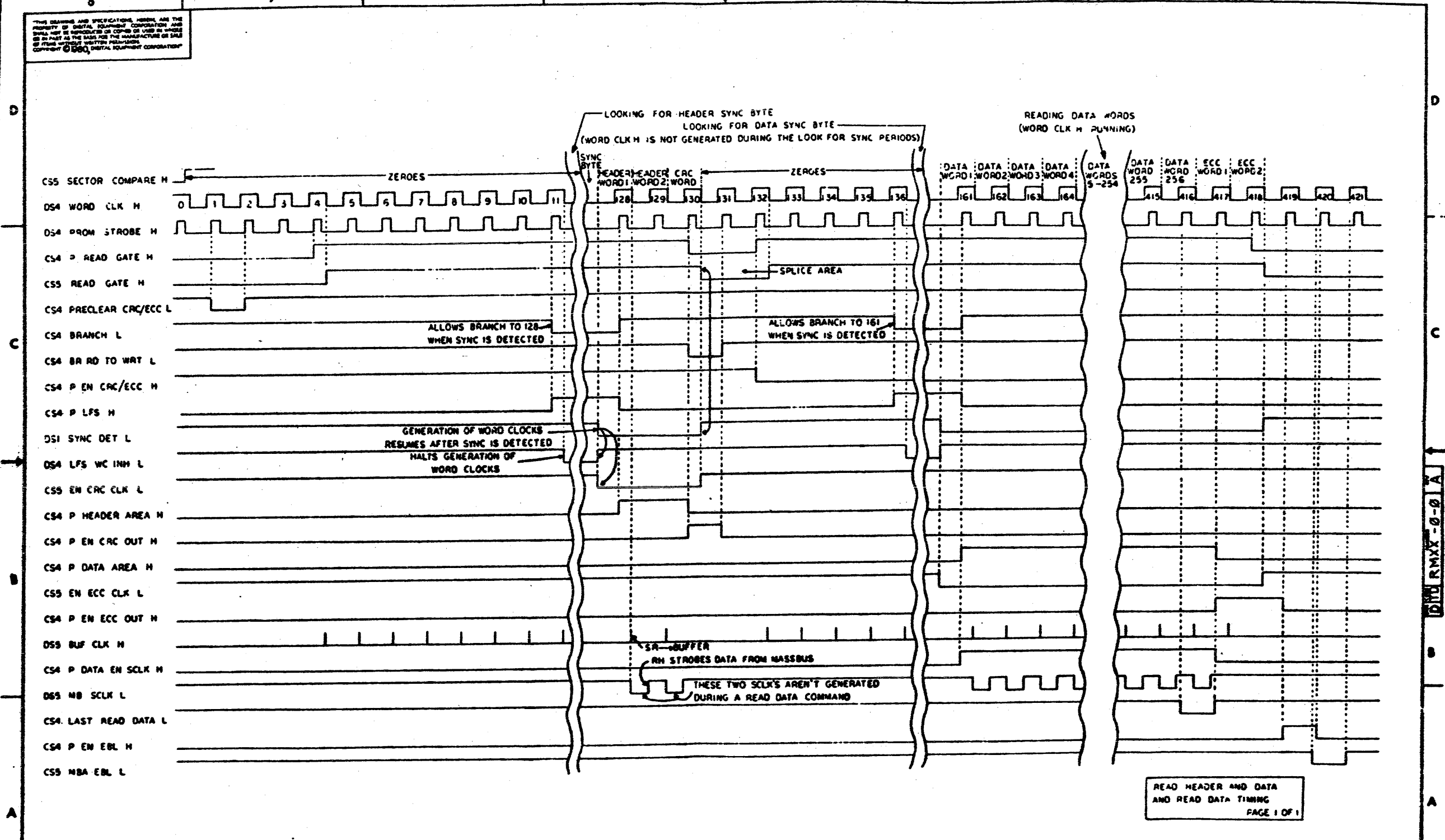


NOTES:
 ①. NOT USEFUL EXCEPT DURING MID-TRANSFER SEKS WHERE IT EXTENDS EN CONTROL TO SATISFY HOLD REQUIREMENTS ON COMMAND BUS (AFTER DROPPING TAG).

TIMING FOR START OF DATA COMMAND ON SECTOR X+1 IMMEDIATELY FOLLOWING DATA COMMAND ON SECTOR X AND OFFSET TIMING
 PAGE 1 OF 1

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0-0-RMXX-0-0 2

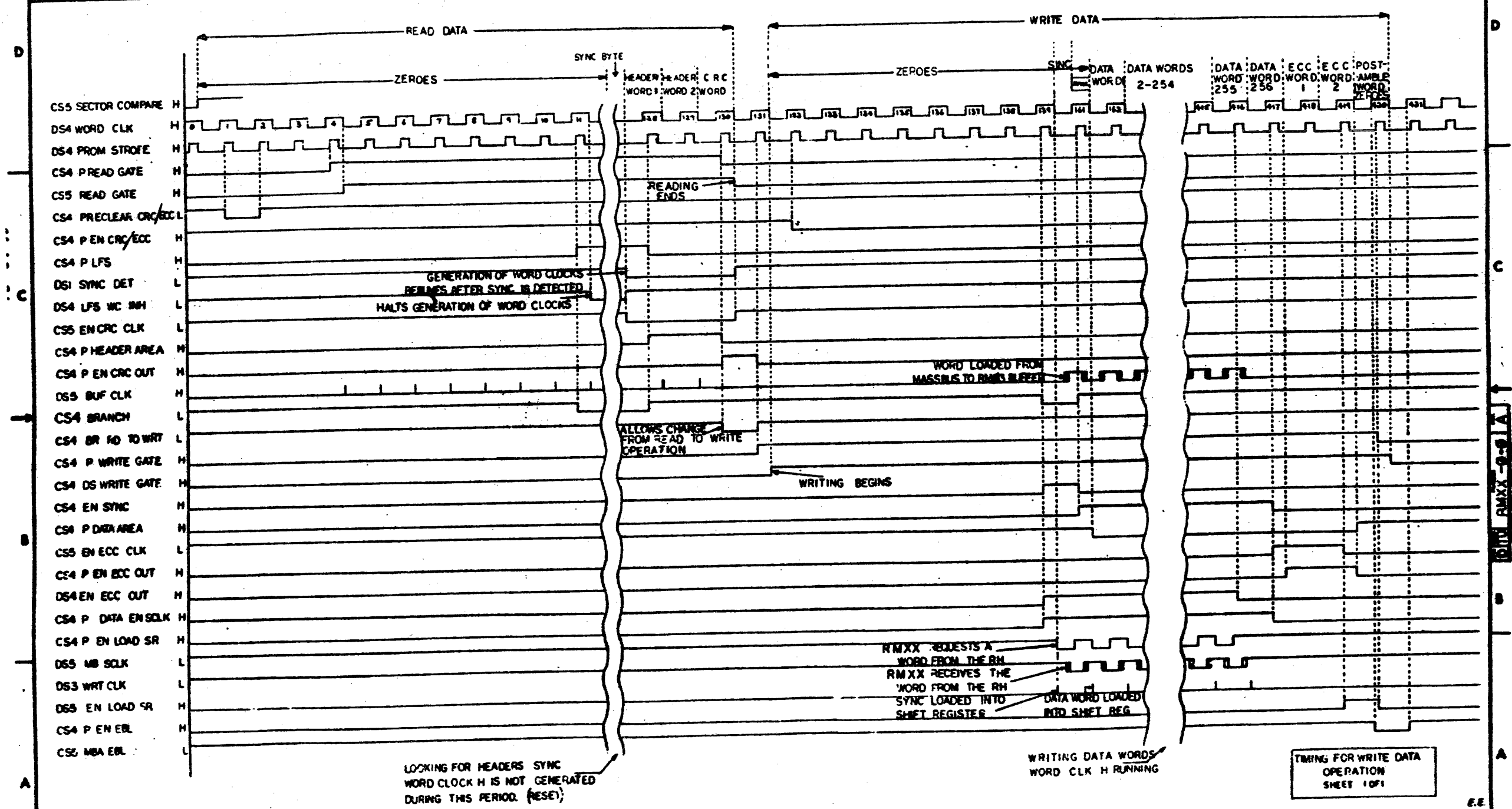


READ HEADER AND DATA AND READ DATA TIMING
 PAGE 1 OF 1

REVISIONS		
CHG	CHANGE NO.	REV.

TITLE	INTERFACE TIMING DIAGRAM	REV. 0	NO. 1	REV. A
SCALE	1:1	SHEET	9 OF 12	DATE
				1 CZ

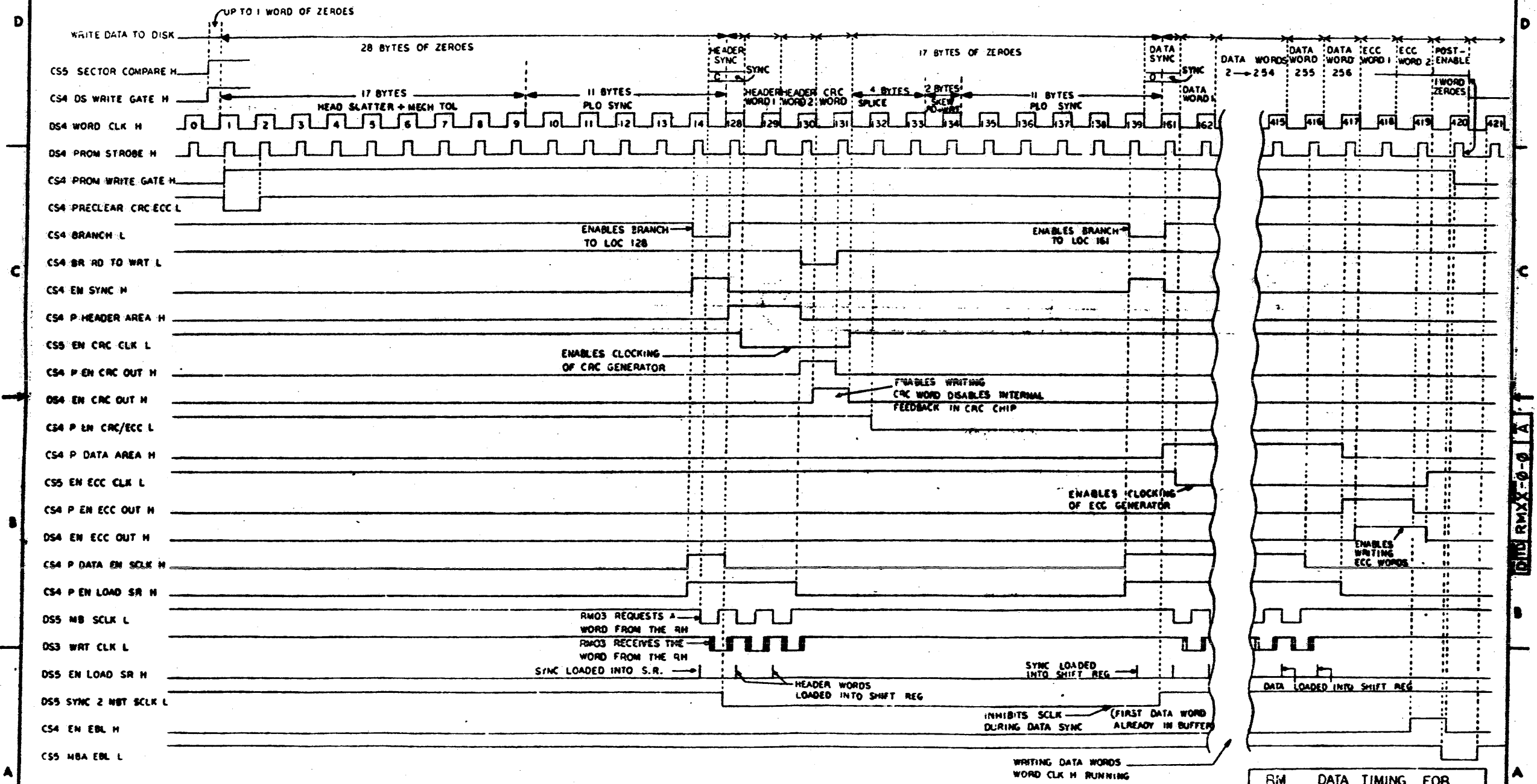
THE SIGNALS ARE DEFINED HERE, AS THE PROPERTY OF DATA. CONTROL SIGNALS ARE NOT IN THIS SECTION. SEE SECTION 0-0-RMXX-0-0 FOR THE DEFINITION OF THE SIGNALS.



REV	DATE	BY	CHKD	APP'D	TITLE	SCALE	SHEET NO	OF	TOTAL	REV	DATE	BY	CHKD	APP'D
					INTERFACE TIMING DIAGRAM									

E.E. RMXX-0-0 A

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RM DATA TIMING FOR WRITE HEADER & DATA

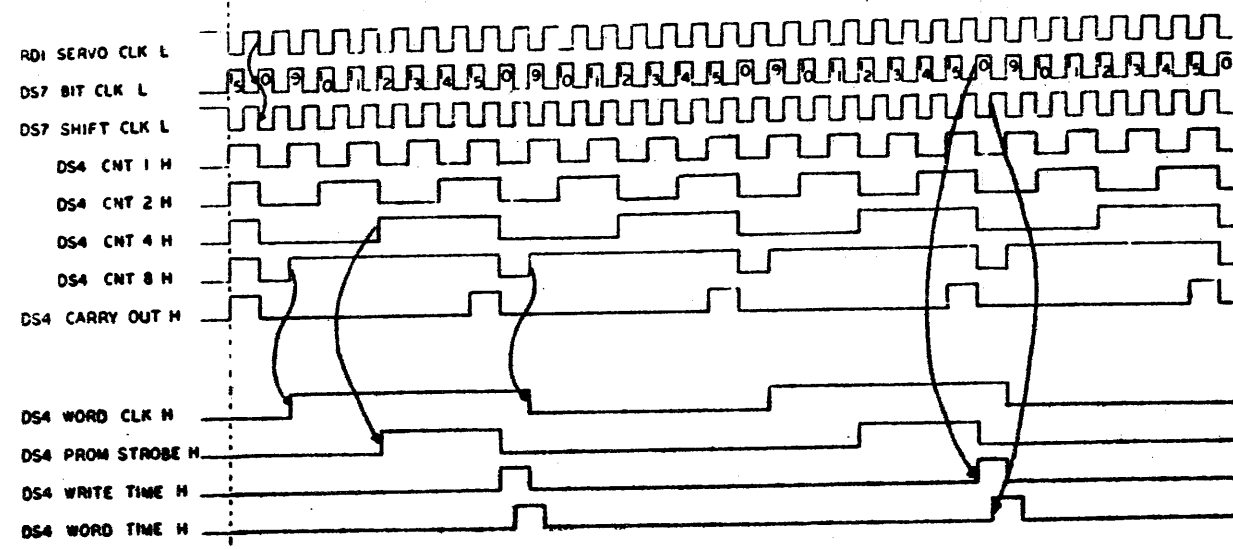
REVISIONS		
CHR	CHANGE NO.	REV.

TITLE	INTERFACE TIMING DIAGRAM	D/E	NUMBER	RMXA-0-0 A	REV.	
SCALE		SHEET	5 OF 12	DATE		

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DIGITAL EQUIPMENT CORPORATION
 DTD RMXX-0-0 2

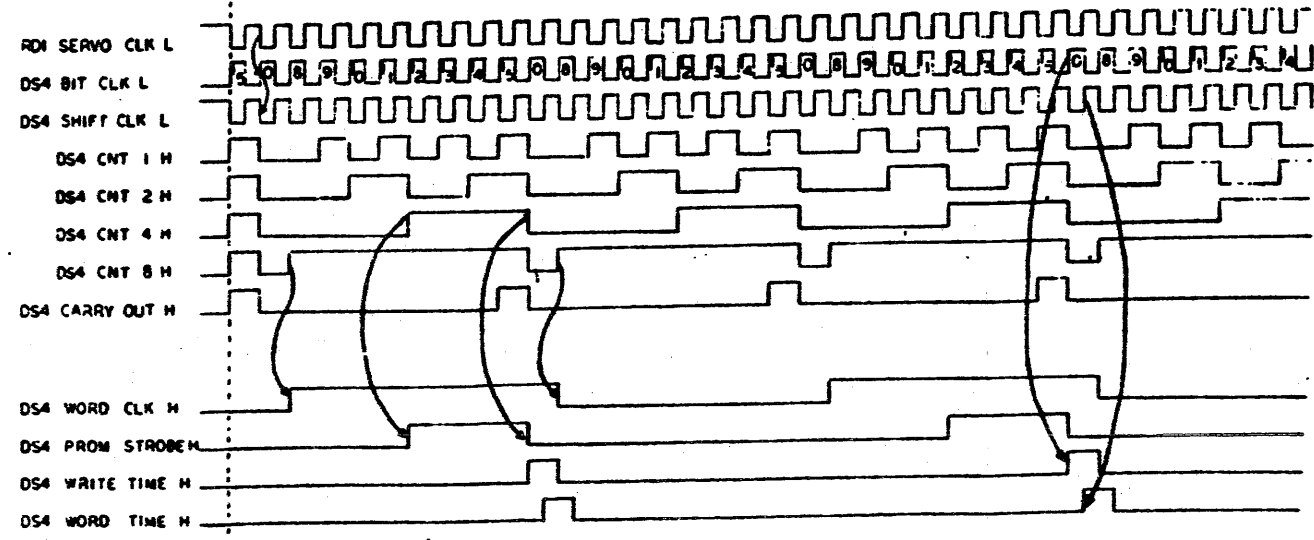
16 BIT MODE



NOTE

1. THESE TIMING DIAGRAMS DESCRIBE THE BASIC DATA TIMING GENERATED ON THE M7685 MODULE (SHEET DS4).
2. THE NUMBER ENCLOSED UNDER THE LEADING EDGE OF THE SIGNAL DS7 BIT CLOCK L (9) INDICATES THE COUNT VALUE OF EG PRODUCED BY THE LEADING EDGE OF THAT PULSE.

18 BIT MODE

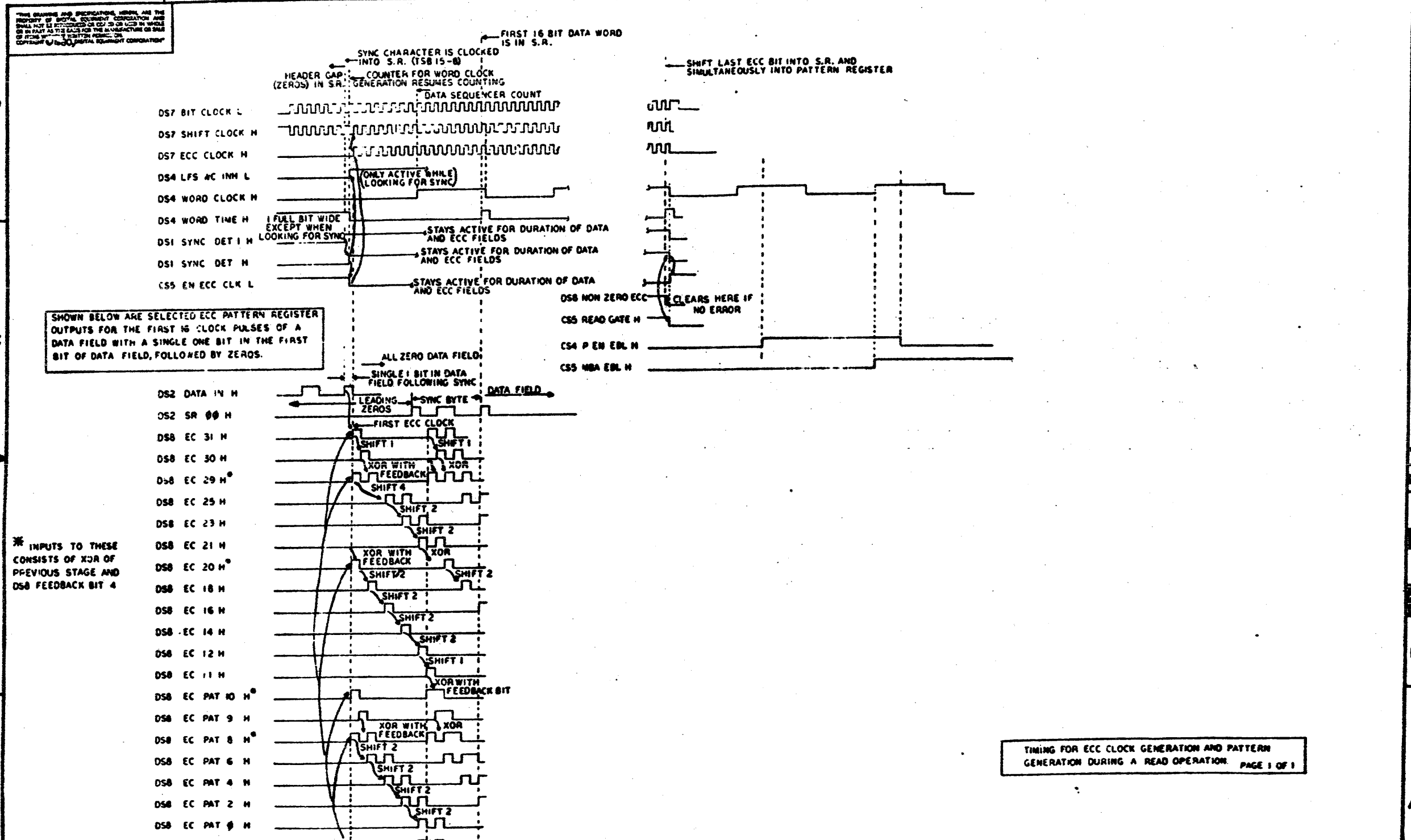


BASIC DATA TIMING
 (CLOCK GENERATION)
 PAGE 1 OF 1

REVISIONS		
ONE	CHANGE NO.	REV.

TITLE	INTERFACE TIMING DIAGRAM	DESIGN	DTD	NUMBER	RMXX-0-0	REV.	A
SCALE		SHEET	7	OF	12	DATE	

DIGITAL EQUIPMENT CORPORATION
 DTD RMXX-0-0 A



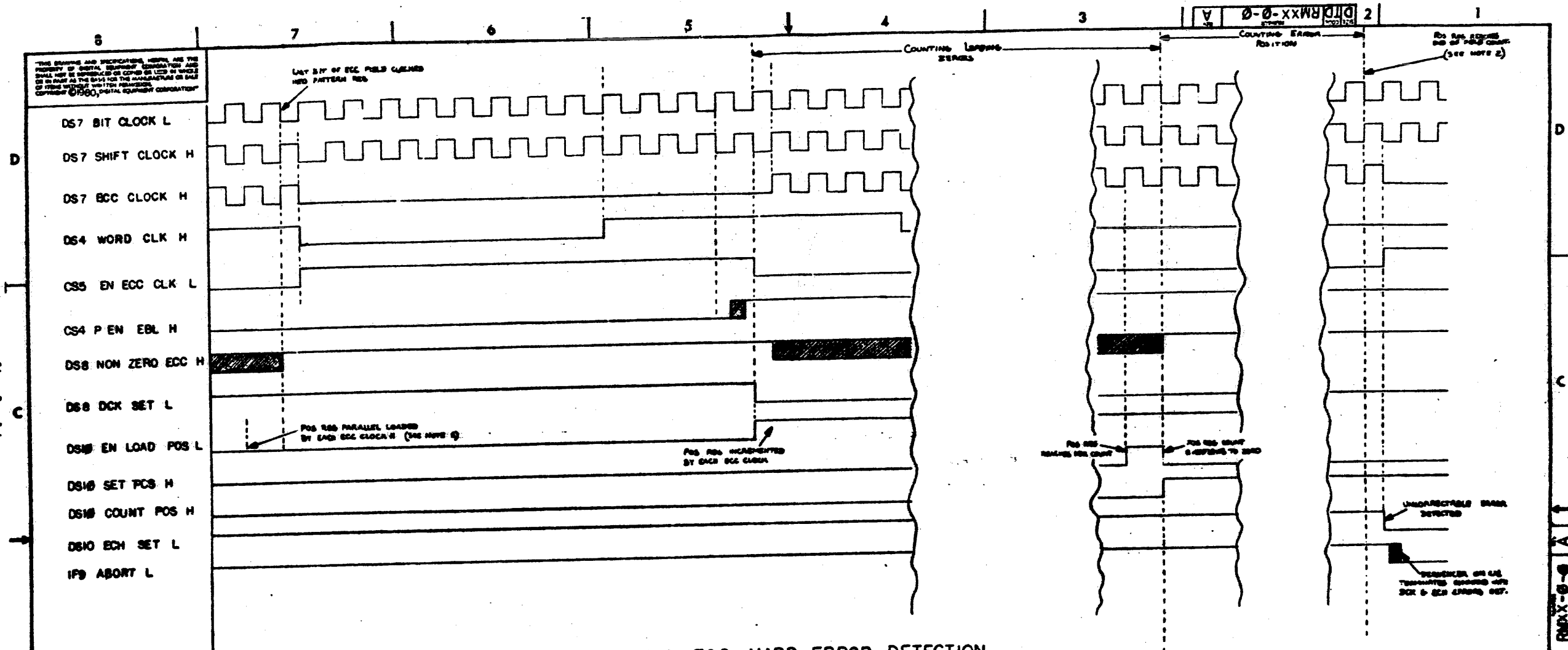
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SHOWN BELOW ARE SELECTED ECC PATTERN REGISTER OUTPUTS FOR THE FIRST 16 CLOCK PULSES OF A DATA FIELD WITH A SINGLE ONE BIT IN THE FIRST BIT OF DATA FIELD, FOLLOWED BY ZEROS.

* INPUTS TO THESE CONSISTS OF XOR OF PREVIOUS STAGE AND DS8 FEEDBACK BIT 4

TIMING FOR ECC CLOCK GENERATION AND PATTERN GENERATION DURING A READ OPERATION. PAGE 1 OF 1

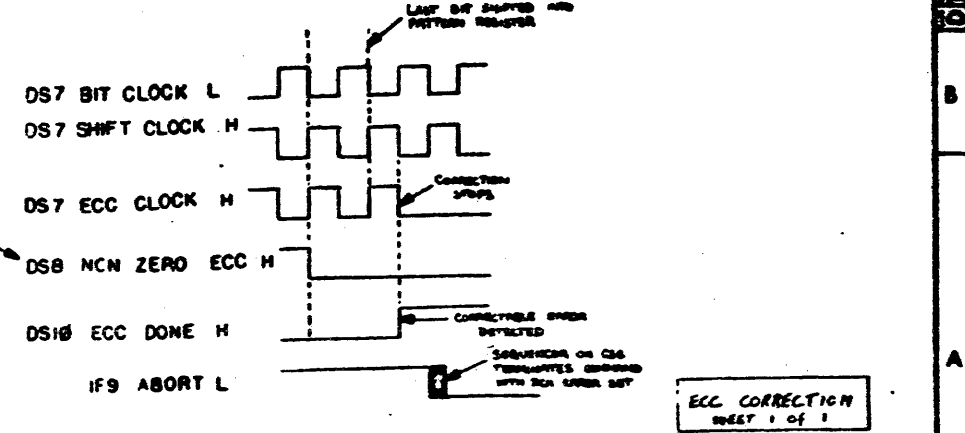
REVISED		
CHK	CHANGE NO.	REV.



TIMING FOR ECC HARD ERROR DETECTION

- NOTES: (1) THE POSITION REGISTER IS PARALLEL RELOADED TO:
- (a) 640000 for 10 BIT MODE.
 - (b) 650000 for 15 BIT MODE.
- (2) THE END OF POS COUNT FOR THE POSITION REGISTER IS:
- (a) 100400 for 10 BIT MODE.
 - (b) 110400 for 15 BIT MODE.

ASSUMING A ZERO ECC CORRECTIBLE ERROR REPEAT IS DETECTED DURING THE CORRECTIBLE PERIOD TERMINATION IS AS SHOWN HERE.



REVISED		
DATE	CHANGE NO.	BY

TITLE		INTERFACE		DRAWN		NUMBER		REV.	
TIMING DIAGRAM		DTD		RMXX-0-0		A			
SCALE		SHEET 12		OF 12		DATE			
								ICZ	

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0-0-RMXX-0-0 2 A

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- PAGE 1 INDEX
- 2. CONTROL SEQUENCER (RESET CONDITION AND BRANCH CODES)
- 3. CONTROL SEQUENCER FLOW (PAGE 1-START)
- 4. CONTROL SEQUENCER FLOW (PAGE 2-ERROR START)
- 5. CONTROL SEQUENCER FLOW (PAGE 3-RECALIBRATE-SEEK-DRIVE CLEAR)
- 6. CONTROL SEQUENCER FLOW (PAGE 4-SEARCH)
- 7. CONTROL SEQUENCER FLOW (PAGE 5-DATA COMMANDS)
- 8. CONTROL SEQUENCER FLOW (PAGE 6-DATA OFFSET-DATA OPI-ABORT EBL)
- 9. CONTROL SEQUENCER ROM LISTINGS (PAGE 1)
- 10. CONTROL SEQUENCER ROM LISTINGS (PAGE 2)
- 11. DATA SEQUENCER ROM LISTINGS

RELEASING TO
SECURITY CONTROL
30 JAN 1964

INDEX
PAGE 1 OF 1

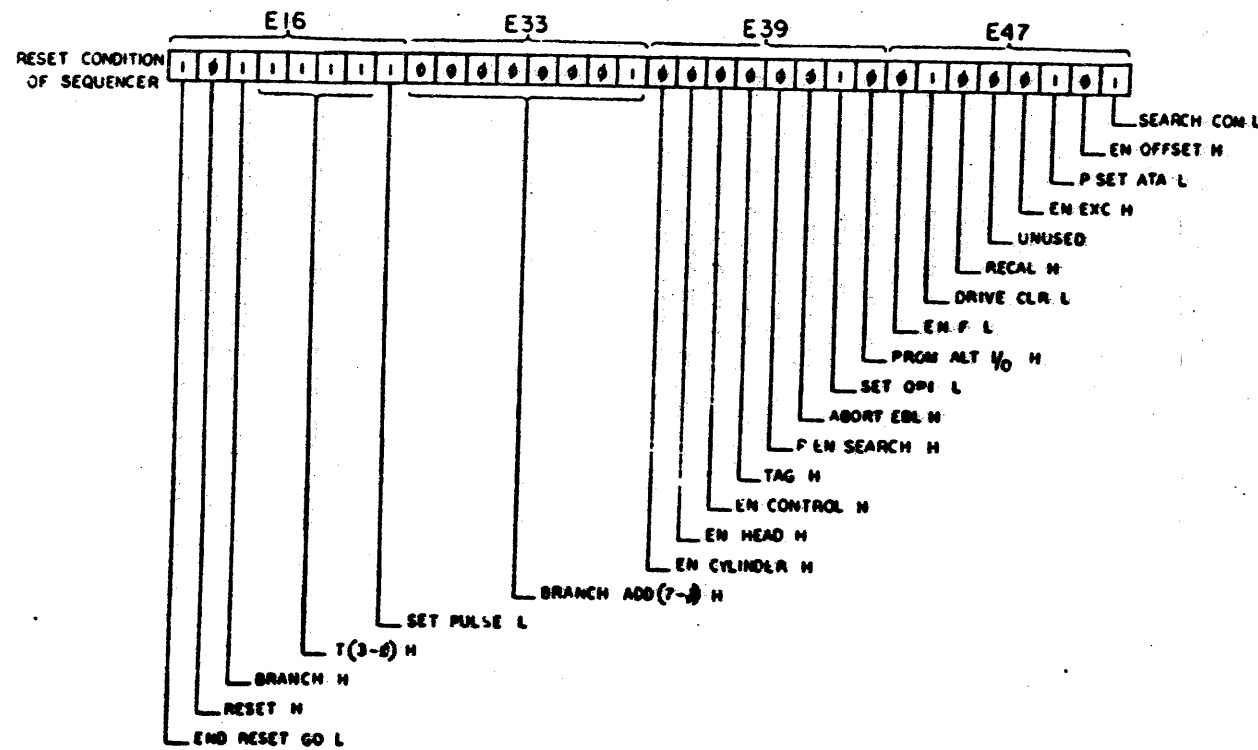
DRILL	DATE	REVISED BY	DATE
CHIEF	1/15/64	DATE	
ENGINEER	1/15/64	DATE	
PROJ. ENG.	1/15/64	DATE	
NEXT HIGHER AUTH.			
B-TC-RM88-0-1		SCALE	1/1
SHEET 1 OF 13		NO.	2
		REV.	A
		CZ	1

RMXX FLOW DIAGRAMS

0-0-RMXX-0-0 2 A

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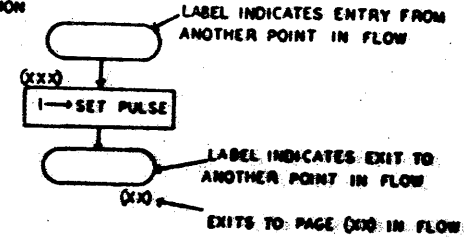
RMXX-0-0



TEST CODE	BRANCH CONDITION
00	OCCUPIED L
01	ON CYLINDER L
02	ABORT L
03	UNIT READY L
04	NOT USED
05	F 4 L
06	ON LATCH L
07	RUN AND GO L
10	NBA EBL L
11	SECTOR COMPARE L
12	OFFSET MODE L
13	DATA COMMAND L
14	SEEK REQUIRED L
15	NEW TRACK REQUIRED L
16	CONTINUE L
17	UNCONDITIONAL BRANCH

NOTE:

- (XXX) IS ROW LOCATION IN OCTAL OF THIS MICROWORD
- EXAMPLE 1-SET PULSE, MEANS ASSERT THE SIGNAL "SET PULSE L"
- PROM 1/0 IS NOT SHOWN IN FLOWS. ALL OTHER SIGNALS ASSERTED DURING ANY MICROCODE WORD ARE SHOWN IN THE FLOW FOR THAT WORD.
- ALL ADDRESSES, BRANCH ADDRESSES AND TEST CODES SHOWN IN THE FLOWS ARE IN OCTAL.

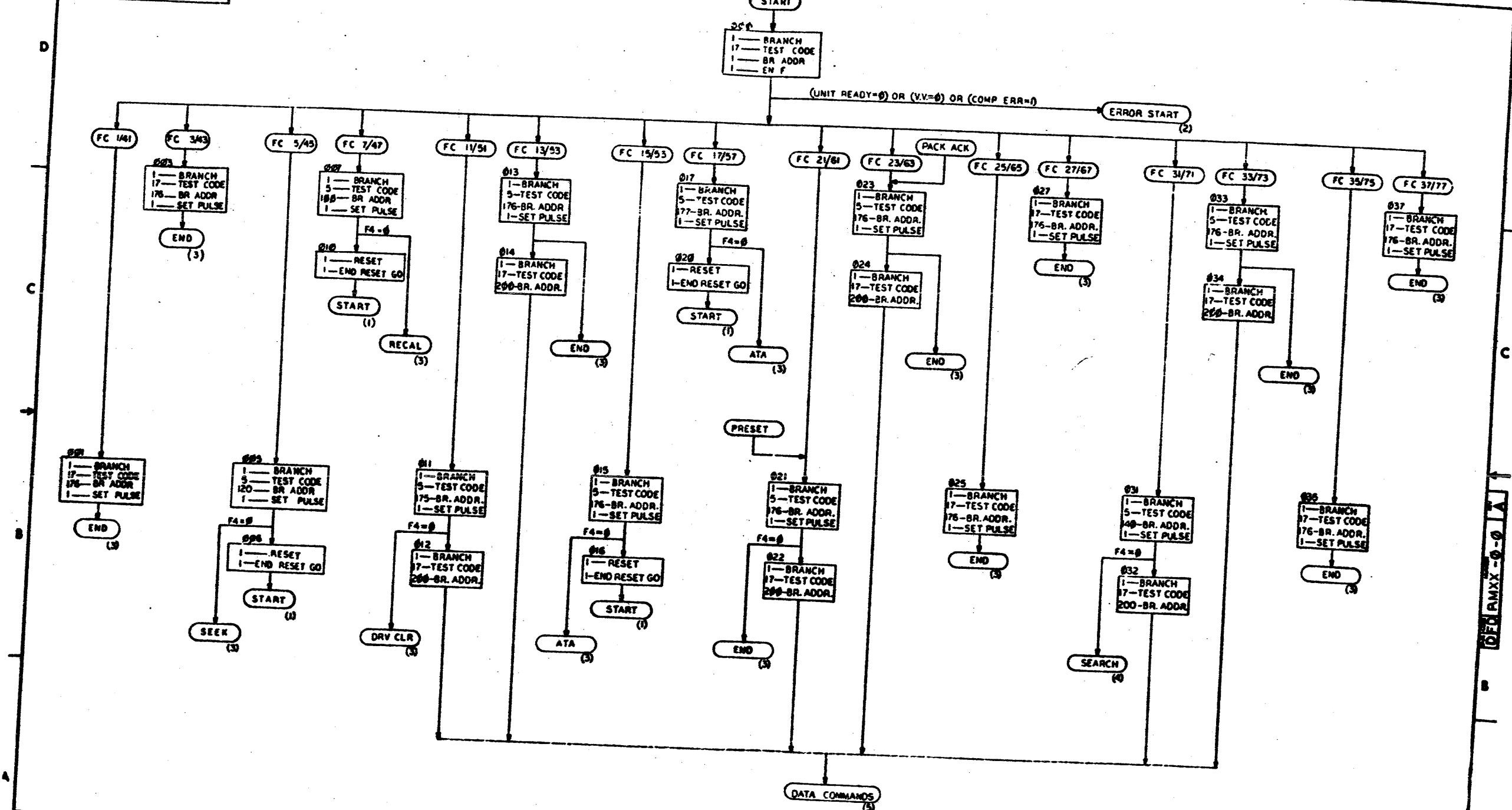


CONTROL SEQUENCER
RESET CONDITION AND
BRANCH CODES PAGE 1 OF 1

REVISED		
DATE	CHANGE NO.	REV.

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0-0-RMXX-0-0 2

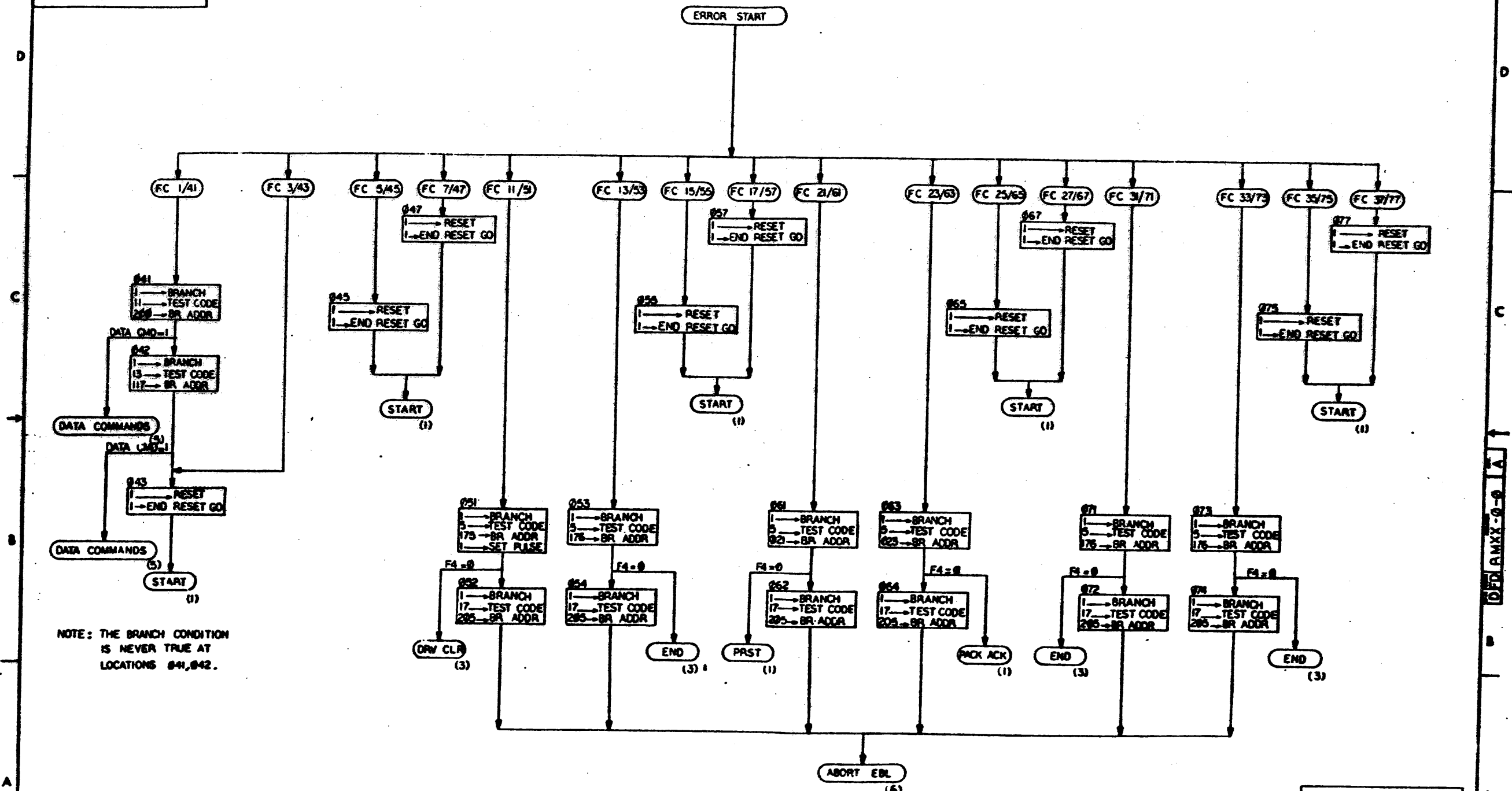


REV	CHG	DATE	BY

CONTROL SEQUENCER FLOW
START PAGE 1 OF 6

TITLE RMXX FLOW DIAGRAMS
SCALE 1/1
SHEET 3 OF 13
REV. A
CZ 1

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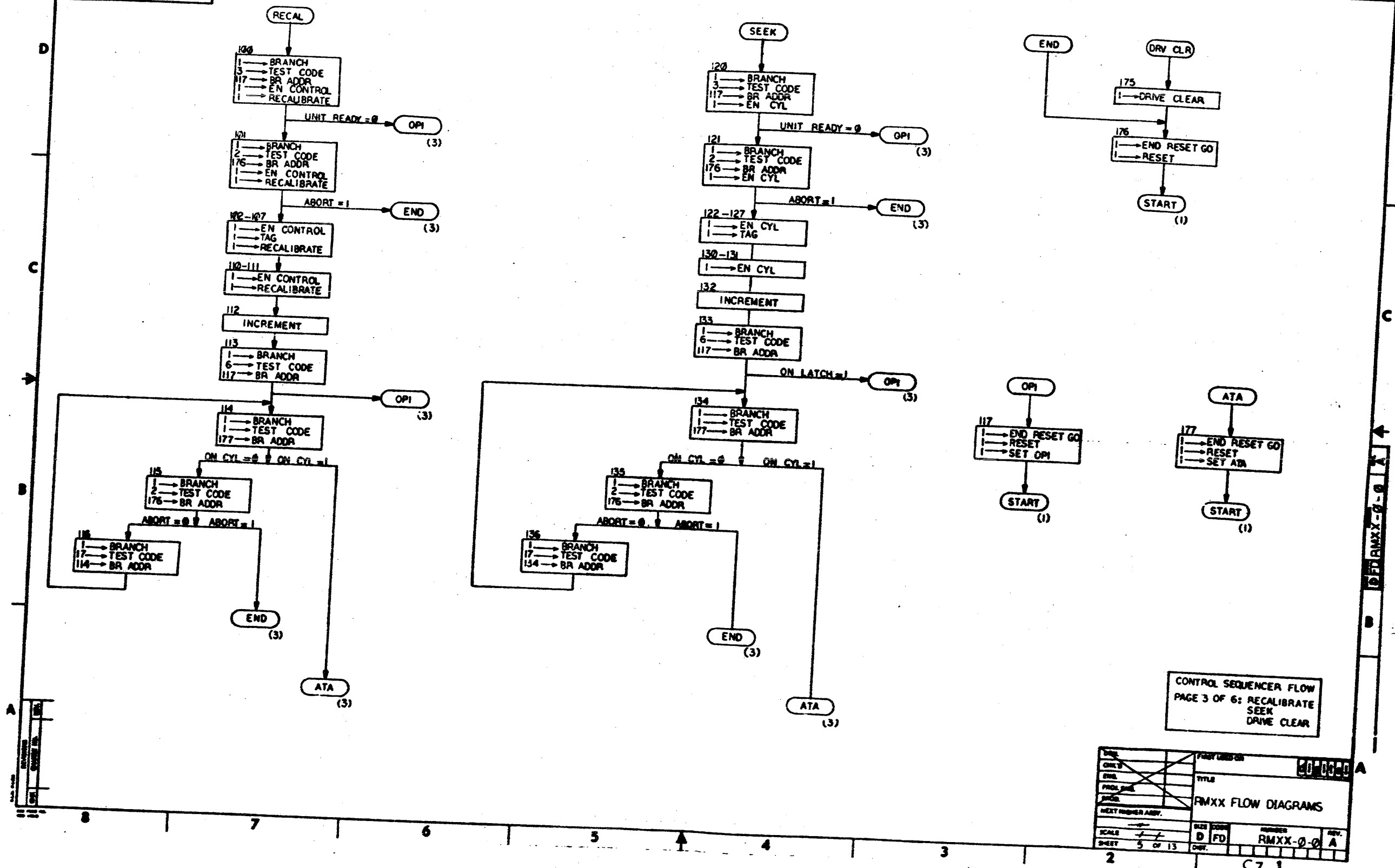
NOTE: THE BRANCH CONDITION IS NEVER TRUE AT LOCATIONS #41, #42.

CONTROL SEQUENCER FLOW
PAGE 2 OF 6: ERROR START

REV	CHG	DATE	BY

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0-0-RMXX-0-0 2

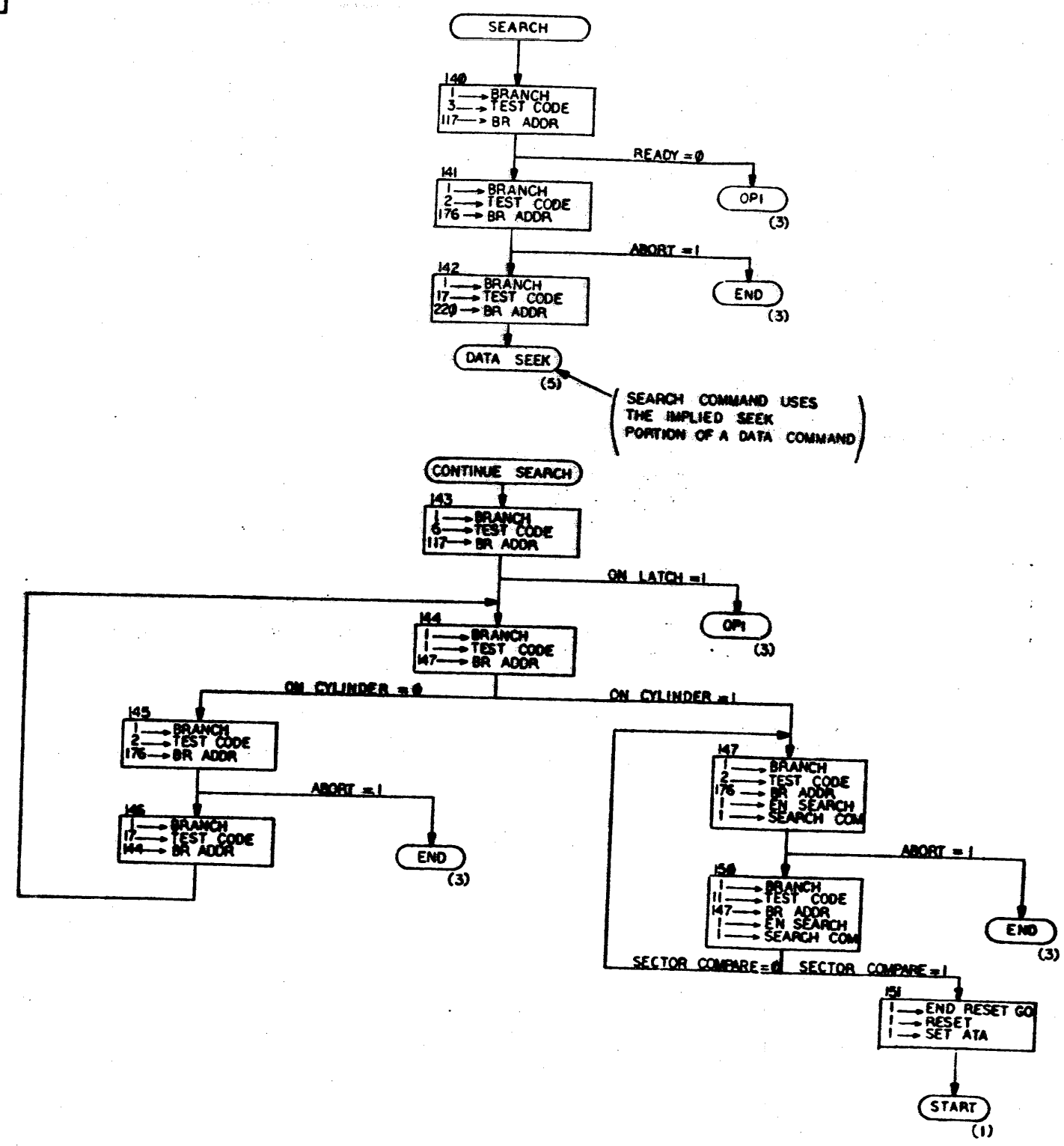


CONTROL SEQUENCER FLOW
PAGE 3 OF 6: RECALIBRATE
SEEK
DRIVE CLEAR

DATE	PROJ. NUMBER	REV.
DESIGN	TITLE	
ENGR.	RMXX FLOW DIAGRAMS	
PROG. MAN.		
DRAW.		
NEXT HIGHER APPR.		
SCALE	SHEET	REV.
5 of 13	D	RMXX-0-0 A
	DRW.	

CZ 1

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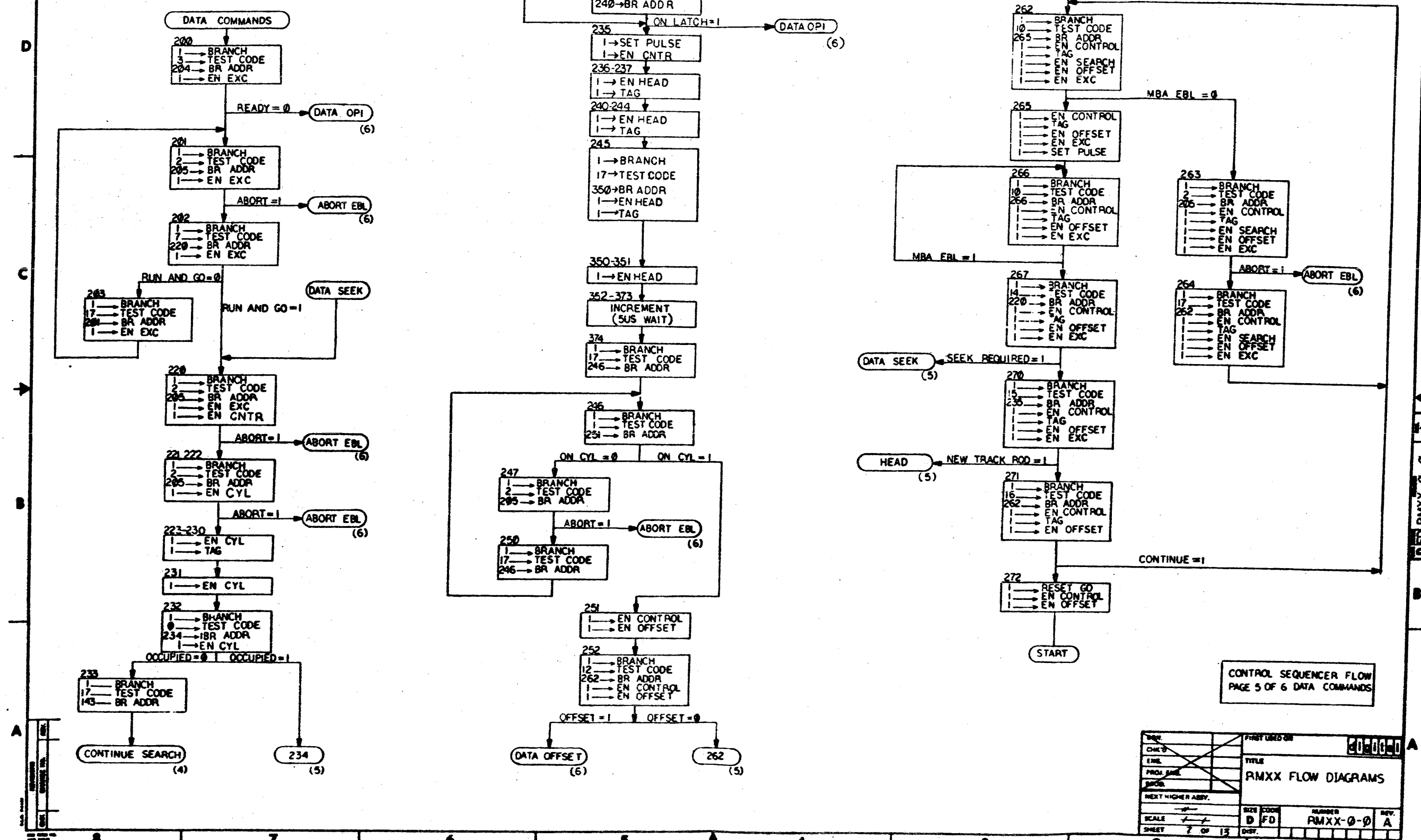


SEARCH COMMAND USES THE IMPLIED SEEK PORTION OF A DATA COMMAND

CONTROL SEQUENCER FLOW
PAGE 4 OF 6: SEARCH

DATE	PERFORMER	400000
CHKD	TITLE	RMXX FLOW DIAGRAMS
PRG. DSGN.	REV.	
INSTR.	REV.	
NEXT HIGHER ASSY.	REV.	
SCALE	REV.	
SHEET 6 OF 13	REV.	

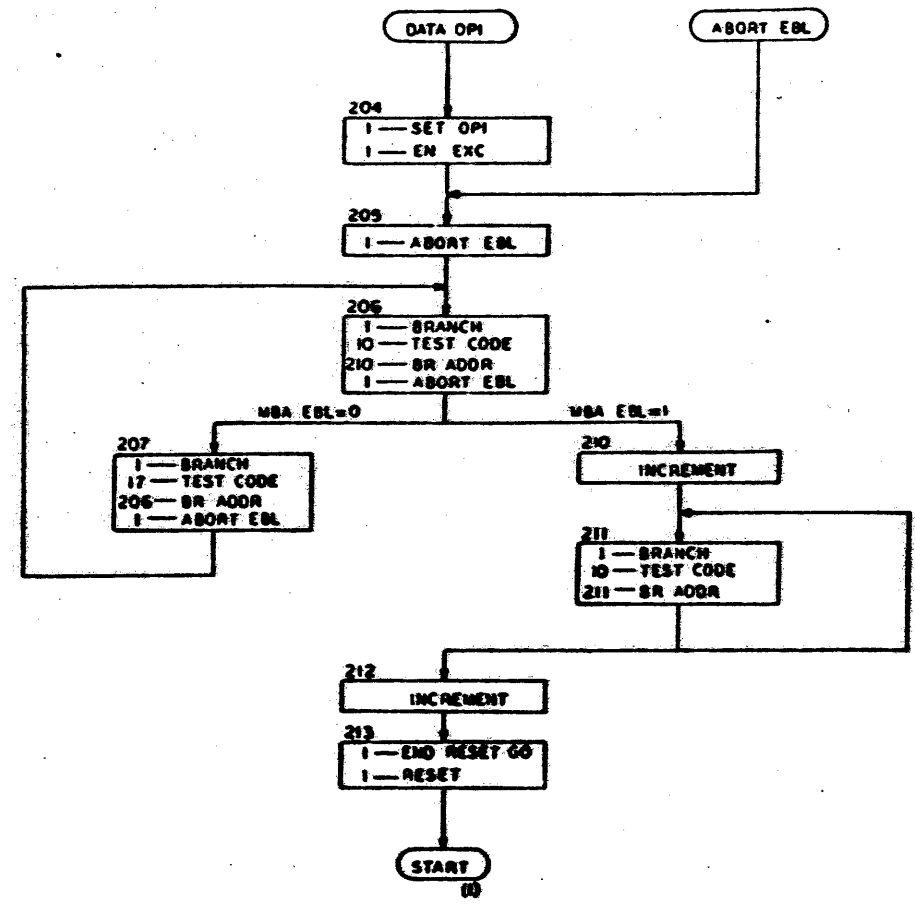
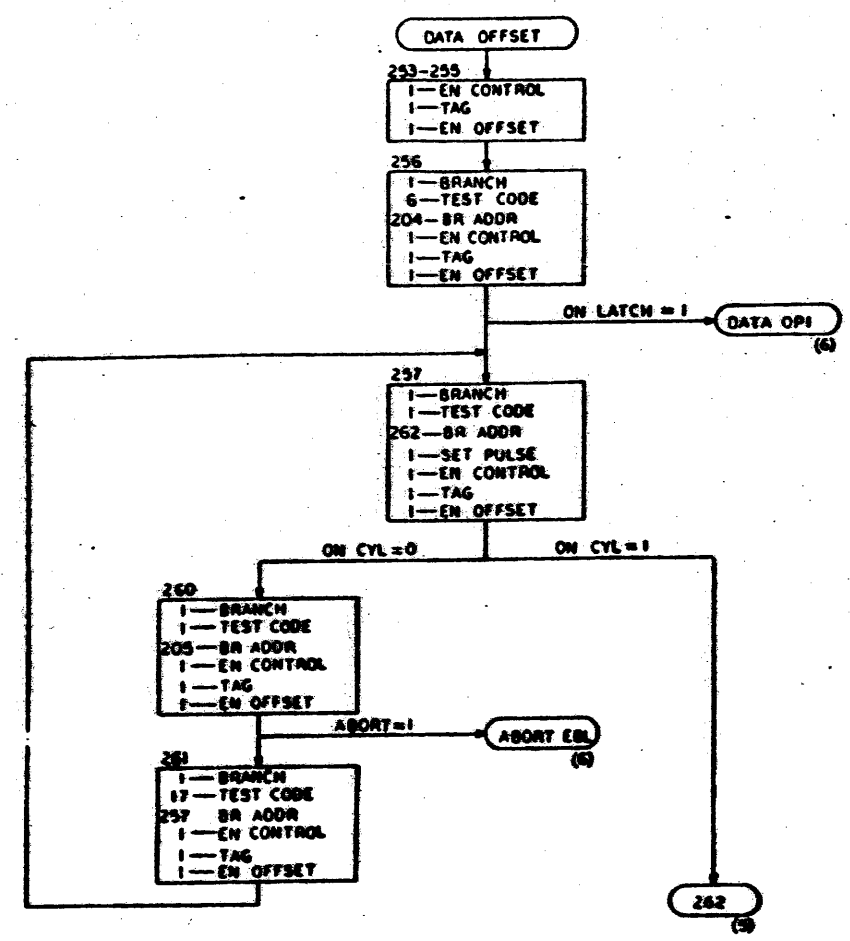
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CONTROL SEQUENCER FLOW
PAGE 5 OF 6 DATA COMMANDS

DATE	PRINTED ON	000001
CHK'D	TITLE	RMXX FLOW DIAGRAMS
ENGR	PROJ. ENG.	
DRWG. ENGR.	NO.	
NEXT HIGHER ABBV.	SIZE CODE	D FD
SCALE	NUMBER	RMXX-0-0 A
SHEET 7 OF 13	REV.	

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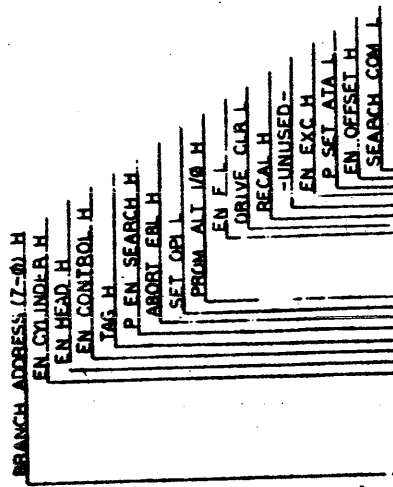


CONTROL SEQUENCER
FLOW
DATA OPI
DATA OFFSET
ABORT EBL PAGE 6 OF 6

REVISIONS		
DATE	CHANGE NO.	REV.

REV	CHG	NO.	BY

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ALL COMMANDS START IN THIS BLOCK IF: IF9 SEQ SKIP L IS HIGH AND CS8 UNIT READY H IS HIGH
EXAMPLE:
PACK ACKNOWLEDGE (23-6-19)
START: 0 BR 23
126 RST

DEC OCT LOC	NO-OP	START	BR	TO (FUNCTION CODE-F4)	
0	000		BR	TO 126	01000001
1	001		BR	TO 126	00000011
2	002	X	RST		00000000
3	003	X	BR	TO 126	00000000
4	004	SEEK	RST		00000000
5	005		RST	TO 64 IF F4 IS 0	00000000
6	006	RECAL	RST	TO 64 IF F4 IS 0	00000000
7	007		RST	TO 125 IF F4 IS 0	00000000
8	010	DR CLR	RST	TO 125 IF F4 IS 0	00000000
9	011		RST	TO 126	00000000
10	012	RELEASE	RST	TO 126 IF F4 IS 0	00000000
11	013		RST	TO 126	00000000
12	014	OFFSET	RST	TO 127 IF F4 IS 0	00000000
13	015		RST	TO 126	00000000
14	016	RTC	RST	TO 126	00000000
15	017		RST	TO 126	00000000
16	020	READ IN	RST	TO 126 IF F4 IS 0	00000000
17	021	PRESET	RST	TO 126	00000000
18	022	PACK	RST	TO 126 IF F4 IS 0	00000000
19	023	ACK	RST	TO 126	00000000
20	024	X	RST	TO 126	00000000
21	025		RST	TO 126	00000000
22	026	X	RST	TO 126	00000000
23	027		RST	TO 126	00000000
24	030	SEARCH	RST	TO 96 IF F4 IS 0	00000000
25	031		RST	TO 126	00000000
26	032	X	RST	TO 126	00000000
27	033		RST	TO 126	00000000
28	034	X	RST	TO 126	00000000
29	035		RST	TO 126	00000000
30	036	X	RST	TO 126	00000000
31	037		RST	TO 126	00000000

X = ILLEGAL

32	040	NO-OP	RST		00000000	1000101
33	041		RST	TO 126 IF DATA CMD	00000000	1000101
34	042	X	RST	TO 126 IF DATA CMD	00000000	1000101
35	043		RST		00000000	1000101
36	044	SEEK	RST		00000000	1000101
37	045		RST		00000000	1000101
38	046	RECAL	RST		00000000	1000101
39	047		RST		00000000	1000101
40	050	DR CLR	RST	TO 125 IF F4 IS 0	00000000	1000101
41	051		RST	TO 133	00000000	1000101
42	052	RELEASE	RST	TO 126 IF F4 IS 0	00000000	1000101
43	053		RST	TO 133	00000000	1000101
44	054	OFFSET	RST		00000000	1000101
45	055		RST		00000000	1000101
46	056	RTC	RST		00000000	1000101
47	057		RST		00000000	1000101
48	060	READ IN	RST	TO 17 IF F4 IS 0	00000000	1000101
49	061	PRESET	RST	TO 133	00000000	1000101
50	062	PACK	RST	TO 17 IF F4 IS 0	00000000	1000101
51	063	ACK	RST	TO 133	00000000	1000101
52	064	X	RST	TO 133	00000000	1000101
53	065		RST		00000000	1000101
54	066	X	RST		00000000	1000101
55	067		RST		00000000	1000101
56	070	SEARCH	RST	TO 126 IF F4 IS 0	00000000	1000101
57	071		RST	TO 133	00000000	1000101
58	072	X	RST	TO 126 IF F4 IS 0	00000000	1000101
59	073		RST	TO 133	00000000	1000101
60	074	X	RST	TO 133	00000000	1000101
61	075		RST		00000000	1000101
62	076	X	RST		00000000	1000101
63	077		RST		00000000	1000101

ALL COMMANDS START IN THIS BLOCK IF: IF9 SEQ SKIP L IS LOW
EXAMPLE:
PACK ACKNOWLEDGE
START:
0 BR 19+32
51 BR 19
19 BR 126
126 RST

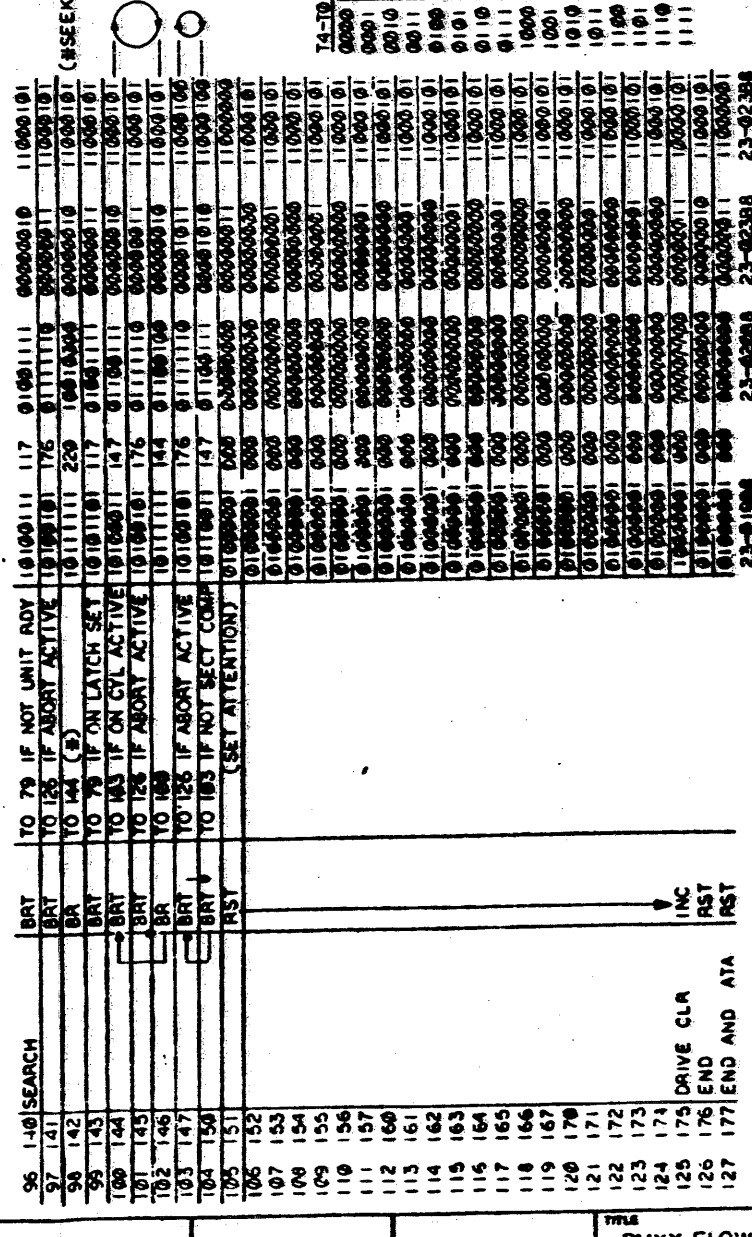
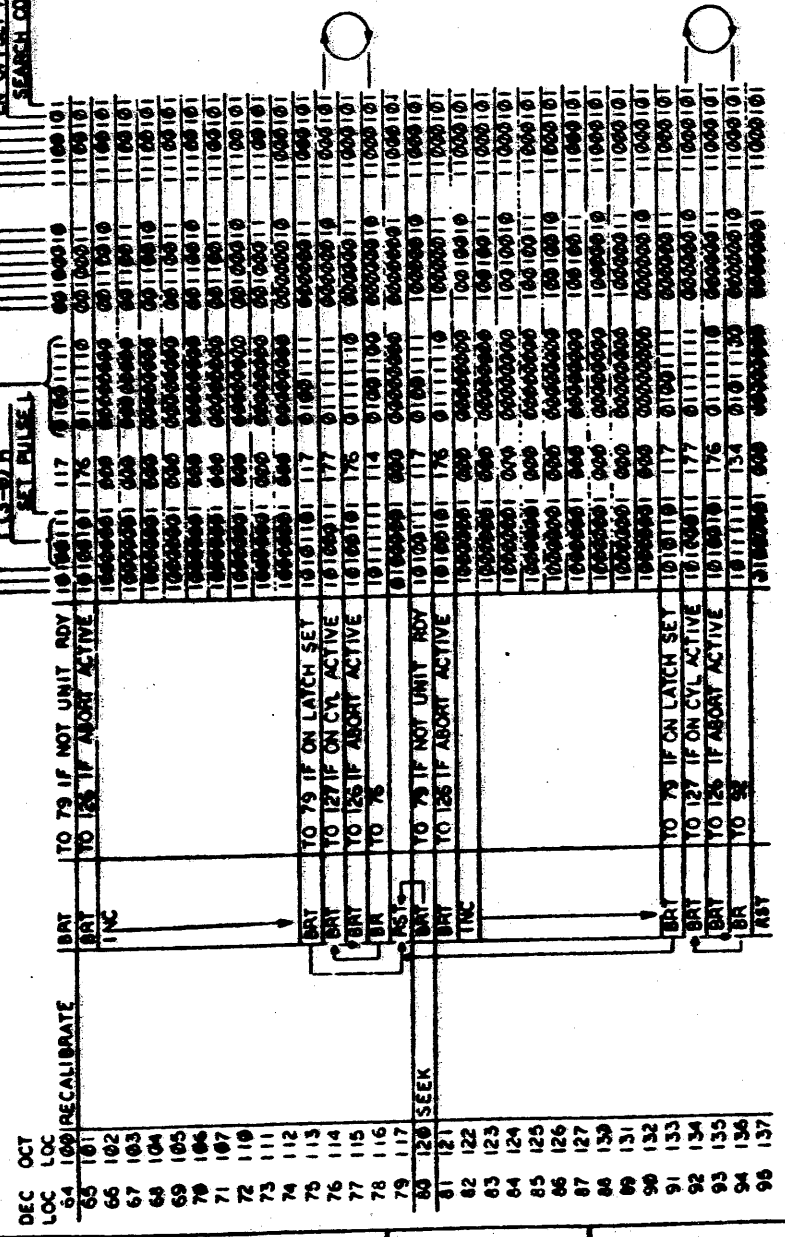
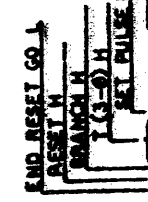
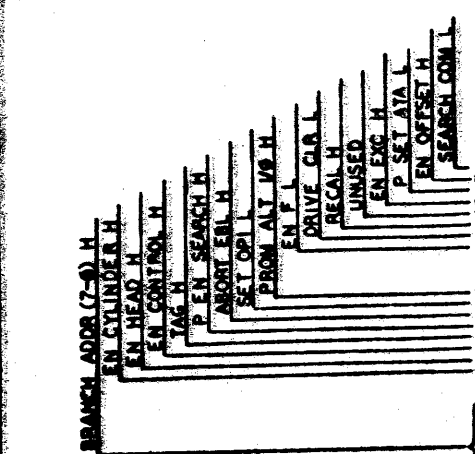
BRANCHING WILL NEVER OCCUR, IT WILL ALWAYS BE INCREMENTED

CONTROL SEQUENCER
ROM LISTINGS
PAGE 1 OF 4

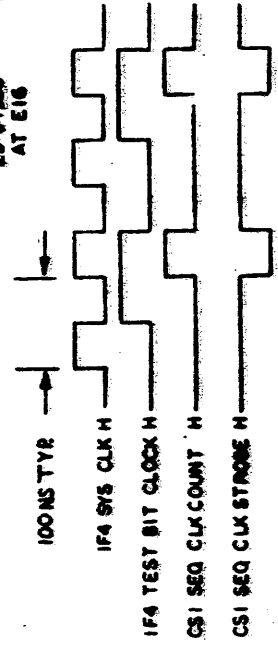
PIN NUMBERS FOR DATA OUTPUTS

17	16	15	14	13	12	11	10	9	8	7
----	----	----	----	----	----	----	----	---	---	---

IF4 TEST BIT CLOCK H
 CS1 SEQ CLK COUNT H
 CS1 SEQ CLK STROBE H



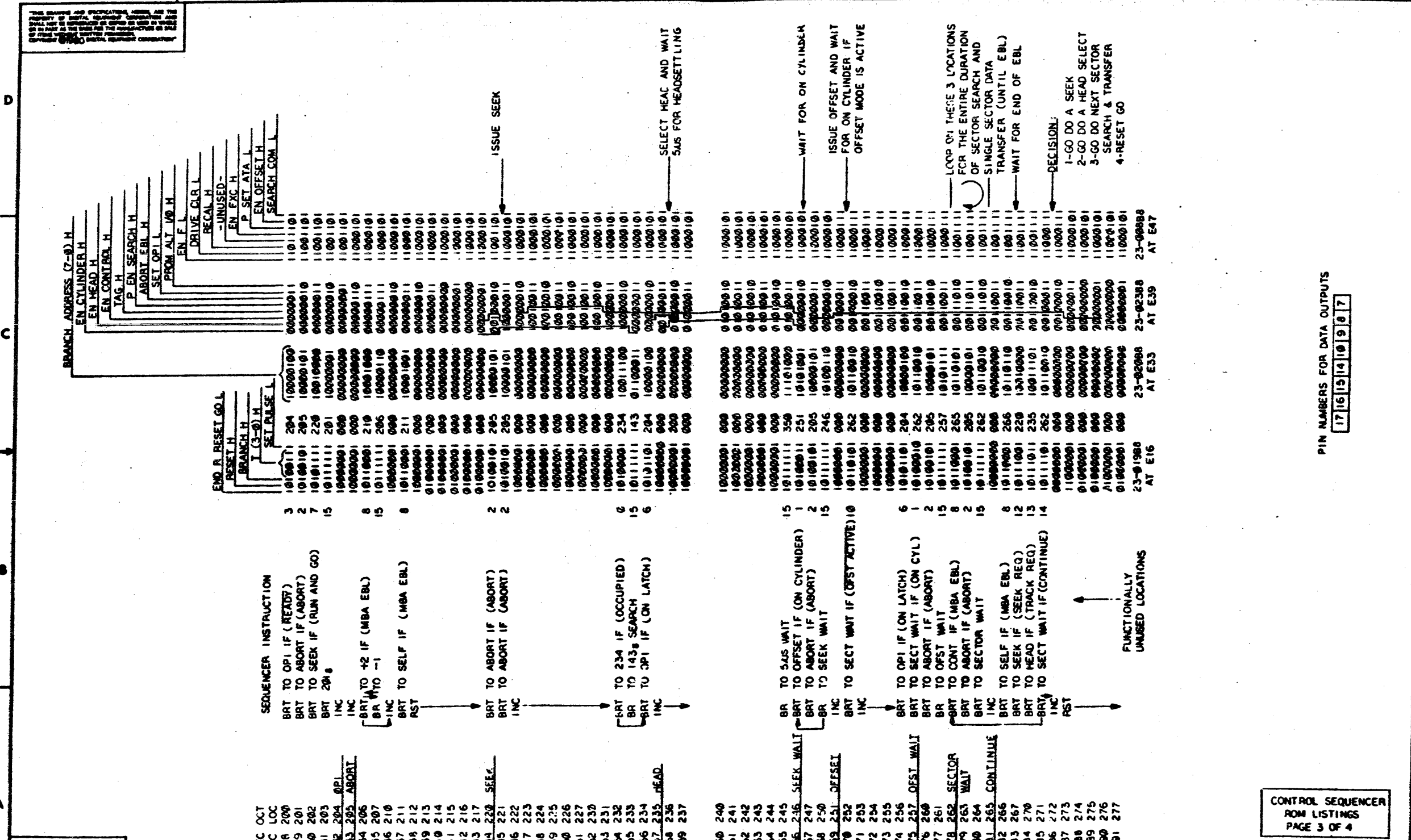
TEST CONDITION	ACTIVE LEVEL
0000 0	L
0000 1	L
0001 0	L
0001 1	L
0010 2	H
0011 3	H
0100 4	L
0101 5	L
0110 6	L
0111 7	H
1000 8	L
1001 9	H
1010 10	H
1011 11	L
1100 12	L
1101 13	L
1110 14	L
1111 15	GND



CONTROL SEQUENCER ROM LISTINGS PAGE 2 OF 4

PIN NUMBERS FOR DATA OUTPUTS
 17 15 13 14 12 11 10 9 7

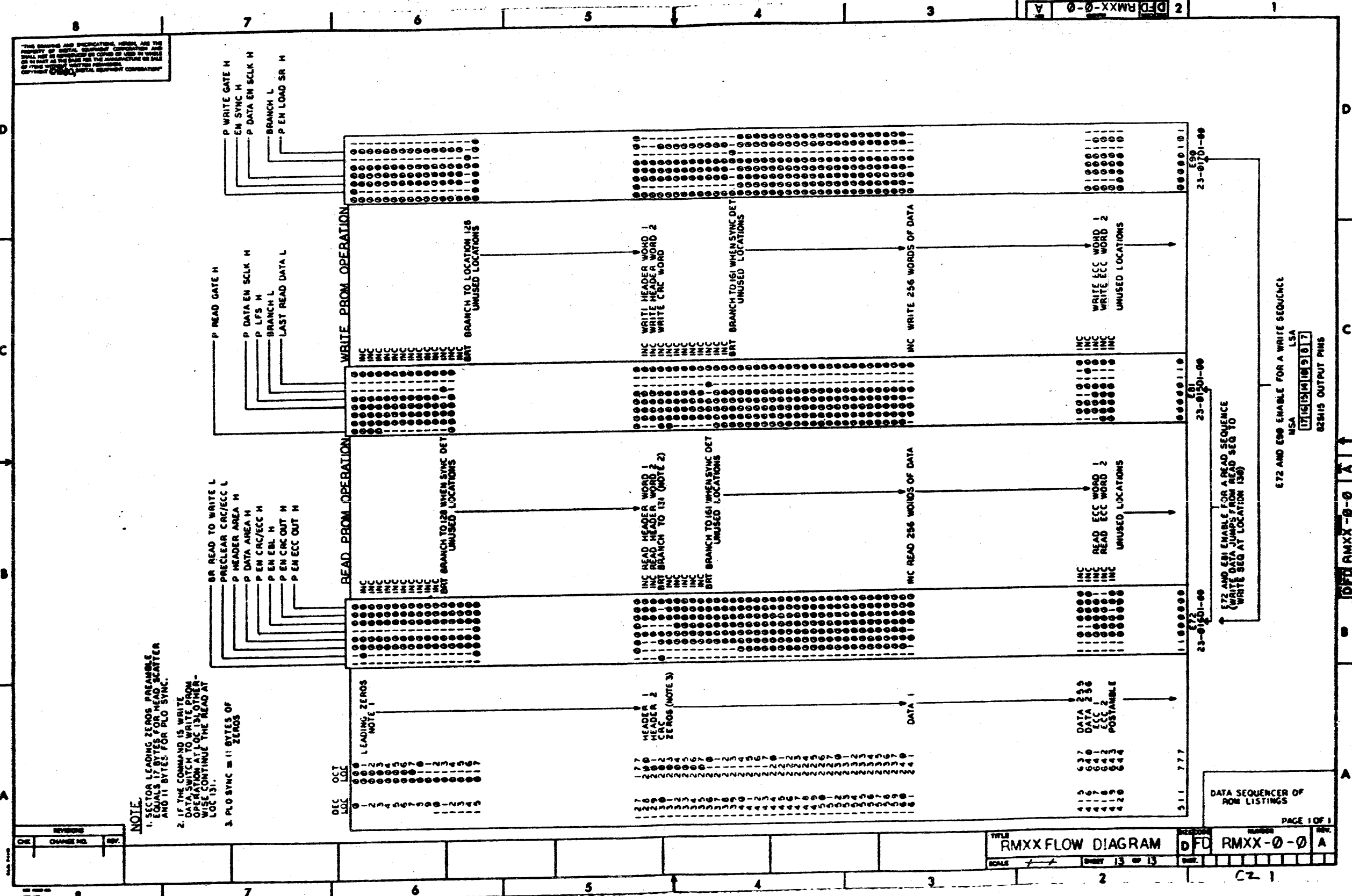
DFD RMXX-0-0 A



CONTROL SEQUENCER ROM LISTINGS PAGE 3 OF 4

PIN NUMBERS FOR DATA OUTPUTS
17 16 15 14 10 9 6 7

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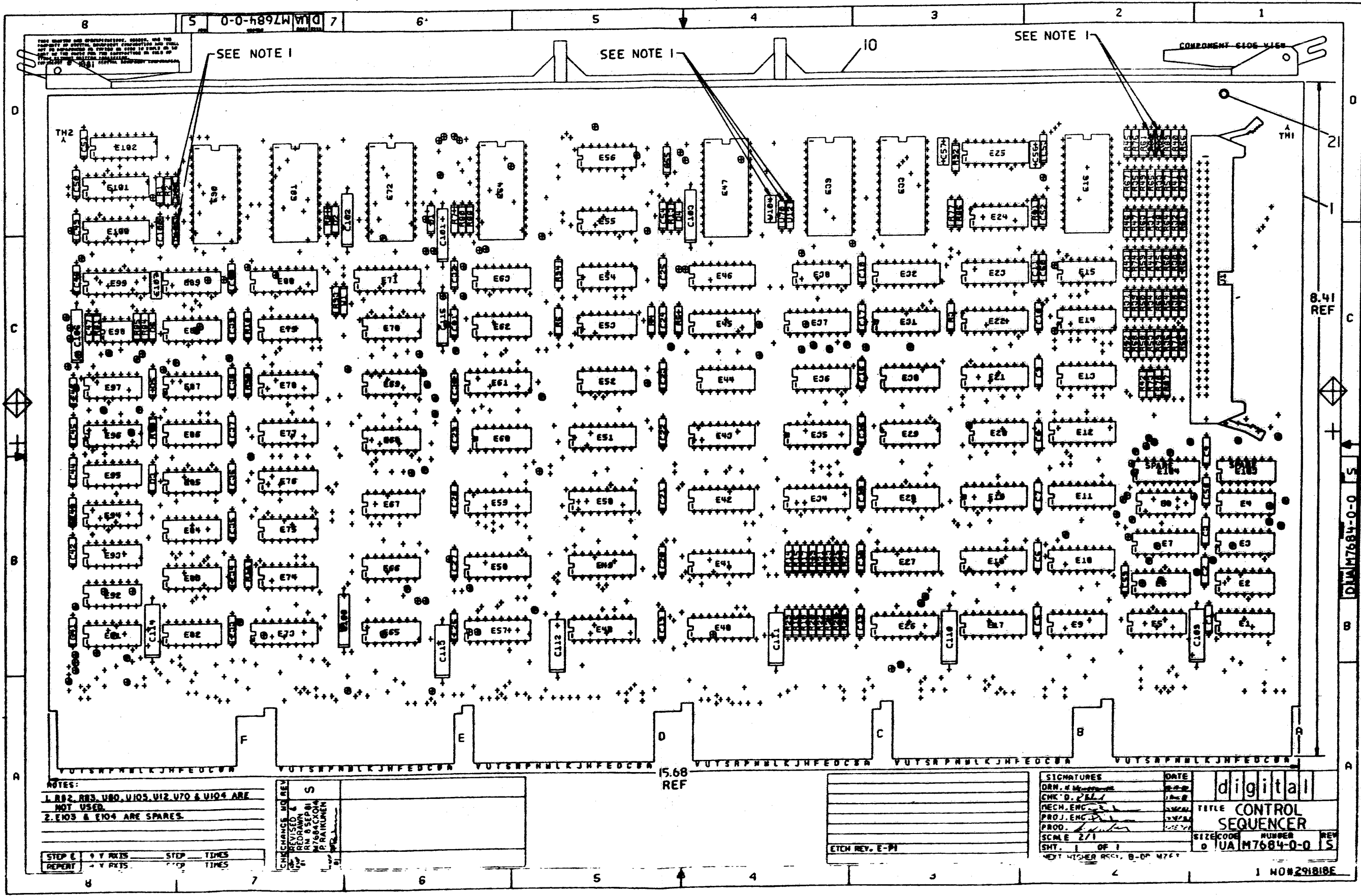
NOTE.
 1. SECTOR LEADING ZEROS PREAMBLE EQUALS 17 BYTES FOR HEAD, SCATTER AND 11 BYTES FOR PLO SYNC.
 2. IF THE COMMAND IS WRITE DATA LOC TO WRITE PROM OPERATION AT LOC 131, OTHERWISE CONTINUE THE READ AT LOC 131.
 3. PLO SYNC = 11 BYTES OF ZEROS

REVISIONS		
CHK	CHANGE NO.	REV.

TITLE: RMXX FLOW DIAGRAM
 NUMBER: DFD RMXX-0-0 A
 SCALE: 1:1
 SHEET: 13 OF 13
 DATE: 11/15/77
 DESIGNED BY: [Signature]
 CHECKED BY: [Signature]
 APPROVED BY: [Signature]

DATA SEQUENCER OF ROM LISTINGS
 PAGE 1 OF 1

E72 AND E81 ENABLE FOR A READ SEQUENCE (WRITE DATA JUMPS FROM READ SEQ TO WRITE SEQ AT LOCATION 130)
 E72 AND E80 ENABLE FOR A WRITE SEQUENCE
 MSA LSA
 [Signature]
 028115 OUTPUT PINS



NOTES:
 1. R12, R13, V10, V12, V70 & V104 ARE NOT USED.
 2. E103 & E104 ARE SPARES.

CHANGE BY	S
REVISION	
REWORK	
DATE	
BY	

STEP	REV	DATE	BY
1			
2			
3			
4			
5			
6			
7			

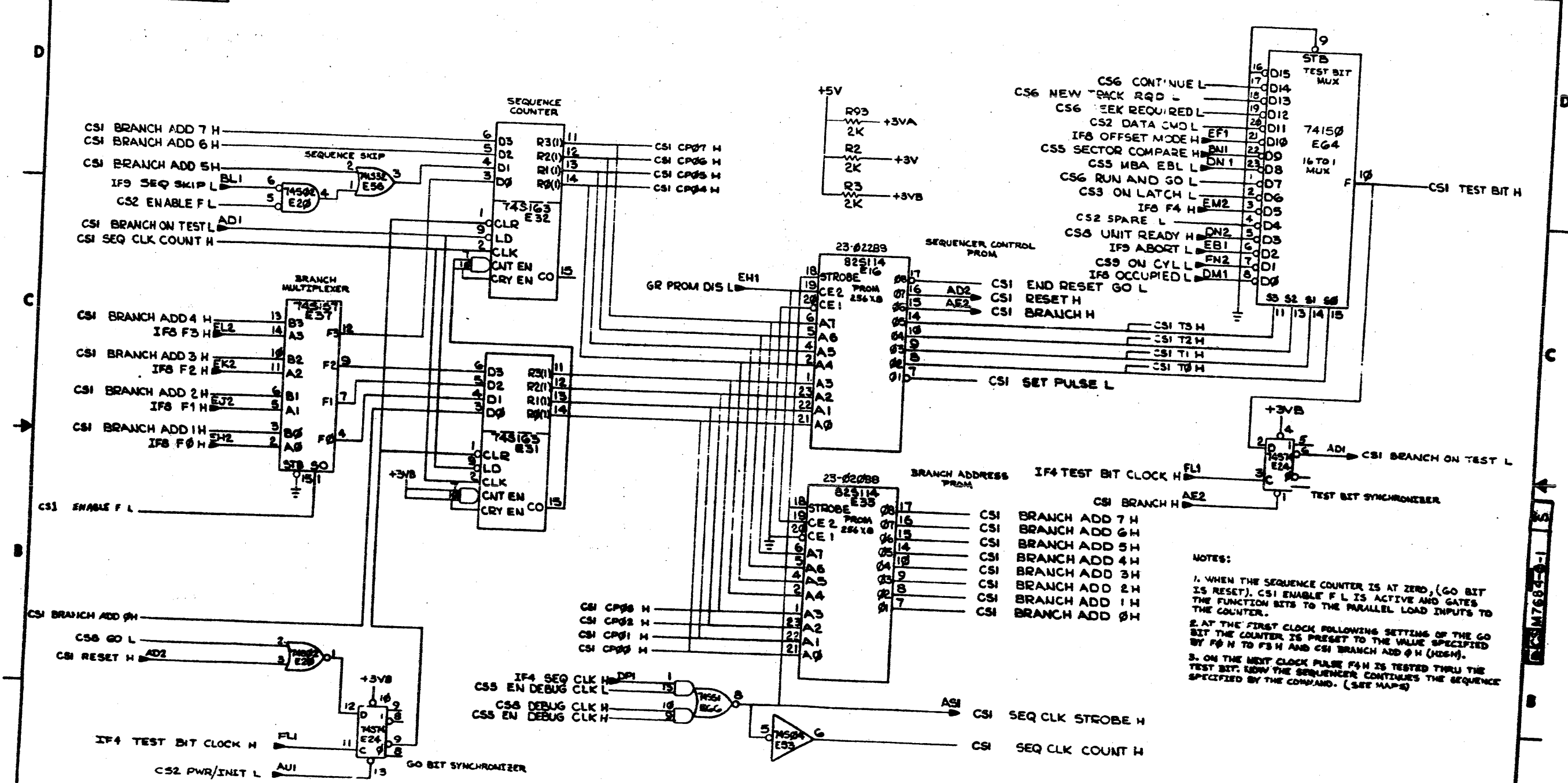
ETCH REV. E-PI

SIGNATURES	DATE
DRN. of Manufacture	
CHK'D. E.M.	
MECH. ENG.	
PROJ. ENG.	
PROO.	
SCALE 2/1	
SHT. 1 OF 1	
NEXT HIGHER ASSY. B-09 M764	

digital	
TITLE CONTROL SEQUENCER	
SIZE CODE	NUMBER
0 UA M7684-0-0	5
REV	

1 NOV 29 198E

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NOTES:

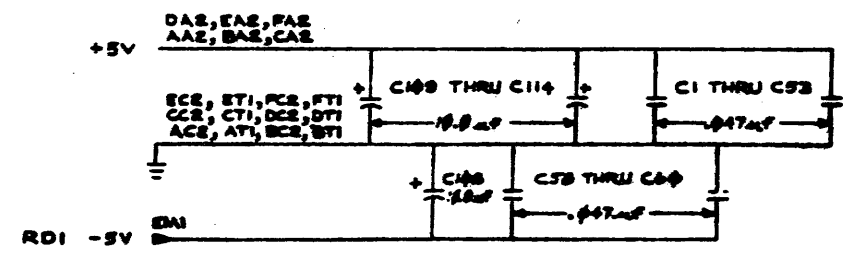
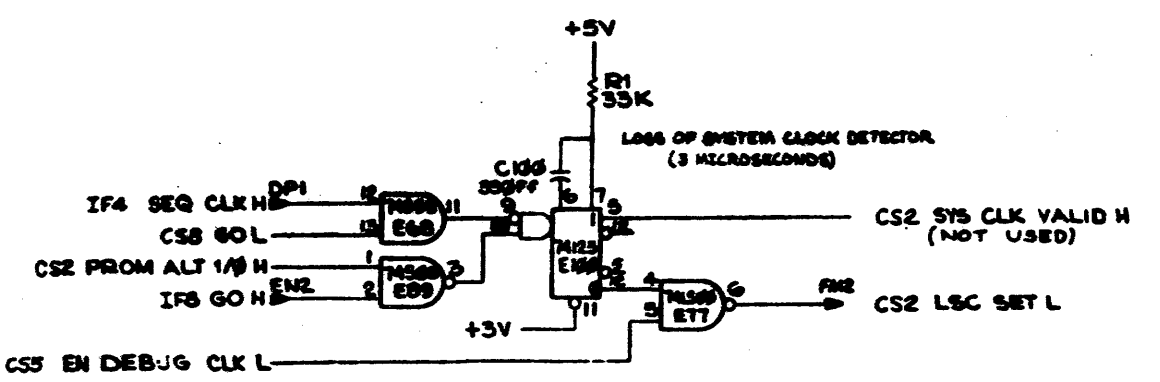
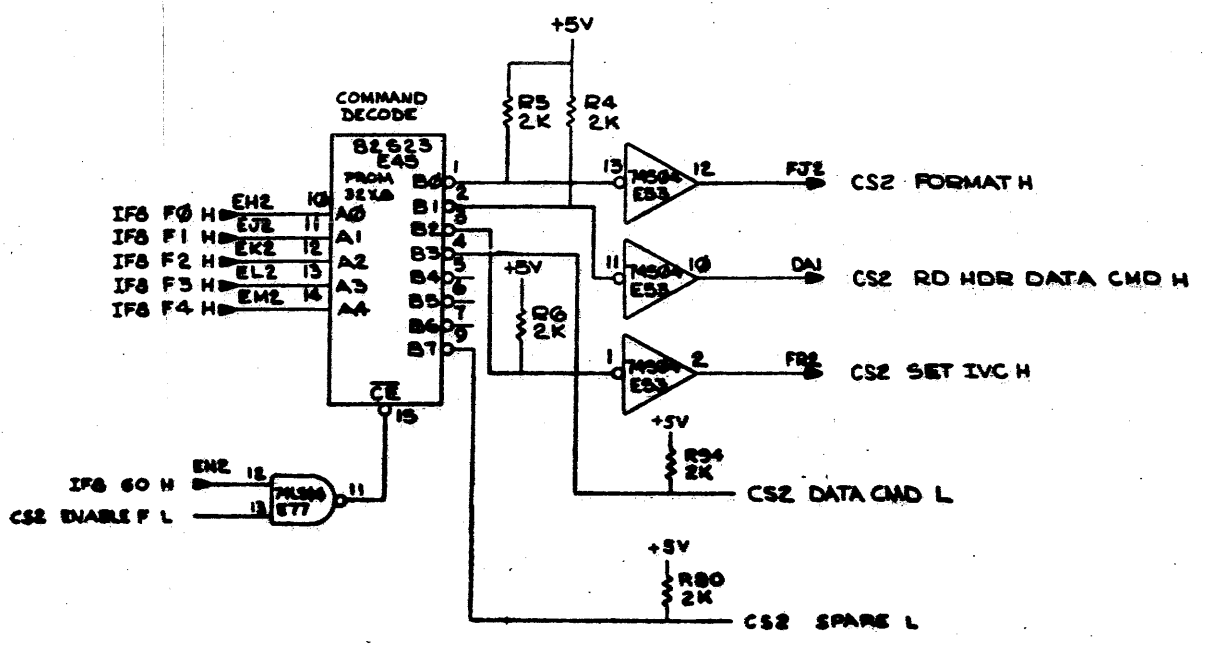
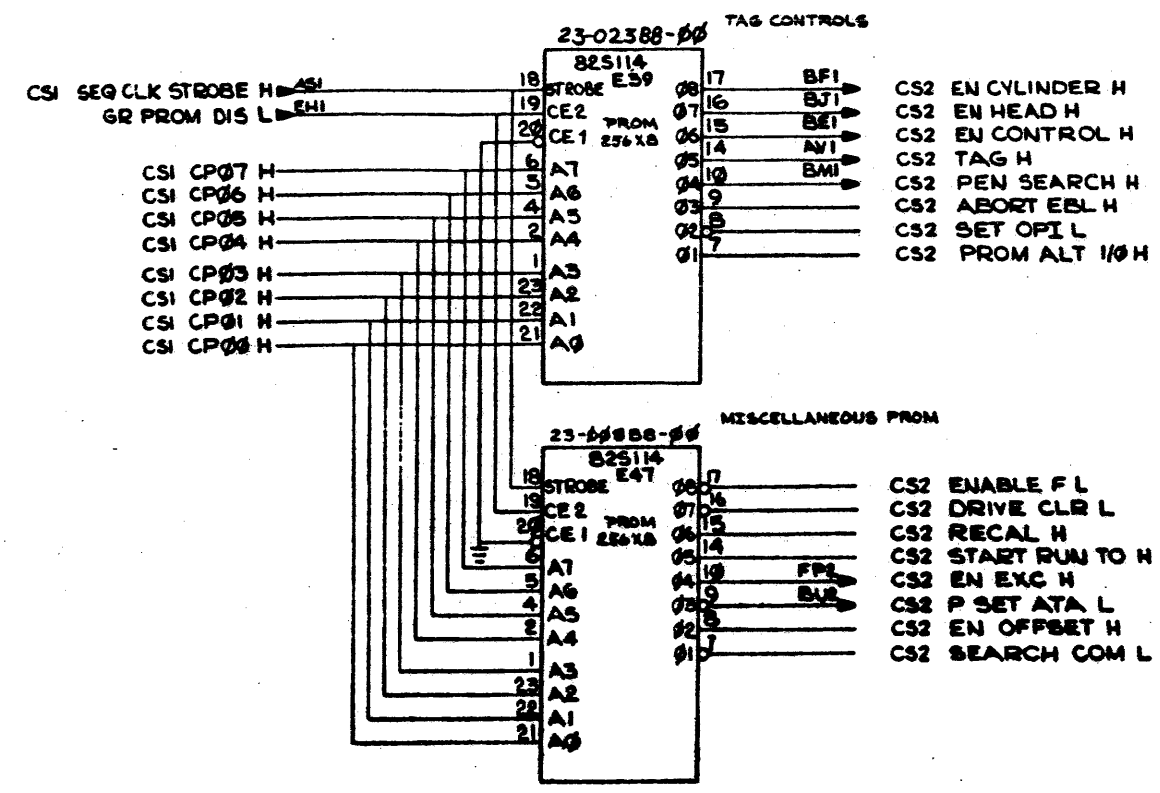
- 1. WHEN THE SEQUENCE COUNTER IS AT ZERO, (GO BIT IS RESET), CS1 ENABLE F L IS ACTIVE AND GATES THE FUNCTION BITS TO THE PARALLEL LOAD INPUTS TO THE COUNTER.
- 2. AT THE FIRST CLOCK FOLLOWING SETTING OF THE GO BIT THE COUNTER IS PRESET TO THE VALUE SPECIFIED BY F0 H TO F3 H AND CS1 BRANCH ADD 0 H (NSDM).
- 3. ON THE NEXT CLOCK PULSE F4 H IS TESTED THRU THE TEST BIT. NOW THE SEQUENCER CONTINUES THE SEQUENCE SPECIFIED BY THE COMMAND. (SEE MAPS)

0001	0002	0003	0004	0005	0006	0007	0008	0009	0010
0011	0012	0013	0014	0015	0016	0017	0018	0019	0020
0021	0022	0023	0024	0025	0026	0027	0028	0029	0030
0031	0032	0033	0034	0035	0036	0037	0038	0039	0040
0041	0042	0043	0044	0045	0046	0047	0048	0049	0050
0051	0052	0053	0054	0055	0056	0057	0058	0059	0060
0061	0062	0063	0064	0065	0066	0067	0068	0069	0070
0071	0072	0073	0074	0075	0076	0077	0078	0079	0080
0081	0082	0083	0084	0085	0086	0087	0088	0089	0090
0091	0092	0093	0094	0095	0096	0097	0098	0099	0100

COMMAND SEQUENCER:
COUNTER, TEST BIT AND
BRANCH ADDRESS

DATE	3-1-77	REVISED BY	RM03	Q10000 A
CHKD	3-1-77	TITLE	CONTROL (CS1)	
ENL	3-2-77	SEQUENCER		
PROL	3-3-77			
PROD	3-4-77			
NEXT PROGR ADV				
PROG	M7684-0	REV	D	CS M7684-0-1
SCALE	1	DWG	S	
SHEET	1	OF	1	

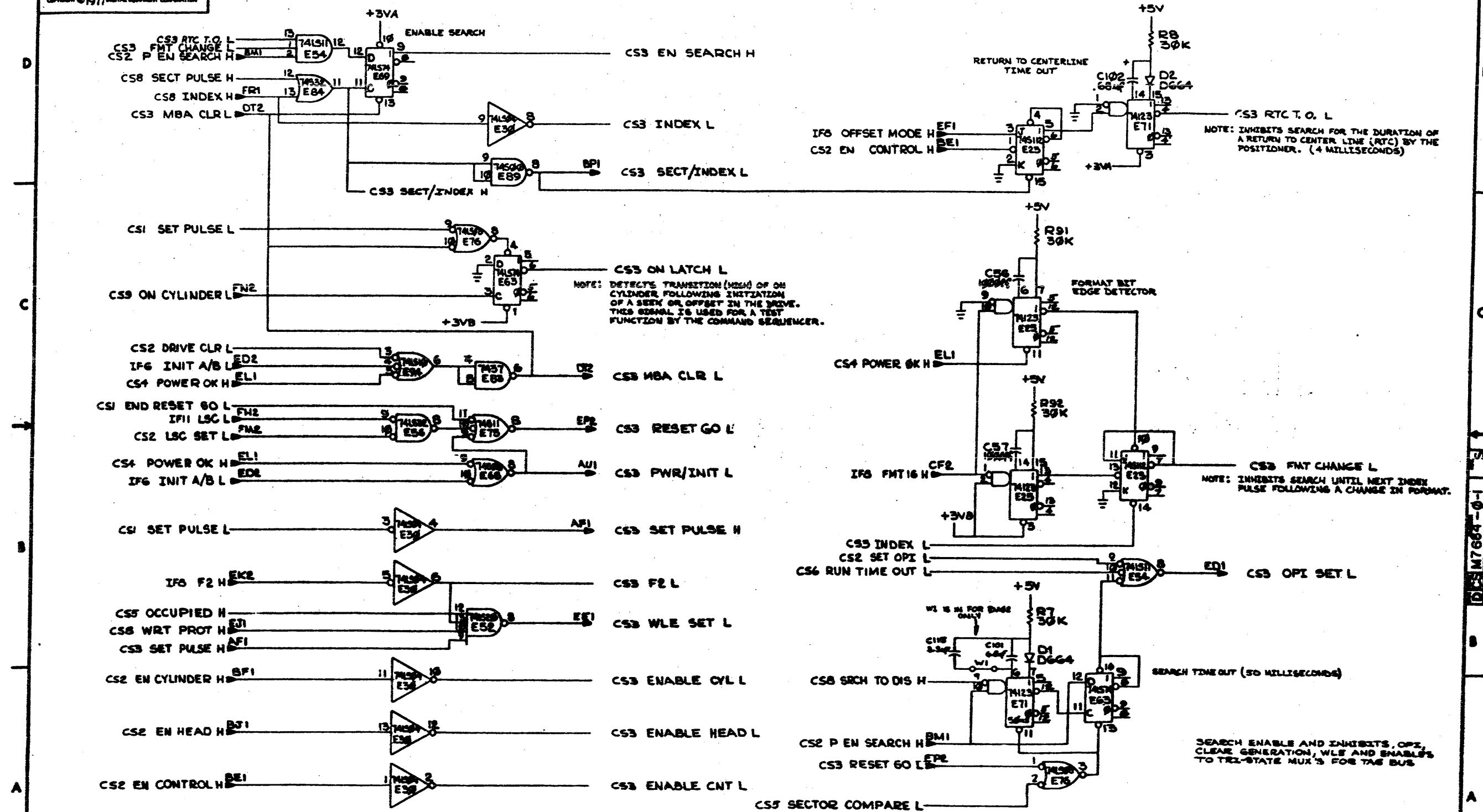
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COMMAND SEQUENCER: DRIVE CONTROLS (TAG) AND COMMAND DECODES

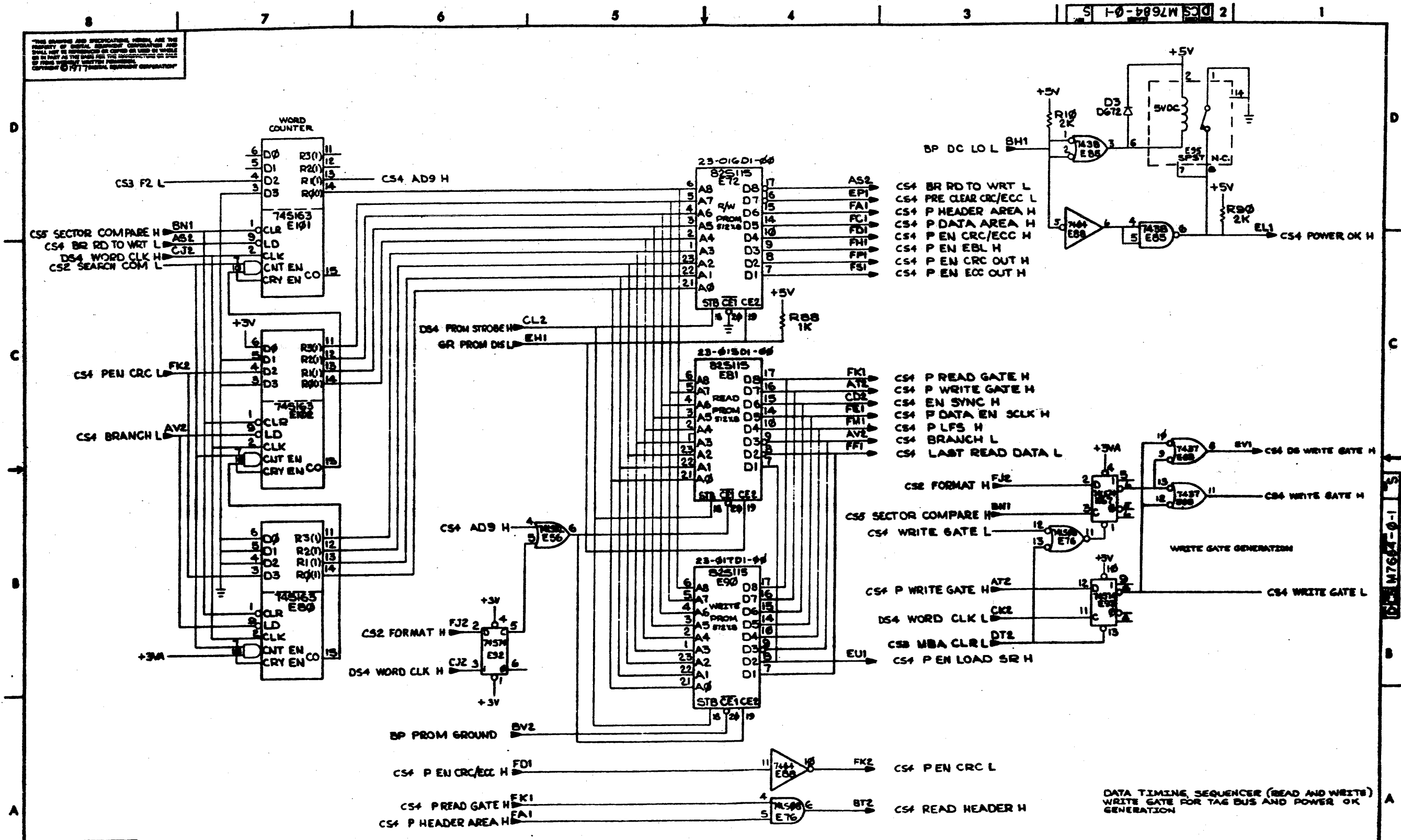
REV	DATE	BY	CHK	CHKD	APP	TITLE	PROJ	ISSUE	REV
						CONTROL SEQUENCER (CS2)	DCS	M7684-9-1	5

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REV.	DATE	BY	CHK	OWNER	NO.	REV.	FILE	NO.	REV.
							CONTROL SEQUENCER (CS3)	DCS M7684-0-1	NS

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- CS4 BR RD TO WRT L
- CS4 PRE CLEAR CRC/ECC L
- CS4 P HEADER AREA H
- CS4 P DATA AREA H
- CS4 P EN CRC/ECC H
- CS4 P EN EBL H
- CS4 P EN CRC OUT H
- CS4 P EN ECC OUT H

- CS4 P READ GATE H
- CS4 P WRITE GATE H
- CS4 EN SYNC H
- CS4 P DATA EN SCLK H
- CS4 P LFS H
- CS4 BRANCH L
- CS4 LAST READ DATA L

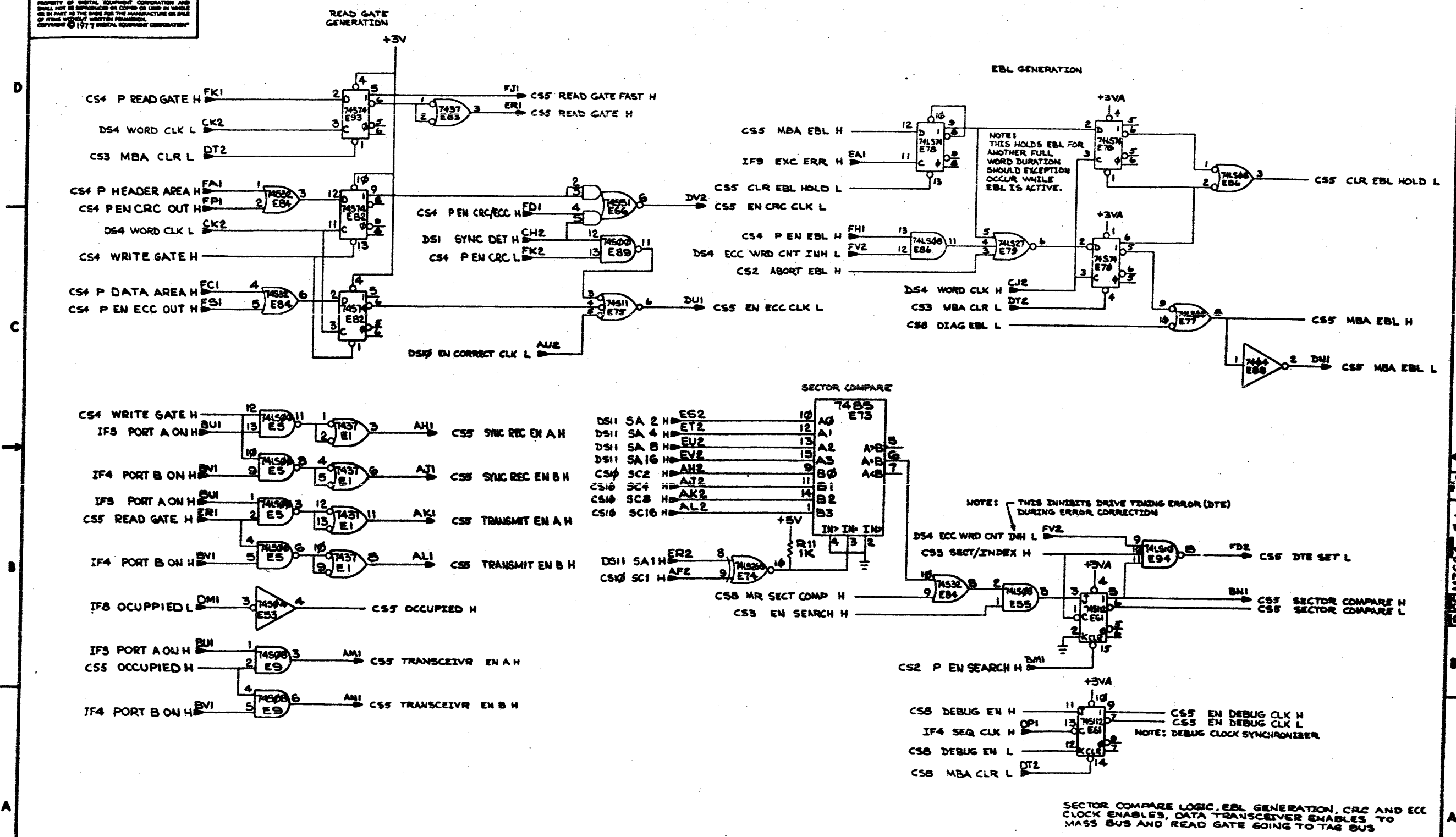
- CS2 FORMAT H
- CS5 SECTOR COMPARE H
- CS4 WRITE GATE L
- CS4 P WRITE GATE H
- DS4 WORD CLK L
- CS5 MBA CLR L
- CS4 P EN LOAD SR H

- CS4 P EN CRC/ECC H
- CS4 P READ GATE H
- CS4 P HEADER AREA H
- CS4 P EN CRC L
- CS4 READ HEADER H

DATA TIMING SEQUENCER (READ AND WRITES)
WRITE GATE FOR TAG BUS AND POWER OK
GENERATION

REV.	DATE	BY	CHKD	APP'D	TITLE	SCALE	SHEET	TOTAL
					CONTROL SEQUENCER (CS4)		4	15
					DCS M7684-0-1			

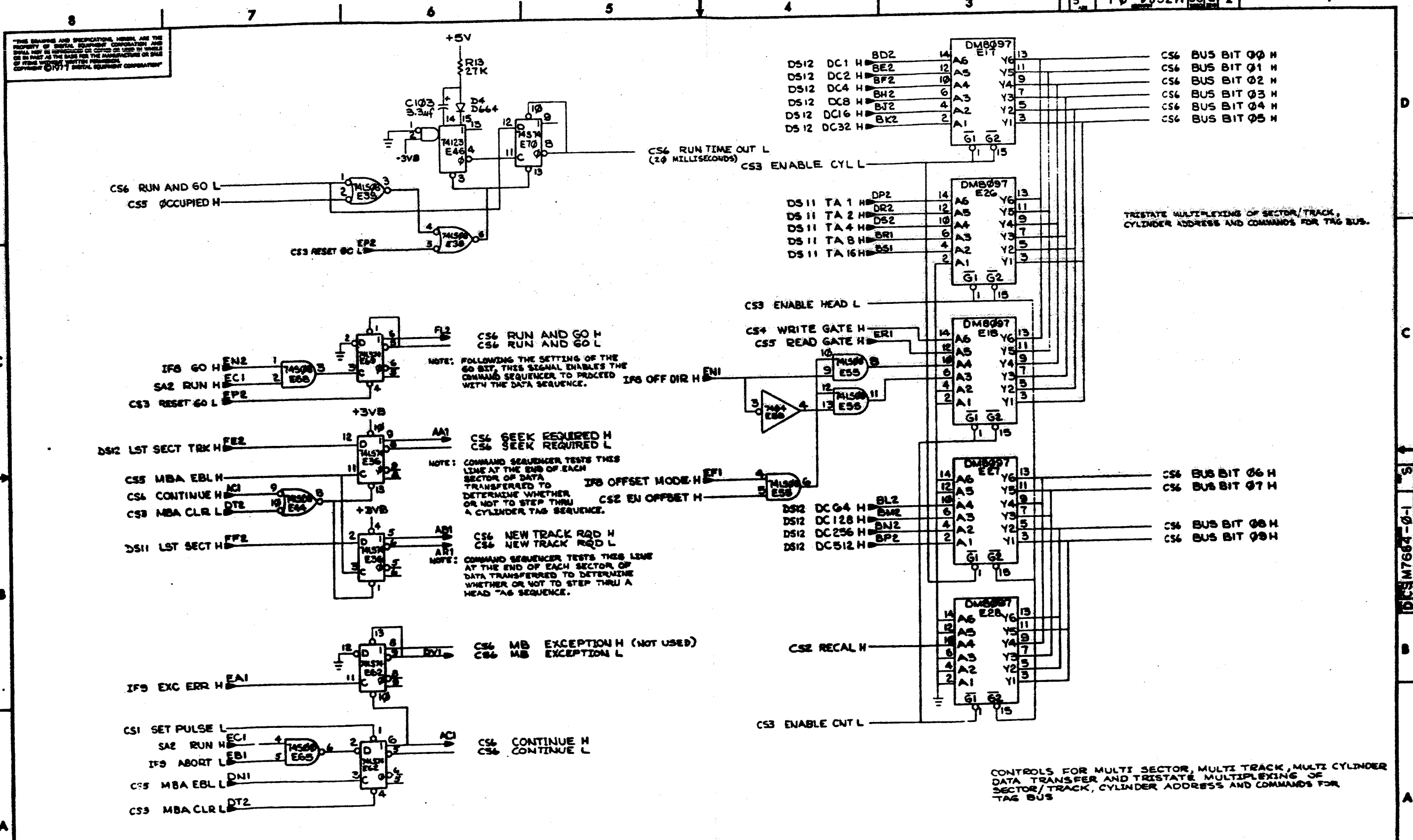
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SECTOR COMPARE LOGIC, EBL GENERATION, CRC AND ECC CLOCK ENABLES, DATA TRANSCIEVR ENABLES TO MASS BUS AND READ GATE GOING TO TAG BUS

REV.	CHG. NO.	REV.	TITLE	DATE	NO.	REV.
			CONTROL SEQUENCER (CS5)	D CS M7684-0-1	1	S
			SCALE	SHEET 5 OF 19		

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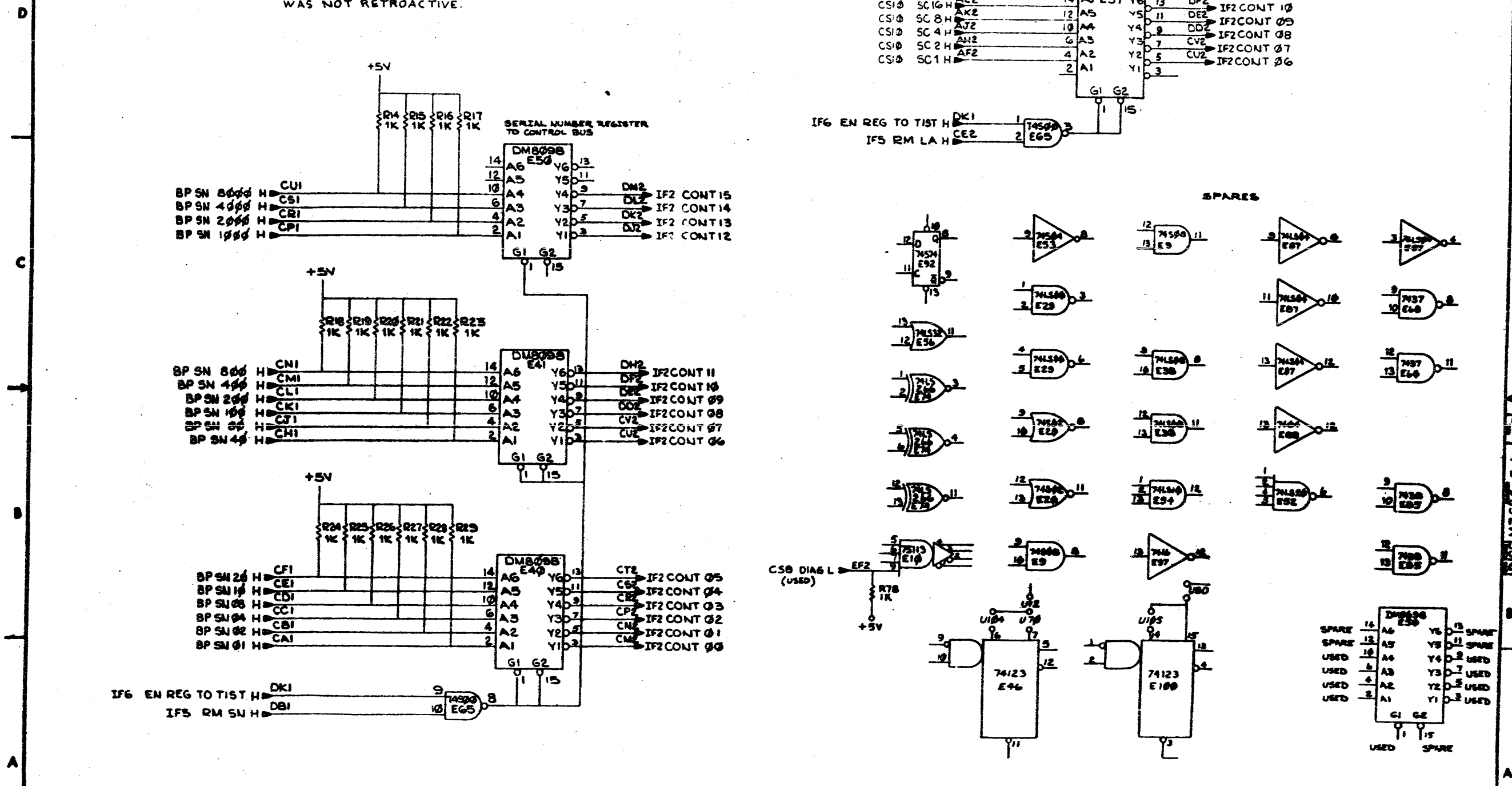
TRISTATE MULTIPLEXING OF SECTOR/TRACK, CYLINDER ADDRESS AND COMMANDS FOR TAG BUS.

CONTROLS FOR MULTI SECTOR, MULTI TRACK, MULTI CYLINDER DATA TRANSFER AND TRISTATE MULTIPLEXING OF SECTOR/TRACK, CYLINDER ADDRESS AND COMMANDS FOR TAG BUS

REV.	DATE	BY	CHK.	CHANGE NO.	REV.	TITLE	NUMBER	REV.
						CONTROL SEQUENCER (CS6)	DCS M7684-0-1	S

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NOTE: ALL ETCH REV B MODULES AND A LIMITED NUMBER OF ETCH REV C MODULES CONTAIN 56 0HM PULL UPS, R14 THRU R29. ECO NO. 6 WAS NOT RETROACTIVE.

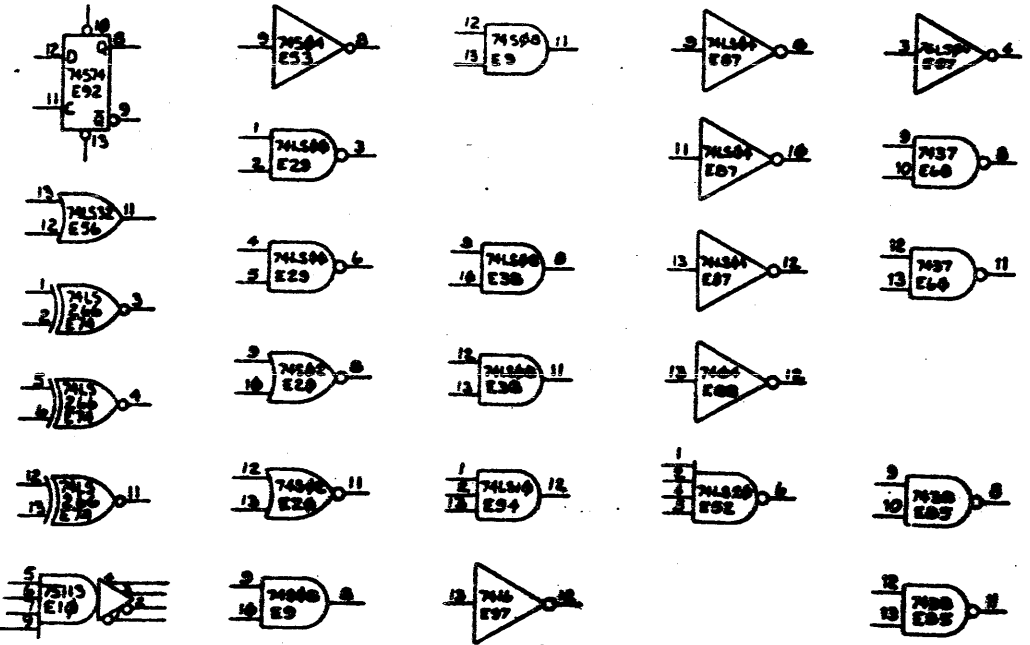


LOOK AHEAD REGISTER TO CONTROL BUS

CS10 SC16 H	AL2	14	A6	E57	Y6	13	DF2	IF2 CONT 10
CS10 SC 8 H	AJ2	12	A5	Y5	11	DE2	IF2 CONT 09	
CS10 SC 4 H	AJ2	10	A4	Y4	9	DD2	IF2 CONT 08	
CS10 SC 2 H	AI2	6	A3	Y3	7	CV2	IF2 CONT 07	
CS10 SC 1 H	AF2	4	A2	Y2	5	CU2	IF2 CONT 06	
		2	A1	Y1	3			

IF6 EN REG TO TIST H DK1
IF5 RM LA H CE2

SPARES

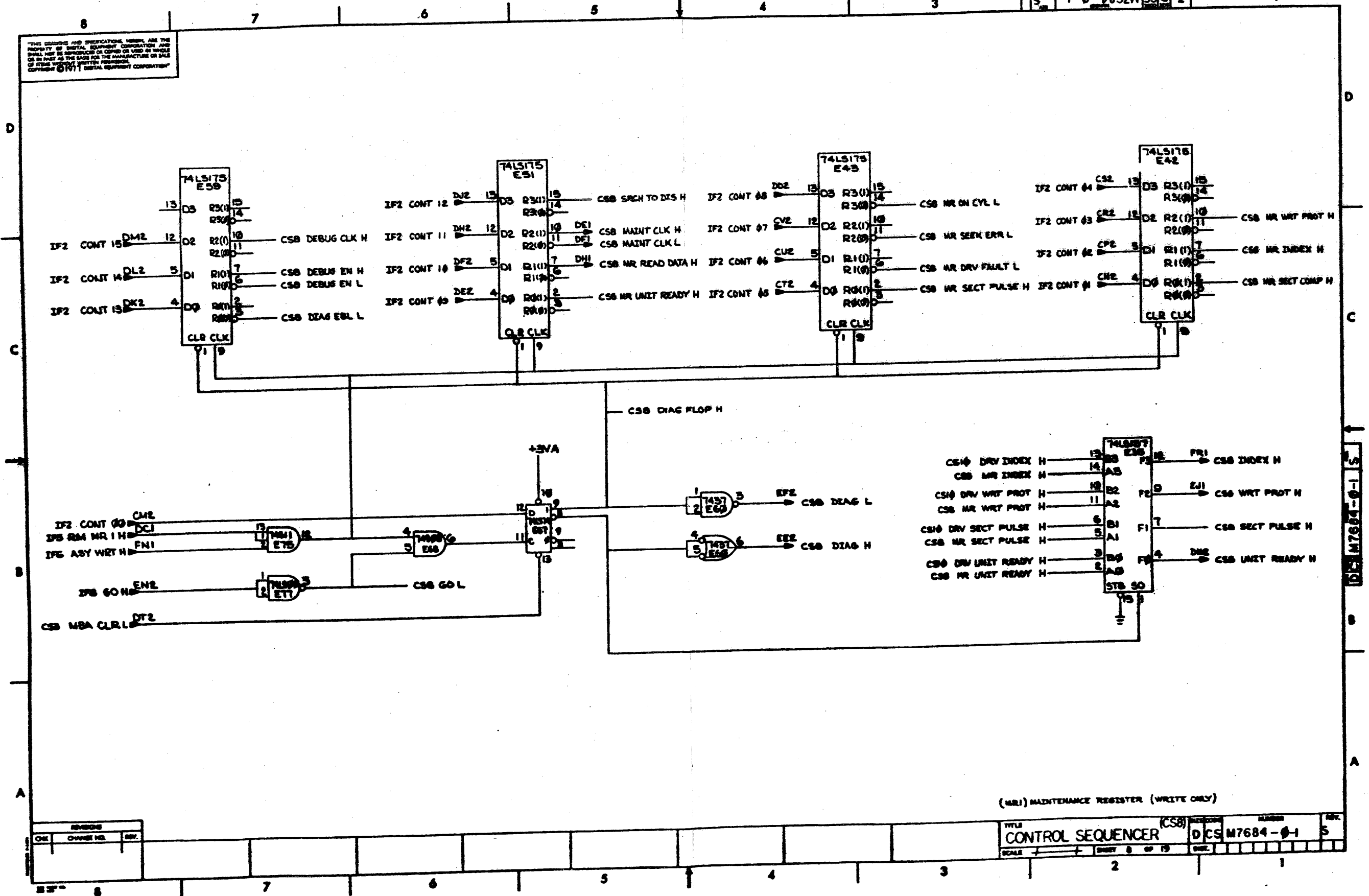


SPARE	14	A6	Y6	13	SPARE
SPARE	12	A5	Y5	11	SPARE
USED	10	A4	Y4	9	USED
USED	6	A3	Y3	7	USED
USED	4	A2	Y2	5	USED
USED	2	A1	Y1	3	USED

REGISTERS: SERIAL NUMBER (LOOK AHEAD)

REV	DATE	BY

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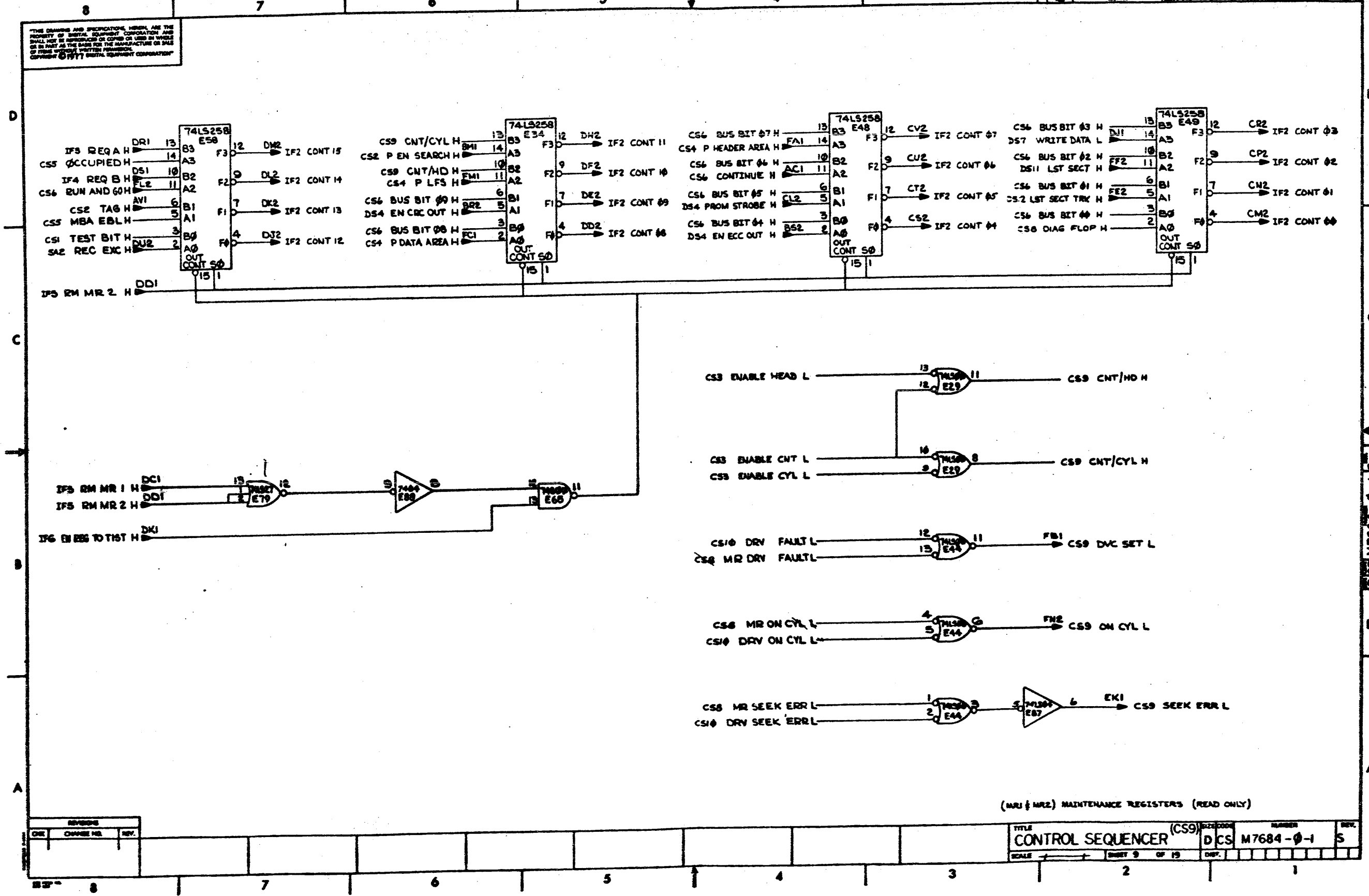


(MRI) MAINTENANCE REGISTER (WRITE ONLY)

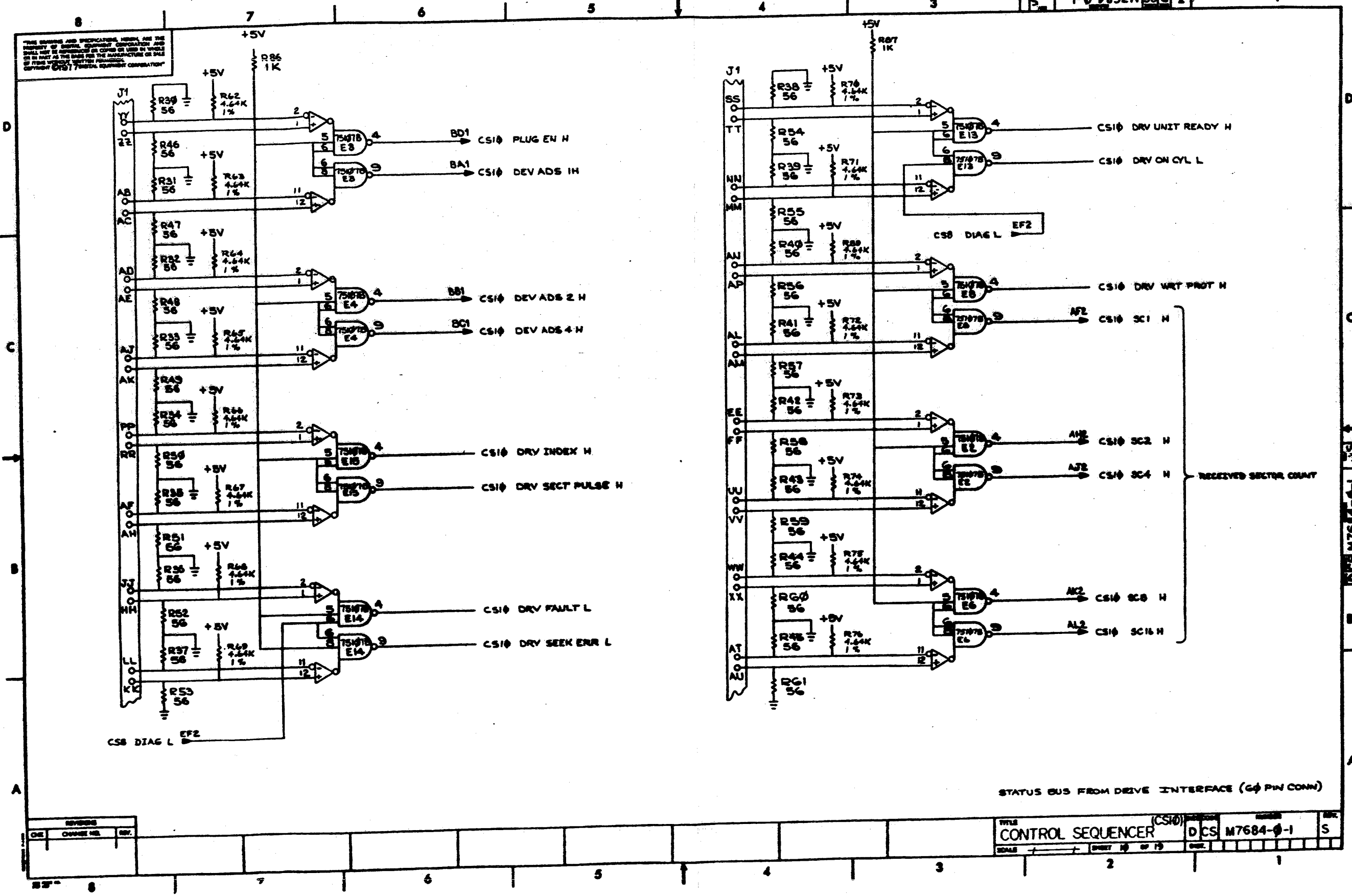
REV.	DATE	BY	CHKD.	OWNER NO.	REV.	DATE	BY	CHKD.	OWNER NO.

TITLE	DCS M7684-01	REV.	5
CONTROL SEQUENCER (CSB)			
SCALE		SHEET 8	OF 13

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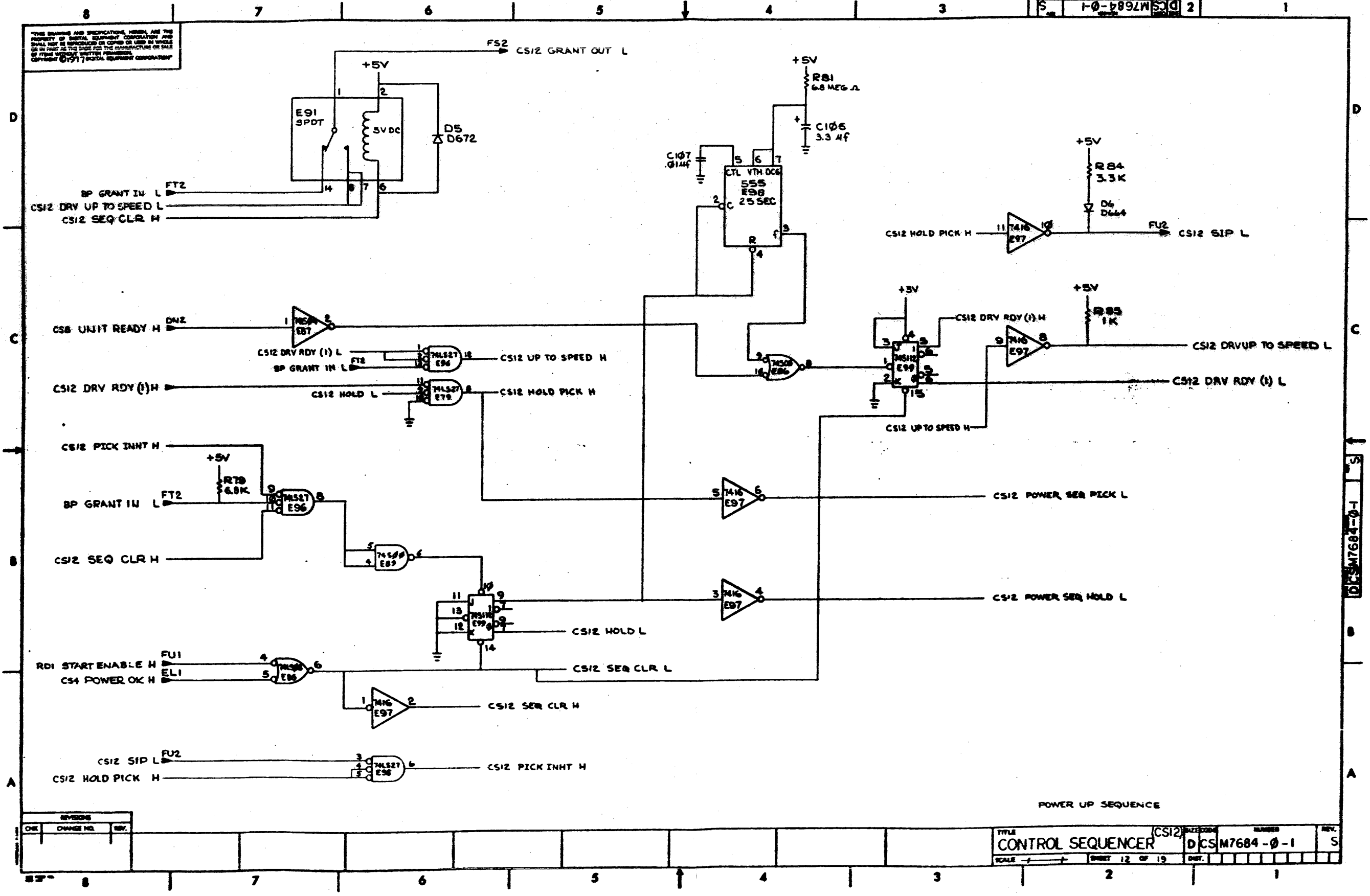


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REV.	CHANGE NO.	DATE

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REVISIONS		
CHK	CHANGE NO.	REV.

TITLE	CONTROL SEQUENCER (CS12)	SIZE CODE	NUMBER	REV.
SCALE	1:1	SHEET	12 OF 19	DIST.
DCS M7684-0-1				S

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AA1	CS6 SEEK REQUIRED H	BA1	CS8 DEV ADS 1 H	CA1	BP SN 01 H	DA1	CS2 RD HDR DATA CMD H	EAI	IF9 EXC ERR H	FA1	CS4 P HEADER AREA H
ABI	CS6 NEW TRACK REQ'D H	BB1	CS8 DEV ADS 2 H	CB1	BP SN 02 H	DB1	IF5 RM SN H	EB1	IF9 ABORT L	FB1	CS9 DVC SET L
AC1	CS6 CONTINUE H	BC1	CS8 DEV ADS 4 H	CC1	BP SN 04 H	DC1	IF5 RM MR 1 H	EC1	SA2 RUN H	FC1	CS4 P DATA AREA H
ADI	CS1 BRANCH ON TEST L	BD1	CS8 PLUG EN H	CD1	BP SN 08 H	DD1	IF5 RM MR 2 H	ED1	CS3 OPT SET L	FD1	CS4 P EN CRC/ECC H
AE1	SPARE	BE1	CS2 EN CONTROL H	CE1	BP SN 10 H	DE1	CS8 MAINT CLK H	EE1	CS3 WLE SET L	FE1	CS4 P DATA EN SOLK H
AF1	CS3 SET PULSE H	BF1	CS2 EN CYLINDER H	CF1	BP SN 20 H	DF1	CS8 MAINT CLK L	EF1	IF8 OFFSET MODE H	FF1	CS4 LAST READ DATA L
AH1	CS5 SYNC REC EN A H	BH1	BP DC LO L	CH1	BP SN 40 H	DH1	CS8 MR READ DATA H	FH1	GR PROM DIS L	FHI	CS4 P EN EBL H
AJ1	CS5 SYNC REC EN B H	BJ1	CS2 EN HEAD H	CJ1	BP SN 80 H	DJ1	DS7 WRITE DATA L	EJ1	CS8 WRT PROT H	FJ1	CS5 READ GATE FAST H
AK1	CS5 TRANSMIT EN A H	BK1	(NOT USED) IF8 DECODE L	CK1	BP SN 100 H	DK1	IF6 EN REG TO TIST H	EK1	CS9 SEEK ERR L	FK1	CS4 P READ GATE H
AL1	CS5 TRANSMIT EN B H	BL1	IF9 SEQ SKIP L	CL1	BP SN 200 H	DL1	SPARE	EL1	CS4 POWER OK H	FL1	IF4 TEST BIT CLOCK H
AM1	CS5 TRNSCEIVR EN A H	BM1	CS2 P EN SEARCH H	CM1	BP SN 400 H	DM1	IF8 OCCUPIED L	EM1	RDI -5V	FM1	CS4 P LFS H
AN1	CS5 TRNSCEIVR EN B H	BN1	CS5 SECTOR COMPARE H	CN1	BP SN 800 H	DN1	CS5 MBA EBL L	EN1	IF8 OFF DIR H	FN1	IF6 ASY WRT H
AP1	SPARE	BP1	CS3 SECT/INDEX L	CP1	BP SN 1000 H	DP1	IF4 SEQ CLK H	EP1	CS4 PRECLEAR CRC/ECC L	FP1	CS4 P EN CRC OUT H
AR1	CS6 NEW TRACK REQ'D L	BR1	DS11 TA 8 H	CR1	BP SN 2000 H	DR1	IF3 REQ A H	ER1	CS5 READ GATE H	FR1	CS8 INDEX H
AS1	CS1 SEQ CLK STROBE H	BS1	DS11 TA 16 H	CS1	BP SN 4000 H	DS1	IF4 REQ B H	ES1	SPARE	FS1	CS4 P EN ECC OUT H
AT1	GND	BT1	GND	CT1	GND	DT1	GND	ET1	GND	FT1	GND
AU1	CS3 PWR/INIT L	BU1	IF3 PORT A ON H	CU1	BP SN 8000 H	DUI	CS5 EN ECC CLOCK L	EU1	CS4 P EN LOAD SR H	FUI	RDI START ENABLE H
AV1	CS2 TAG H	BV1	IF4 PORT B ON H	CV1	SPARE	DVI	CS6 MB EXCEPTION L	EVI	CS4 DS WRITE GATE H	FVI	SPARE

AA2	+5V	BA2	+5V	CA2	+5V	DA2	+5V	EA2	+5V	FA2	+5V
AB2	-15V	BB2	-15V	CB2	-15V	DB2	-15V	EB2	-15V	FB2	-15V
AC2	GND	BC2	GND	CC2	GND	DC2	GND	EC2	GND	FC2	GND
AD2	CS1 RESET H	BD2	DS12 DC 1 H	CD2	CS4 EN SYNC H	DD2	IF2 CONT 00	ED2	IF6 INIT A/B L	FD2	CS5 DTE SET L
AE2	CS1 BRANCH H	BE2	DS12 DC 2 H	CE2	IF5 RM LA H	DE2	IF2 CONT 09	EE2	CS8 DIAG H	FE2	DS12 LST SECT TRK H
AF2	CS10 SC 1 H	BF2	DS12 DC 4 H	CF2	IF8 FMT 16 H	DF2	IF2 CONT 10	EF2	CS8 DIAG L	FF2	DS11 LST SECT H
AH2	CS10 SC 2 H	BH2	DS12 DC 8 H	CH2	DS1 SYNC DET H	DH2	IF2 CONT 11	EH2	IF8 F0 H	FH2	IF11 LSC L
AJ2	CS10 SC 4 H	BJ2	DS12 DC 16 H	CJ2	DS4 WORD CLK H	DJ2	IF2 CONT 12	EJ2	IF8 F1 H	FJ2	CS2 FORMAT H
AK2	CS10 SC 8 H	BK2	DS12 DC 32 H	CK2	DS4 WORD CLK L	DK2	IF2 CONT 13	EK2	IF8 F2 H	FK2	CS4 P EN CRC L
AL2	CS10 SC 16 H	BL2	DS12 DC 64 H	CL2	DS4 PROM STROBE H	DL2	IF2 CONT 14	EL2	IF8 F3 H	FL2	CS6 RUN AND GO H
AM2	SPARE	BM2	DS12 DC 128 H	CM2	IF2 CONT 00	DM2	IF2 CONT 15	EM2	IF8 F4 H	FM2	CS2 LSC SET L
AN2	SPARE	BN2	DS12 DC 256 H	CN2	IF2 CONT 01	DN2	CS8 UNIT READY H	EN2	IF8 GO H	FN2	CS9 OPT CYL L
AP2	SPARE	BP2	DS12 DC 512 H	CP2	IF2 CONT 02	DP2	DS11 TA1 H	EP2	CS3 RESET GO L	FP2	CS2 EN EHC H
AR2	SPARE	BR2	DS4 EN CRC OUT H	CR2	IF2 CONT 03	DR2	DS11 TA2 H	ER2	DS11 SA 1 H	FR2	CS2 SET IVC H
AS2	CS4 BR RD TO WRT L	BS2	DS4 EN ECC OUT H	CS2	IF2 CONT 04	DS2	DS11 TA4 H	ES2	DS11 SA 2 H	FS2	CS12 GRANT OUT L
AT2	CS4 P WRITE GATE H	BT2	CS4 READ HEADER H	CT2	IF2 CONT 05	DT2	CS3 TA CLR L	ET2	DS11 SA 4 H	FT2	BP GRANT IN L
AU2	DS10 EN CORRECT CLK L	BU2	CS2 P SET ATA L	CU2	IF2 CONT 06	DU2	SA2 REC EXC H	EU2	DS11 SA 8 H	FU2	CS12 SIP L
AV2	CS4 BRANCH L	BV2	BP PROM GROUND	CV2	IF2 CONT 07	DV2	CS5 EN CRC CLOCK L	EV2	DS11 SA 16 H	FV2	DS4 LCC WRD CNT INH L

I/O SIGNAL LIST

REVISIONS		
CHG	CHANGE NO.	REV.

TITLE	(CS13) CONTROL SEQUENCER	DATE CODE	DCS	NUMBER	M7684-01	REV.	S
SCALE		SHEET	13	OF 19			

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DEC PART NUMBER: 23-1550 ORIGINAL: IRENE BELLETIERE SHEET 1 OF 4

Table with 8 columns: DEC HEX OCT, OCT HEX BIN, DEC HEX OCT, OCT HEX BIN, DEC HEX OCT, OCT HEX BIN, DEC HEX OCT, OCT HEX BIN. Contains binary data for various control signals.

DEC PART NUMBER: 23-1550 ORIGINAL: IRENE BELLETIERE SHEET 2 OF 4

Table with 8 columns: DEC HEX OCT, OCT HEX BIN, DEC HEX OCT, OCT HEX BIN, DEC HEX OCT, OCT HEX BIN, DEC HEX OCT, OCT HEX BIN. Contains binary data for various control signals.

DEC PART NUMBER: 23-1550 ORIGINAL: IRENE BELLETIERE SHEET 3 OF 4

Table with 8 columns: DEC HEX OCT, OCT HEX BIN, DEC HEX OCT, OCT HEX BIN, DEC HEX OCT, OCT HEX BIN, DEC HEX OCT, OCT HEX BIN. Contains binary data for various control signals.

DEC PART NUMBER: 23-1550 ORIGINAL: IRENE BELLETIERE SHEET 4 OF 4

Table with 8 columns: DEC HEX OCT, OCT HEX BIN, DEC HEX OCT, OCT HEX BIN, DEC HEX OCT, OCT HEX BIN, DEC HEX OCT, OCT HEX BIN. Contains binary data for various control signals.

READ CONTROLS FROM AT EBI (CS4) TITLE 932 X 8 908/7700 PATTERN 808C 23-077

Table with 3 columns: CHK, CHAN. NO., REV.

Table with 4 columns: TITLE CONTROL SEQUENCER, DCS, NUMBER M7684-0-1, REV. S

D E S M7684-0-1 S

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DEC PART NUMBER: 23-01001 ORIGINAL: THESE BELLETTIERE DATE ORIGINATED: 5-4-77 SHEET 1 OF 4

Table with 12 columns: DEC HEX OCT, LOC LOC LOC, DAT DAT DAT, etc. for sheets 1 and 2.

DEC PART NUMBER: 23-01001 ORIGINAL: THESE BELLETTIERE DATE ORIGINATED: 5-4-77 SHEET 3 OF 4

Table with 12 columns: DEC HEX OCT, LOC LOC LOC, DAT DAT DAT, etc. for sheets 3 and 4.

DEC PART NUMBER: 23-01001 ORIGINAL: THESE BELLETTIERE DATE ORIGINATED: 5-4-77 SHEET 1 OF 4

Table with 12 columns: DEC HEX OCT, LOC LOC LOC, DAT DAT DAT, etc. for sheets 1 and 2.

DEC PART NUMBER: 23-01001 ORIGINAL: THESE BELLETTIERE DATE ORIGINATED: 5-4-77 SHEET 3 OF 4

Table with 12 columns: DEC HEX OCT, LOC LOC LOC, DAT DAT DAT, etc. for sheets 3 and 4.

COMMON READ/WRITE CONTROLS FROM # 72 (CS4) TITLE: 010 0 0 PERM/PROH PATTERNS NONE 23-01001

REVISIONS table with columns: CHG, CHANGE NO., REV.

CONTROL SEQUENCER (CS19) table with columns: TITLE, NUMBER, SCALE, SHEET, etc.

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DEC PART NUMBER: 23-01708 ORIGINATOR: IRENE BALLETTIERE DATE ORIGINATED: 3-6-77 SHEET 1 OF 4

Table with 10 columns: DEC HEX OCT, LOC LOC LOC, DAT DAT DAT, and 10 columns of data values.

DEC PART NUMBER: 23-01708 ORIGINATOR: IRENE BALLETTIERE DATE ORIGINATED: 3-6-77 SHEET 2 OF 4

Table with 10 columns: DEC HEX OCT, LOC LOC LOC, DAT DAT DAT, and 10 columns of data values.

DEC PART NUMBER: 23-01708 ORIGINATOR: IRENE BALLETTIERE DATE ORIGINATED: 3-6-77 SHEET 3 OF 4

Table with 10 columns: DEC HEX OCT, LOC LOC LOC, DAT DAT DAT, and 10 columns of data values.

DEC PART NUMBER: 23-01708 ORIGINATOR: IRENE BALLETTIERE DATE ORIGINATED: 3-6-77 SHEET 4 OF 4

Table with 10 columns: DEC HEX OCT, LOC LOC LOC, DAT DAT DAT, and 10 columns of data values.

WRITE CONTROL TAP AT E30 (CS4) TITLE: 010 & 0 PHL/PROM PATTERN MWE 23-01708

Table with 3 columns: REV, CHANGE NO., REV.

Table with 4 columns: TITLE (CONTROL SEQUENCER (CS16)), NUMBER (M7684-0-1), SCALE, SHEET (16 OF 19).

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DCS M7684-0-1

DEC PART NUMBER: 23-00000 ORIGINATOR: TRW BELLEVILLE DATE ORIGINATED: 5-3-77 BINARY DATA "1" = HIGH SHEET 1 OF 2

Table with columns: DEC HEX OCT, OCT HEX BIN, DEC HEX OCT, OCT HEX BIN, DEC HEX OCT, OCT HEX BIN, DEC HEX OCT, OCT HEX BIN. Rows 1-31.

DEC PART NUMBER: 23-00000 ORIGINATOR: TRW BELLEVILLE DATE ORIGINATED: 12-20-77 BINARY DATA "1" = HIGH SHEET 1 OF 2

Table with columns: DEC HEX OCT, OCT HEX BIN, DEC HEX OCT, OCT HEX BIN, DEC HEX OCT, OCT HEX BIN, DEC HEX OCT, OCT HEX BIN. Rows 1-31.

DEC PART NUMBER: 23-00000 ORIGINATOR: TRW BELLEVILLE DATE ORIGINATED: 5-3-77 BINARY DATA "1" = HIGH SHEET 2 OF 2

Table with columns: DEC HEX OCT, OCT HEX BIN, DEC HEX OCT, OCT HEX BIN, DEC HEX OCT, OCT HEX BIN, DEC HEX OCT, OCT HEX BIN. Rows 1-31.

DEC PART NUMBER: 23-00000 ORIGINATOR: TRW BELLEVILLE DATE ORIGINATED: 12-20-77 BINARY DATA "1" = HIGH SHEET 2 OF 2

Table with columns: DEC HEX OCT, OCT HEX BIN, DEC HEX OCT, OCT HEX BIN, DEC HEX OCT, OCT HEX BIN, DEC HEX OCT, OCT HEX BIN. Rows 1-31.

COMMENT SEQUENCER MISCELLANEOUS AT E47(C52)

TAG CONTROLS AT E39 (C52)

REVISIONS table with columns: ONE, CHANGE NO., REV.

TITLE CONTROL SEQUENCER (C517) DCS M7684-0-1

SCALE SHEET 17 OF 19

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DEC PART NUMBER: 21-01000 ORIGINATOR: IRENE BELLETTIERE DATE ORIGINATED: 8-25-77 BINARY DATA '1' = HIGH SHEET 1 OF 3

DEC PART NUMBER: 21-01000 ORIGINATOR: IRENE BELLETTIERE DATE ORIGINATED: 8-25-77 BINARY DATA '1' = HIGH SHEET 1 OF 3

DEC PART NUMBER: 21-01000 ORIGINATOR: IRENE BELLETTIERE DATE ORIGINATED: 8-25-77 BINARY DATA '1' = HIGH SHEET 2 OF 3

DEC PART NUMBER: 21-01000 ORIGINATOR: IRENE BELLETTIERE DATE ORIGINATED: 8-25-77 BINARY DATA '1' = HIGH SHEET 2 OF 3

SEQUENCER CONTROL PLOM AT E16 (CS1) TITLE 200 X 0 FOR/PAGE PATTERN 73-02200

BRANCH ADDRESS AT E33 (CS1) TITLE 200 X 0 FOR/PAGE PATTERN 23-01000

REVISIONS table with columns: REV, CHANGE NO., DATE, TITLE, CONTROL SEQUENCER, DCS, M7684-C-1, SHEET 18 OF 19, NUMBER, SCALE, SHEET, REV.

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DIC PART NUMBER: 23-20441 ORIGINATOR: INFA BELLESTIERE
 LEFT COLUMN OF BIN DATA IS MSB (PIN 80P2) DATE ORIGINATED: 4-29-77
 STANDBY DATA "1" = HIGH SHEET 1 OF 1
 STANDBY DATA "0" = LOW

DIC	HEX	OCT	DAT	HEX	OCT	DAT	DIC	HEX	OCT	DAT	HEX	OCT	DAT	DIC	HEX	OCT	DAT	DIC	HEX	OCT	DAT	
0	00	00	017	00	00001111																	
1	01	01	013	00	00010111																	
2	02	02	013	00	00011011																	
3	03	03	013	00	00010111																	
4	04	04	017	00	00011111																	
5	05	05	013	00	00010111																	
6	06	06	013	00	00010111																	
7	07	07	013	00	00010111																	
8	08	08	017	00	00011111																	
9	09	09	017	00	00011111																	
10	0A	0A	013	00	00010111																	
11	0B	0B	013	00	00010111																	
12	0C	0C	013	00	00010111																	
13	0D	0D	013	00	00010111																	
14	0E	0E	013	00	00010111																	
15	0F	0F	013	00	00010111																	
16	10	10	013	00	00010111																	
17	11	11	013	00	00010111																	
18	12	12	013	00	00010111																	
19	13	13	013	00	00010111																	
20	14	14	003	03	00000011																	
21	15	15	003	03	00000011																	
22	16	16	013	00	00010111																	
23	17	17	013	00	00010111																	
24	18	18	003	03	00000011																	
25	19	19	003	03	00000011																	
26	1A	1A	013	00	00010111																	
27	1B	1B	013	00	00010111																	
28	1C	1C	003	03	00000011																	
29	1D	1D	001	01	00000001																	
30	1E	1E	013	00	00010111																	
31	1F	1F	013	00	00010111																	

COMMAND DECODE AT E45 (CS2)

DIGITAL EQUIPMENT CORPORATION MAYFIELD, MASSACHUSETTS	
TITLE	23 2 0
	NON/FORM PATTERN SPEC
	23-20441

REVISIONS		
CHK	CHANGE NO.	REV.

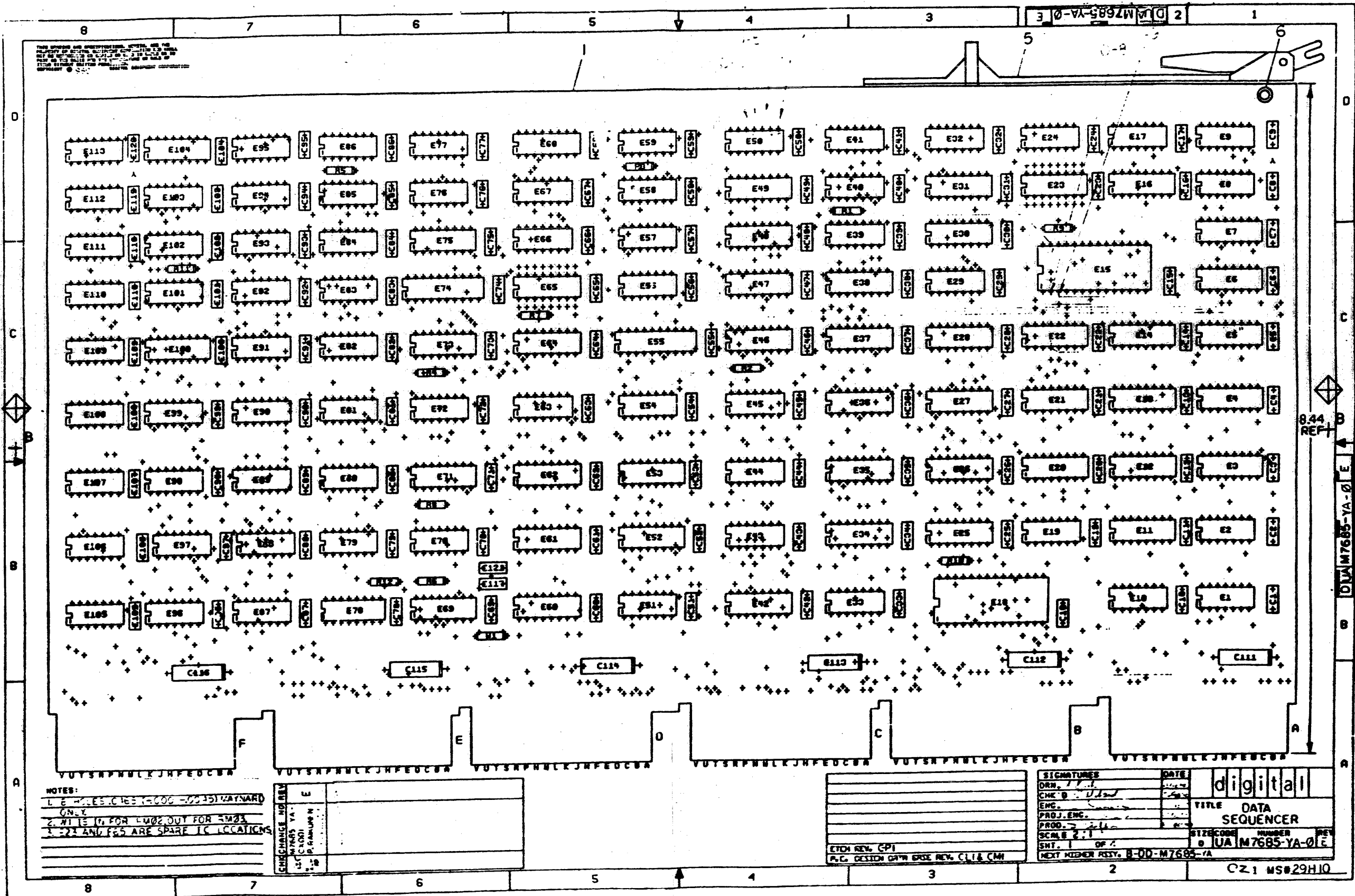
LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION	REFERENCE DESIGNATOR
1	D-MD-5012484-0-0	5012484-00	M7684	1	
2		1012784-00	.047 MFD 50V +80-20% CER	58	C1-C55,C58-C60
3		1005306-00	*** THIS ITEM IS NOT USED ***	-	
4		1005334-00	3.3MFD 20V 10% S.TANT	3	C103,C106,C115
5		1009964-00	.68 MFD 35V 10% S.TANT	1	C102
6		1000042-00	1000.0 MMF 100V 5X200PPM MICA	2	C56,C57
7		1001631-00	390.0 MMF 100V 5X200PPM MICA	1	C100
8		1100114-00	D 664 QS\75PCB PIV= 25V SP	4	D1,D2,D4,D6
9		1105275-00	D 672 TR= 15NS PIV= 60V SI	2	D3,D5
10		1216988-02	HANDLE,MODULE,HEX TWO EJECTORS	1	
11		1302602-00	56.0 .25 W 5.0 % CC	32	R30-R61
12		1300365-00	1.0 K .25 W 5.0 % CC	23	R11,R14-R29,R77,R78,R85-R88
13		1302388-00	2.0 K .25 W 5.0 % CC	10	R2-R6,R10,R90,R93,R94,R80
14		1302394-00	30.0 K .25 W 5.0 % CC	4	R7,R8,R91,R92
15		1313349-00	33.0 K .25 W 5.0 % CC	1	R1
16		1305346-00	27.0 K .25 W 5.0 % CC	1	R13
17		1304856-00	4.64 K .25 W 1.0 % RN55D-F10	16	R62-R76,R89
18		1301423-00	6.80 K .25 W 5.0 % CC	1	R79
19		1302666-00	*** THIS ITEM IS NOT USED ***	-	
20		1300439-00	3.30 K .25 W 5.0 % CC	1	R84
21		9000024-01	EYELET,ROLL FLANGE .1210DX .192	12	
22		2300888-00	B8-01	1	E47
23		1910091-00	DEC 7437 AND GATE-QUAD 2IN,BU	3	E1,E60,E83
24		1911219-00	7438 NAND GATE-QUAD 2IN,B	1	E85
25		1909928-00	7416 INVERTER GATE-HEX 1I	1	E97
26		1910224-00	DEC 7485 COMPARATOR-4BIT	1	E73
27		1910436-00	DEC 74123 ONE SHOT-DUAL,RETRIS	4	E25,E46,E71,E100
28		1910153-00	DEC 74150 MUX 1 OF 16	1	E64
29		1910268-01	DEC 75107B-01 RECEIVER,LINE,DUA	8	E2,E3,E4,E6,E8,E13,E14,E15
30		1911341-00	75113 DRIVER,LINE,DUAL,MA	7	E7,E10,E11,E12,E19,E21,E22

REVISION HISTORY		BASIC PART NO: M7684		DRN: JVV	DATE: 13-FEB-78	DIGITAL			
ENG	ECO NUMBER	REV	SECTION A OF A	CHK'D: RSW <td>DATE: 13-FEB-78 <td colspan="4">TITLE PARTS LIST</td> </td>	DATE: 13-FEB-78 <td colspan="4">TITLE PARTS LIST</td>	TITLE PARTS LIST			
IB	00008	K	SECTION.VARIATION INDEX			CONTROL SEQUENCER			
KIT	00009	L	[A] 00						
CL	00010	M	[B]						
--	00011	N	[C]	DES.ENG: I.BELLETTIER	DATE: 13-FEB-78				
BHF	ML012	P	[D]			DOCUMENT NUMBER			
WH	M7684-CX013	R	[E]	RESP.ENG.: I.BELLETTIERE	DATE: 13-FEB-78				
WH	M7684-CX014	S	[F]			SIZE	CODE	NUMBER	REV
WH			[H]	MFG.ENG.: J.MILLER	DATE: 13-FEB-78	K	PL	M7684-0-DBP	S
			[J]	ASSEMBLY NUMBER:	TOP DOCUMENT NUMBER:	FILE NAME:		EDIT #	
			[K]	D-UA-M7684-0-0	B-DD-M7684-0	Z1028S.PLS		27	
			[L]						
			[M]						
			[N]						

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LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION 00	REFERENCE DESIGNATOR
31	31		1911527-00	8097 BUFFER GATE-HEX 2INP	5	E17,E18,E26,E27,E28
32	32		1914087-00	8098 BUFFER GATE-HEX 2IN,	4	E40,E41,E50,E57
33	33		1910532-00	74S00 NAND GATE-QUAD 2IN	2	E65,E89
34	34		1912388-00	74S02 NOR GATE-QUAD 2IN,PO	1	E20
35	35		1910534-00	74S04 INVERTER GATE-HEX 1I	1	E53
36	36		1912389-00	74S08 AND GATE-QUAD 2IN,PO	2	E9,E68
37	37		1910537-00	74S11 AND GATE-TRIPLE 3INP	1	E75
38	38		1911712-00	74S51 AND-OR GATE-INVERT D	1	E66
39	39		1910544-00	74S74 FF-D DUAL,EDGE TRIGG	5	E24,E70,E82,E92,E93
40	40		1910545-00	74S112 FF-JK DUAL,EDGE TRIG	3	E23,E61,E99
41	41		1910548-00	74S157 MUX 1 OF 2 (QUAD)	1	E37
42	42		1914082-00	74S163 COUNTER,SYNCH UP/DOW	5	E31,E32,E80,E101,E102
43	43		1913340-00	74S32 OR GATE-QUAD 2IN	1	E84
44	44		1912847-00	LS157 MUX 1 OF 2(QUAD)	1	E35
45	45		1912799-00	LS00 NAND-GATE-QUAD 2IN,P	3	E5,E29,E77
46	46		1912803-00	74LS04 INVERTER GATE,HEX	2	E30,E87
47	47		1912805-00	LS08 AND GATE-QUAD 2IN,PO	5	E38,E44,E55,E76,E86
48	48		1912807-00	LS10 NAND GATE-TRIPLE 3IN	1	E94
49	49		1912808-00	LS11 AND GATE-TRIPLE 3IN	1	E54
50	50		1912810-00	LS20 NAND GATE-DUAL 4IN	1	E52
51	51		1912813-00	LS27 NOR GATE-TRIPLE 3IN	2	E79,E96
52	52		1912816-00	LS32 OR GATE-QUAD 2IN,POS	1	E56
53	53		1912824-00	LS74 FF-D DUAL,EDGE TRIGG	6	E36,E62,E63,E67,E69,E78
54	54		1912853-00	LS175 FF-D QUAD	4	E42,E43,E51,E59
55	55		1911944-00	555CN TIMER,FUNCT.BLOCK	1	E98
56	56		23017D1-00	D1-02	1	E90
57	60		1912859-00	LS258 MUX 1 OF 2 (DUAL),	4	E34,E48,E49,E58
58	58		1912862-00	LS266 X-NOR GATE-QUAD,OPN,	1	E74
59	59		23022B8-00	B8-01	1	E16
60	60		23016D1-00	D1-02	1	E72
61	61		23204A1-00	A1-03,A1-04,A1-05	1	E45
62	62		1209941-11	*** THIS ITEM IS NOT USED ***	-	
63	63		23015D1-00	D1-02	1	E81
64	64		1909686-00	7404 INVERTER GATE-HEX 1I	1	E88
65	65		1214224-00	RLY,REED, 15V COIL,SPDT	1	E91
66	66		9105740-55	*** THIS ITEM IS NOT USED ***	-	
67	67		9107256-11	*** THIS ITEM IS NOT USED ***	-	
68	68		23020B8-00	B8-01	1	E33
69	69		23023B8-00	B8-01	1	E39
70	70		1214413-00	RLY,REED, 5V COIL,SPST	1	E95
71	71		9009185-00	JUMPER, WIRE, INSULATED, BLACK B	1	W1
72	72		1005965-00	*** THIS ITEM IS NOT USED ***	-	
73	73		1001610-00	.01 MFD 50V +80-20% Z5U CER	1	C107
74	74		1017472-00	10 MFD 35V +50-10% AL EL	8	C101,C108-C114
75	75		1312934-00	6.80 M .25 W 5.0 Z CC	1	R81
76	76		1216832-04	HEADER 60POS WITH LATCHES	1	J1

D	I	B	I	T	A	L	TITLE	CONTROL SEQUENCER	SECTION A OF A	SIZE	CODE	DOCUMENT NUMBER	REV
										K	PL	M7684-0-DBP	S



NOTES:
 1. E-HC92ES, C165, Y-C006 -00351 WATNARD ONLY.
 2. W1 IS 10, FOR L402 OUT FOR 4M23.
 3. 721 AND 725 ARE SPARE IC LOCATIONS.

CHANGE NO.	REV.	DATE	BY	CHK.

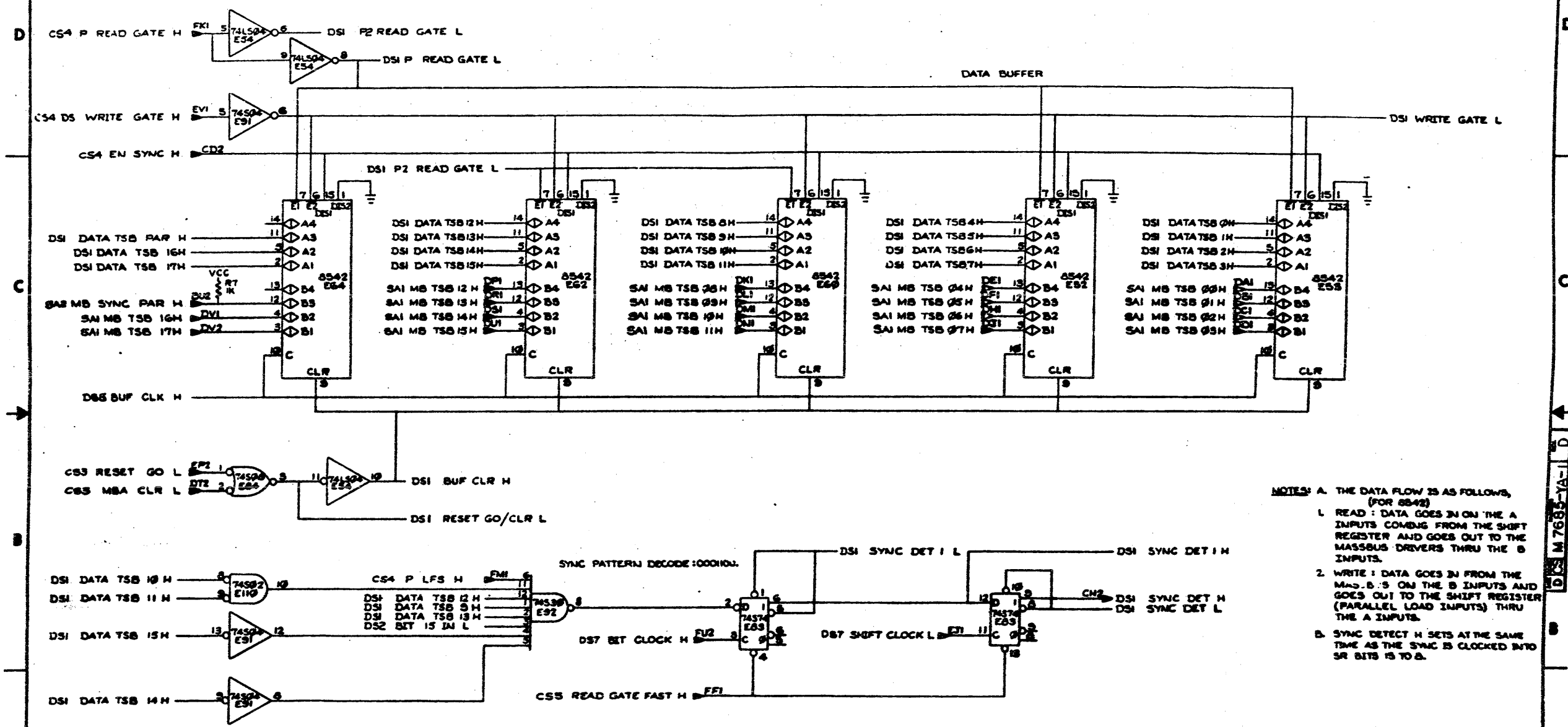
ETON REV. CPI
P.C. DESIGN DRN ERSE REV. C11 & C14

SIGNATURES	DATE
DRN: [Signature]	[Date]
CHK: [Signature]	[Date]
ENGR: [Signature]	[Date]
PROJ. ENG.:	
PROD.:	
SCM: [Signature]	[Date]
SMT: [Signature]	[Date]
NEXT HIGHER REV. B-DD-M7685-YA	

digital
TITLE DATA SEQUENCER
SIZE CODE CZ 1
NUMBER UA M7685-YA-01E
REV

CZ 1 MS# 29H10

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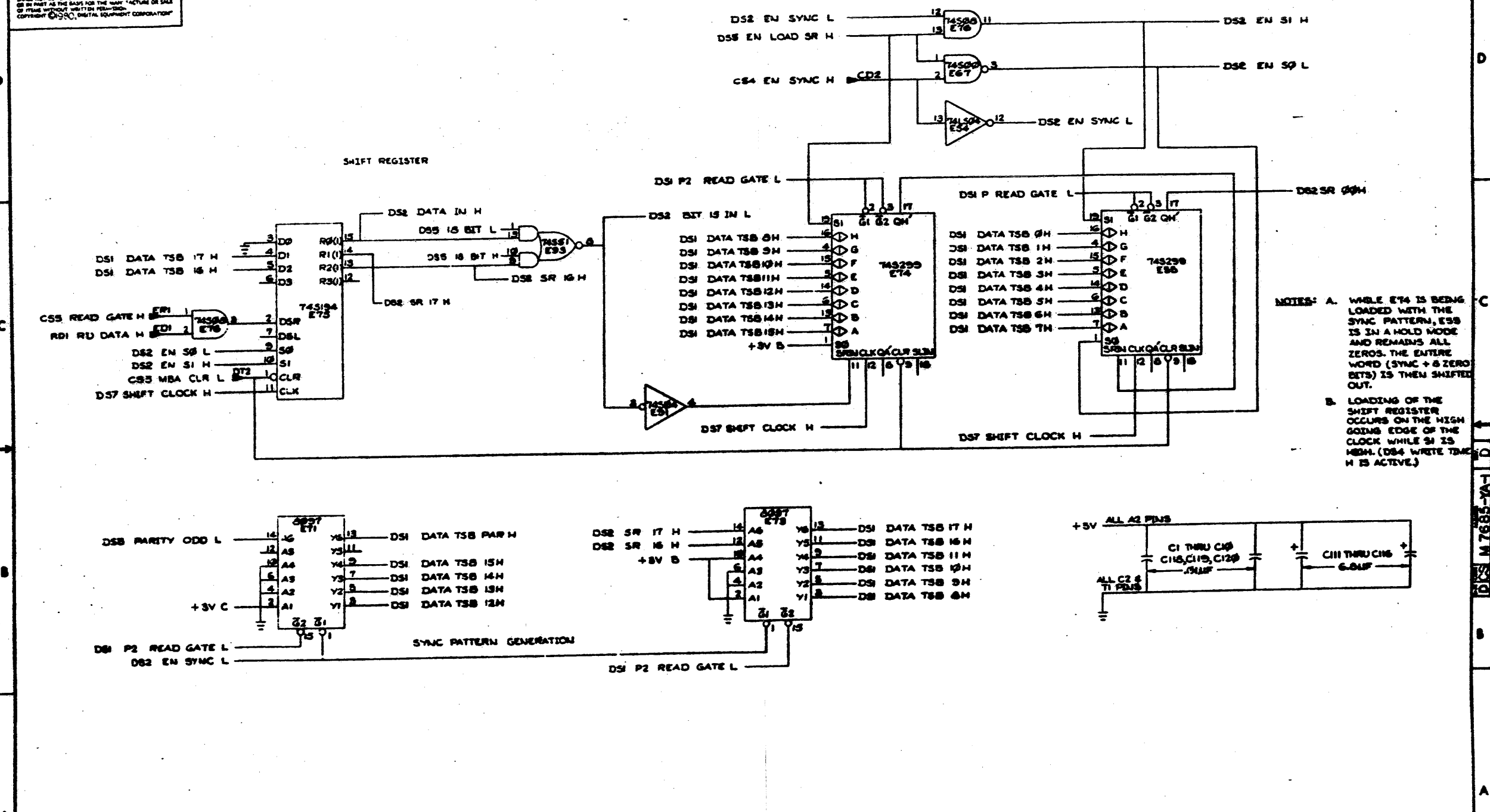
- NOTES: A. THE DATA FLOW IS AS FOLLOWS, (FOR 8542)
1. READ : DATA GOES IN ON THE A INPUTS COMING FROM THE SHIFT REGISTER AND GOES OUT TO THE MASSBUS DRIVERS THRU THE B INPUTS.
 2. WRITE : DATA GOES IN FROM THE MASSBUS ON THE B INPUTS AND GOES OUT TO THE SHIFT REGISTER (PARALLEL LOAD INPUTS) THRU THE A INPUTS.
 3. SYNC DETECT H SETS AT THE SAME TIME AS THE SYNC IS CLOCKED INTO SR BITS 15 TO 8.

DATA BUFFER AND SYNC DETECT

DATE: 11/10/70	DESIGNER: RMO/5	REV: 1
CHKD: 11/10/70	TITLE: DATA SEQUENCER (DS)	REV: 1
PROJ ENG: 11/10/70	DATE: 11/10/70	REV: 1
FRONT: 11/10/70	DATE: 11/10/70	REV: 1
NEXT NUMBER: 11/10/70	DATE: 11/10/70	REV: 1
FILE NO: M7685-YA	REV: 1	REV: 1
SCALE: 1	REV: 1	REV: 1
SHEET: 2	REV: 1	REV: 1
of 15	REV: 1	REV: 1
REV: 1	REV: 1	REV: 1

8 7 6 5 4 3 2 1

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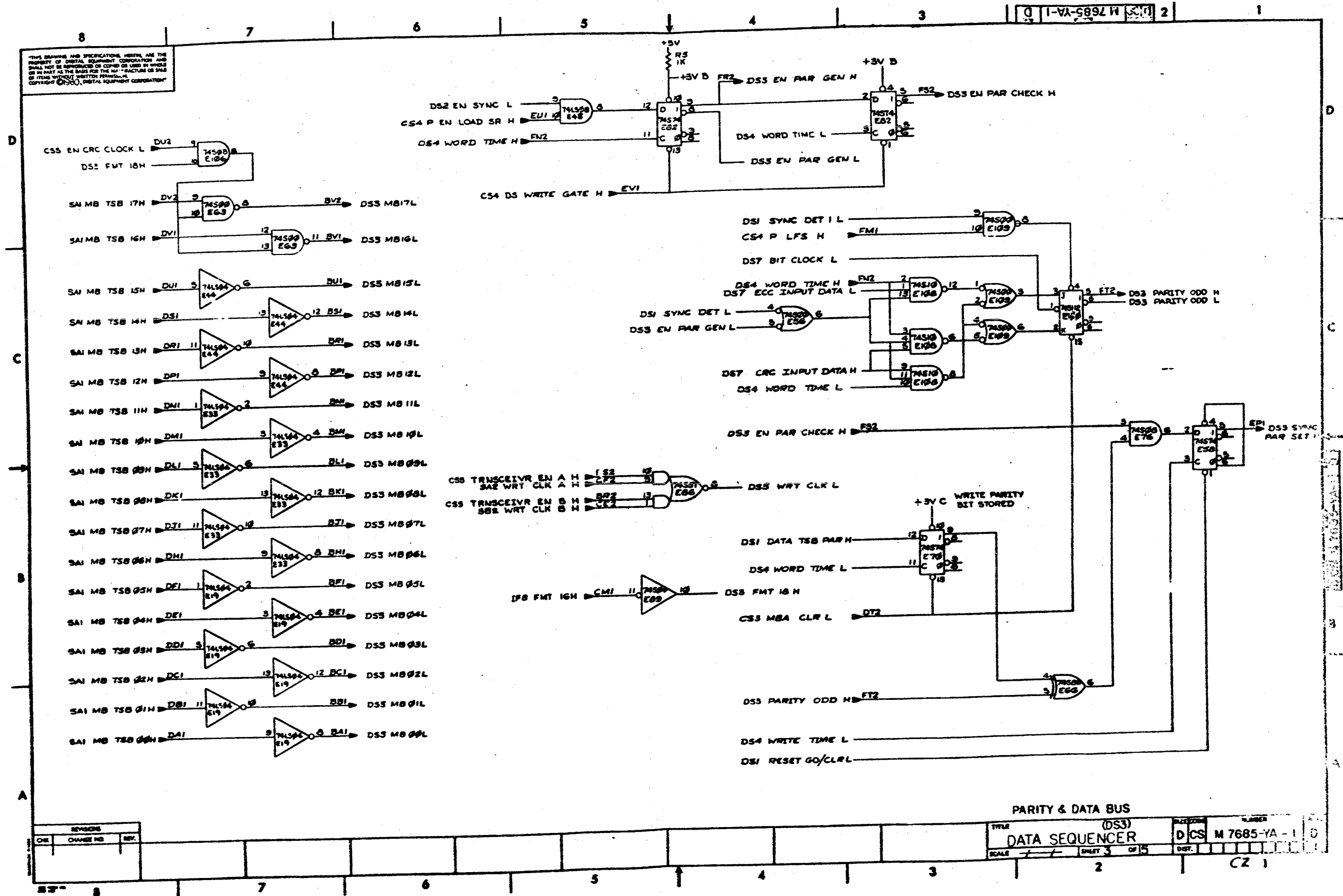
NOTES: A. WHILE E74 IS BEING LOADED WITH THE SYNC PATTERN, E76 IS IN A HOLD MODE AND REMAINS ALL ZEROS. THE ENTIRE WORD (SYNC + 8 ZERO BITS) IS THEN SHIFTED OUT.
 B. LOADING OF THE SHIFT REGISTER OCCURS ON THE HIGH GOING EDGE OF THE CLOCK WHILE S1 IS HIGH. (DS4 WRITE TIME H IS ACTIVE.)

REVISIONS		
ONE	CHANGE NO.	REV.

SHIFT REGISTER AND SYNC PATTERN GENERATION (WRITE)		TITLE (DS2)		DATE CODE		NUMBER		REV.	
DATA SEQUENCER		DCS M 7685-YA-1		D		CZ 1			
SCALE 1/1		SHEET 2 OF 15							

D-CS-M7685-YA-1 8 of 15

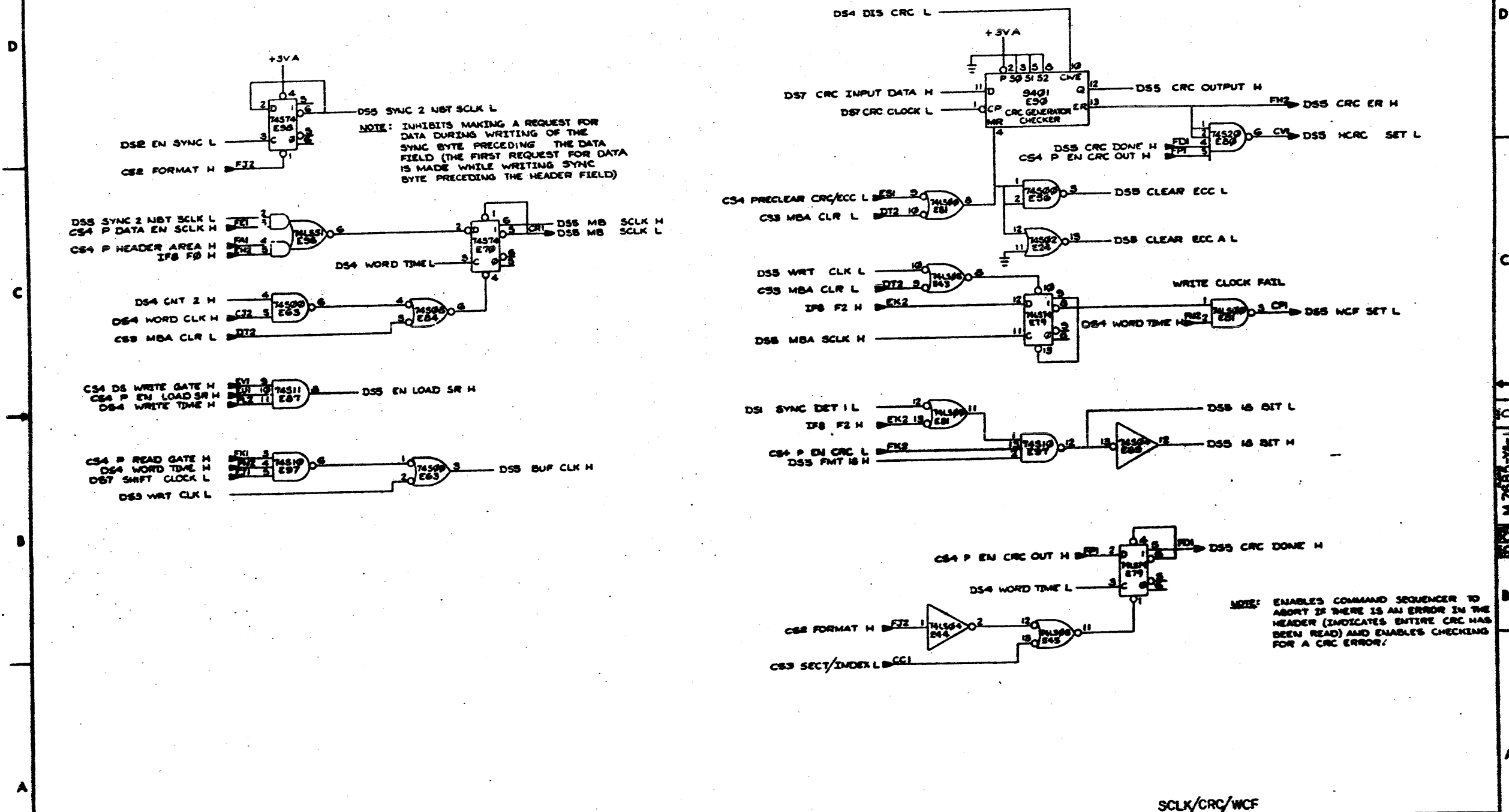
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REVISIONS		
CHK	CHANGE NO.	REV.

TITLE		NUMBER	
(DS3) DATA SEQUENCER		DCS M 7685-YA-1 0	
SCALE	SHEET	OF	DIST.
1	3	15	[blank]
CZ 1			[blank]

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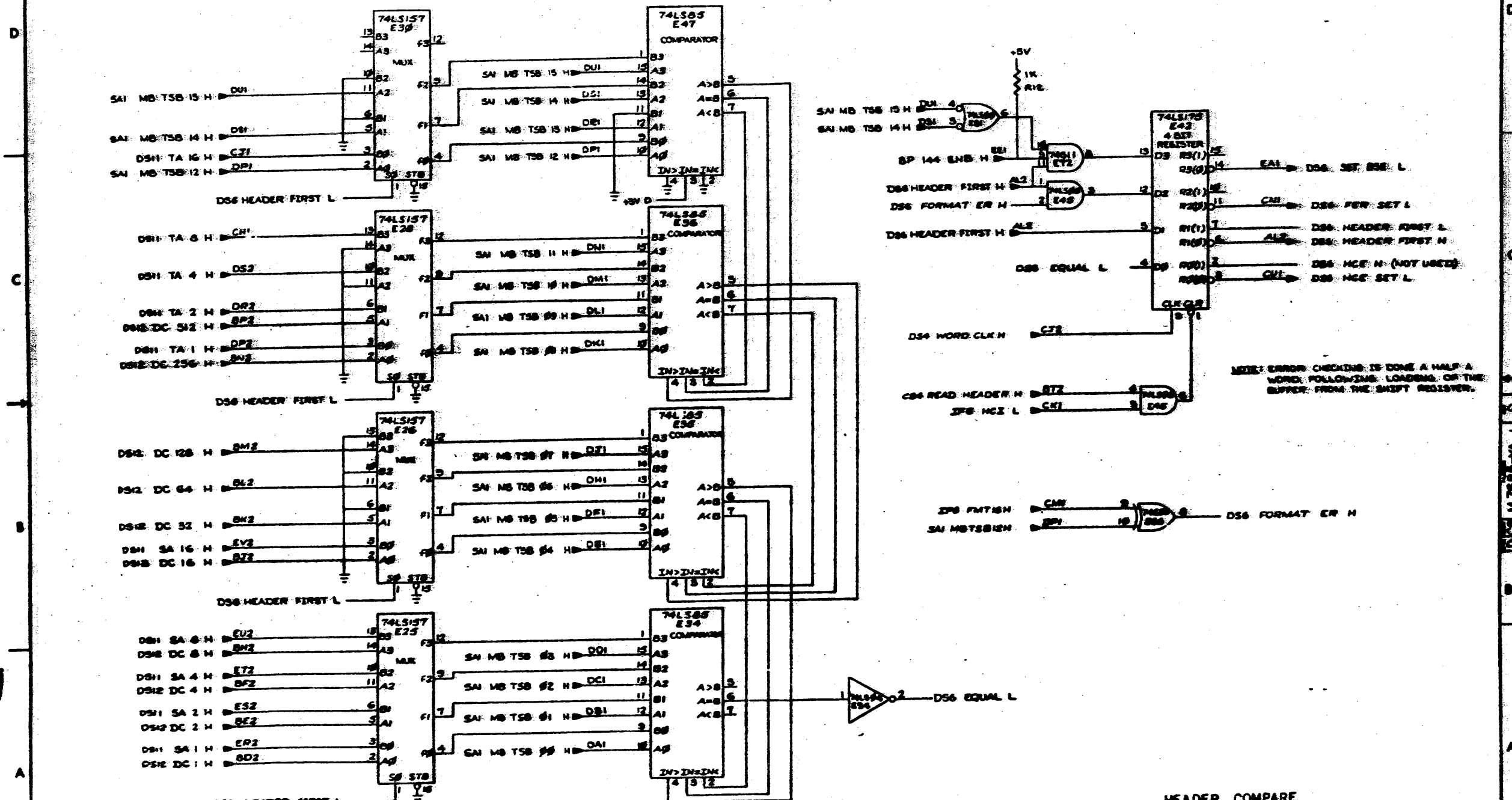


NOTE: INHIBITS MAKING A REQUEST FOR DATA DURING WRITING OF THE SYNC BYTE PRECEDING THE DATA FIELD (THE FIRST REQUEST FOR DATA IS MADE WHILE WRITING SYNC BYTE PRECEDING THE HEADER FIELD)

NOTE: ENABLES COMMAND SEQUENCER TO ABORT IF THERE IS AN ERROR IN THE HEADER (INDICATES ENTIRE CRC HAS BEEN READ) AND ENABLES CHECKING FOR A CRC ERROR.

REVISED													TITLE		DRAWING NUMBER		REV.	
CHK	CHANGE NO.	REV.											DCS M 7685-YA-1 D		CZ 1			

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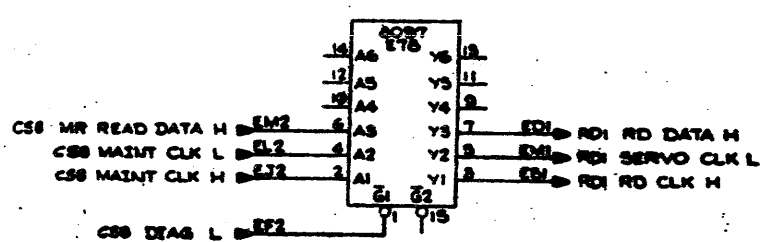
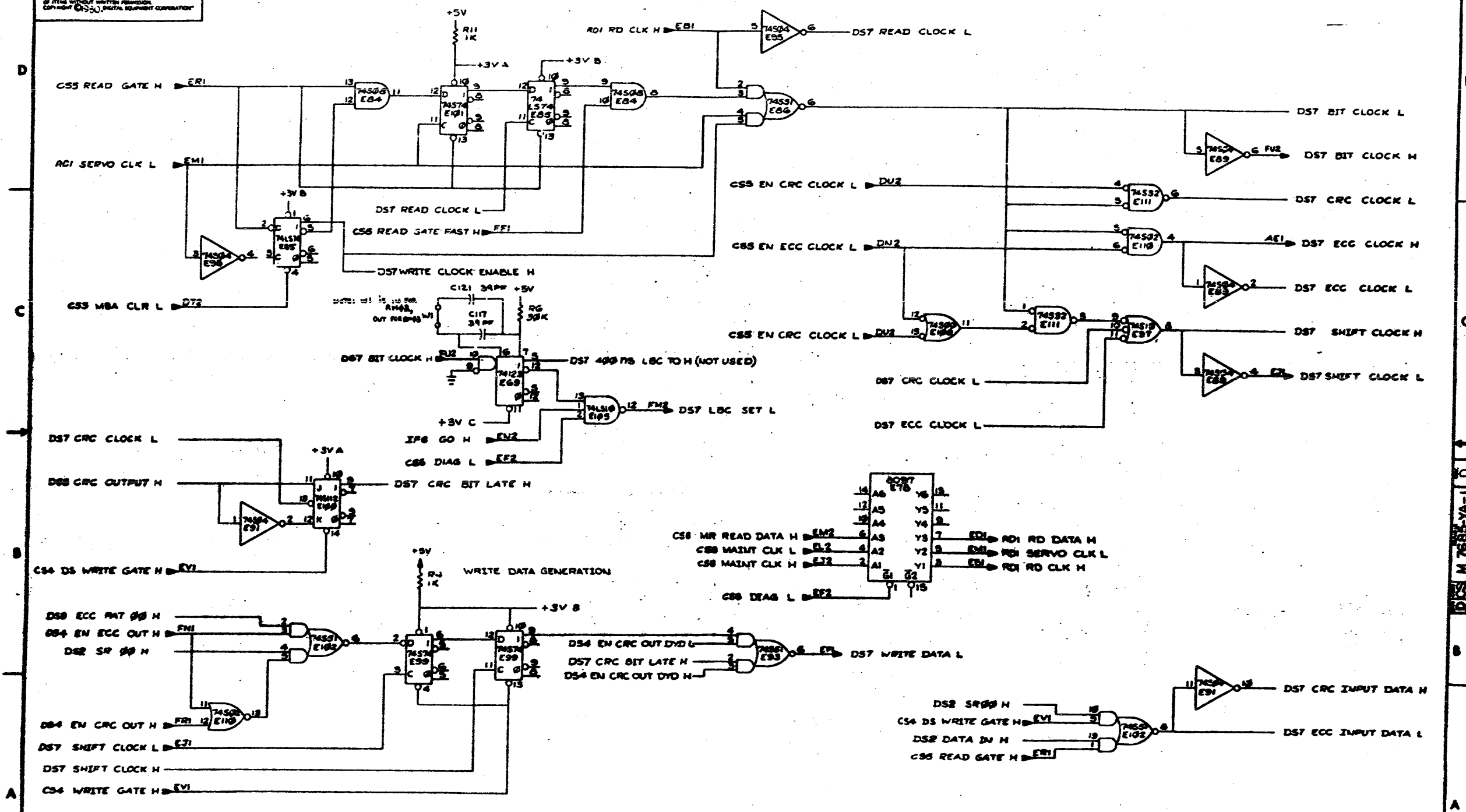


REV	DATE	BY

TITLE		D-1-VA-9891M	
DSS DATA SEQUENCER		D-1-VA-9891M 2	
SCALE	1	SHEET	6 OF 15
DATE		DESIGNER	CZ 1

D-1-VA-9891M 2
6 OF 15
D-CS-M7685-YA-1

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REV	DATE	CHANGE NO.	BY

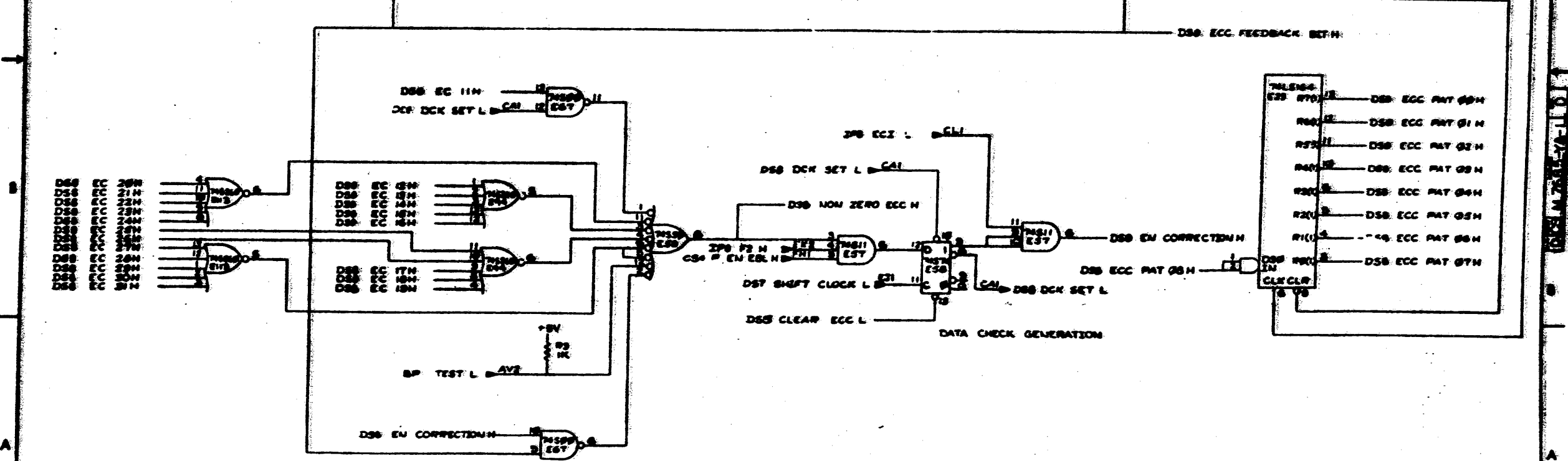
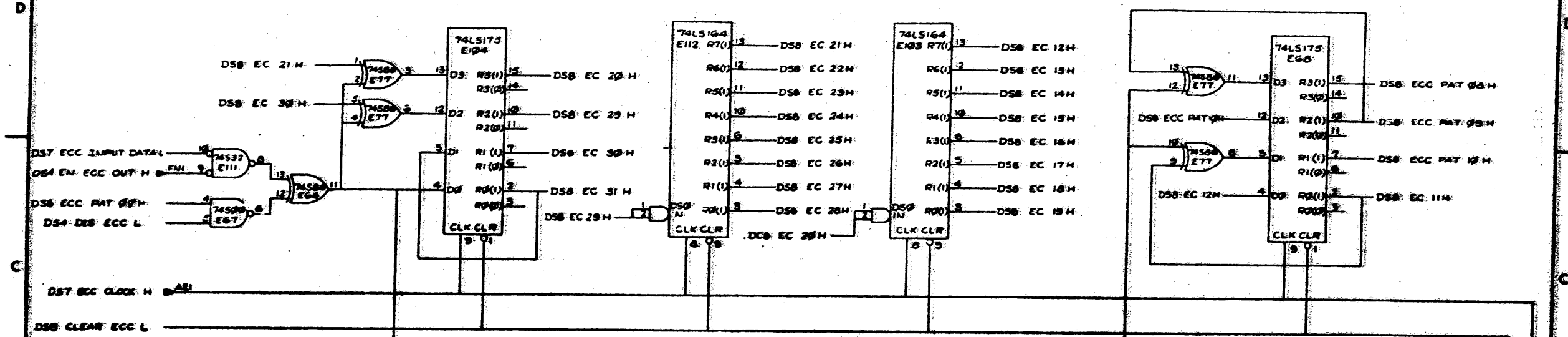
BIT CLOCK GENERATION AND WRITE DATA

TITLE	(DS7) DATA SEQUENCER	DATE	1971	REV.	C
SCALE	1:1	SHEET	7 OF 15	DES.	

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D 1-74-5892 M 2

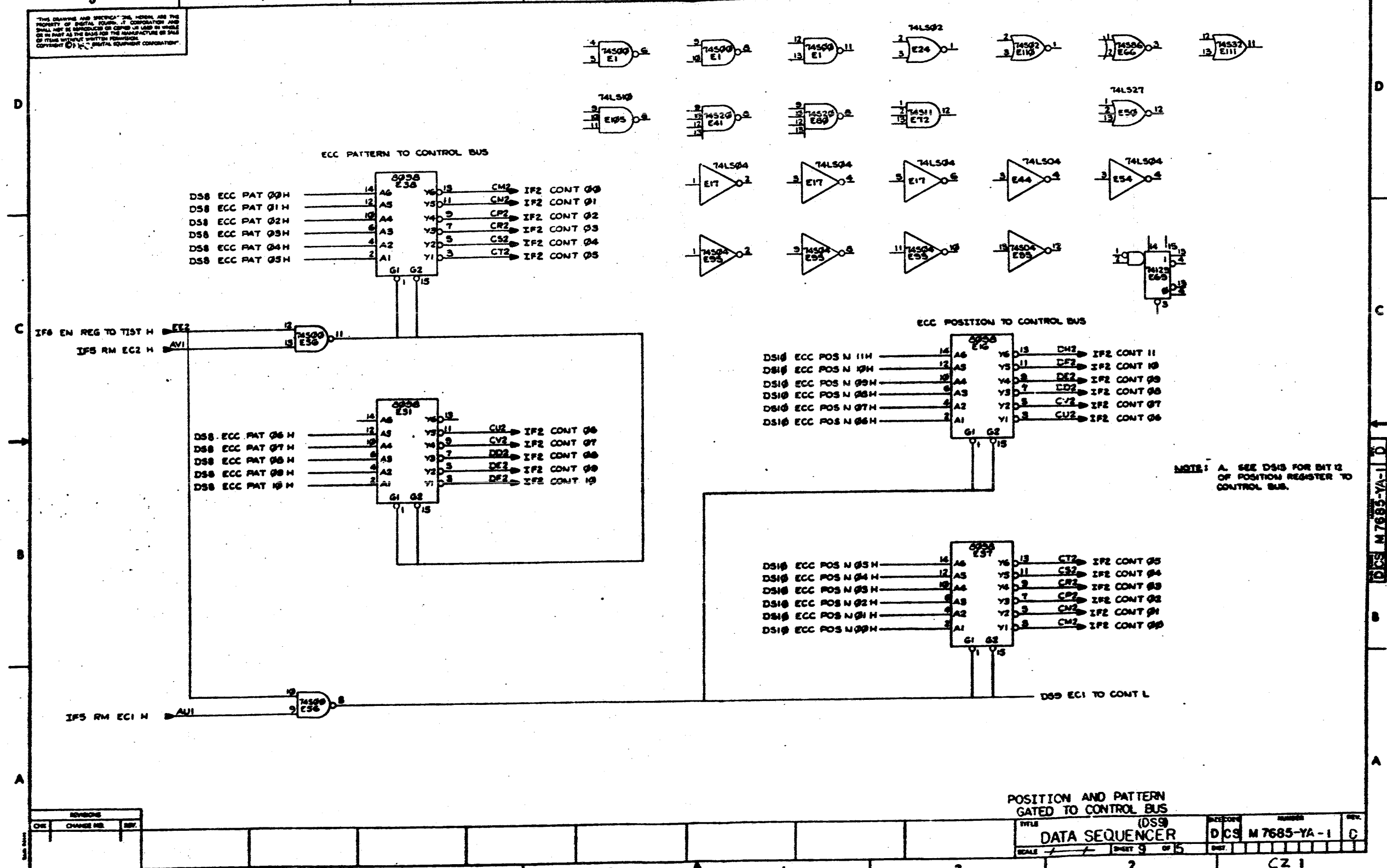
ECC PATTERN REGISTER



REVISED		
DATE	CHANGE NO.	BY

ECC PATTERN REGISTER AND DCK GENERATION
 TITLE (DSB) DATA SEQUENCER
 SCALE 1/8" = 1" SHEET 8 OF 15
 DCS M 7685-YA-1 D
 CZ 1

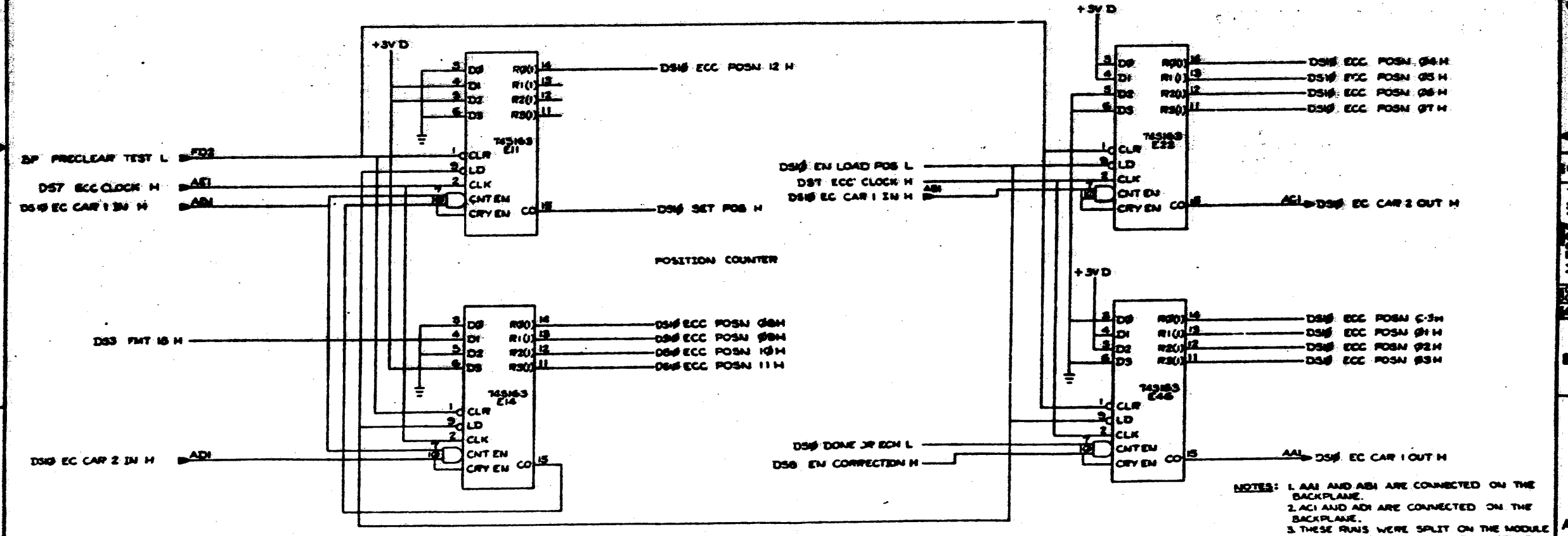
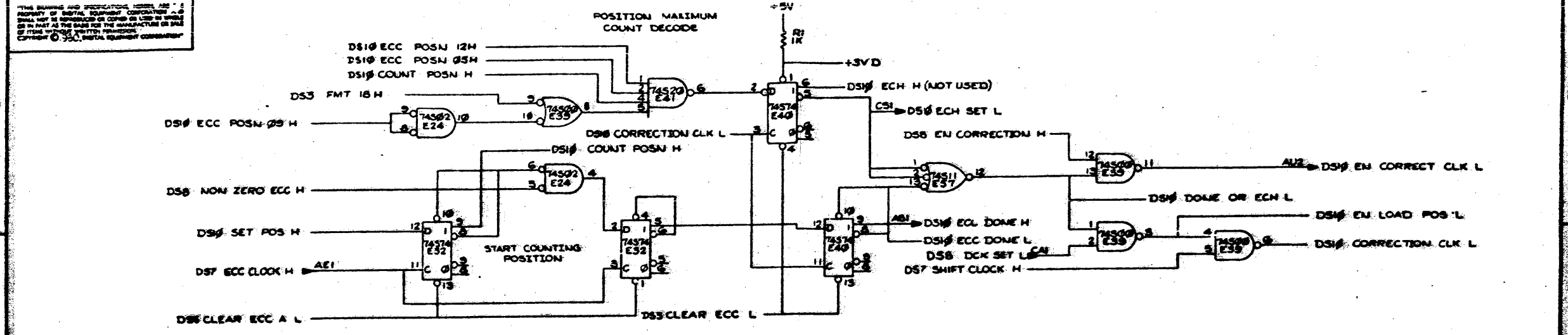
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NOTE: A. SEE DS45 FOR BIT 12 OF POSITION REGISTER TO CONTROL BUS.

REV.	CHANGE NO.	REV.

TITLE		DCS M7685-YA-1		REV.
DATA SEQUENCER		DCS M7685-YA-1		C
SCALE	SHEET	OF		REV.
	9	15		
CZ 1				

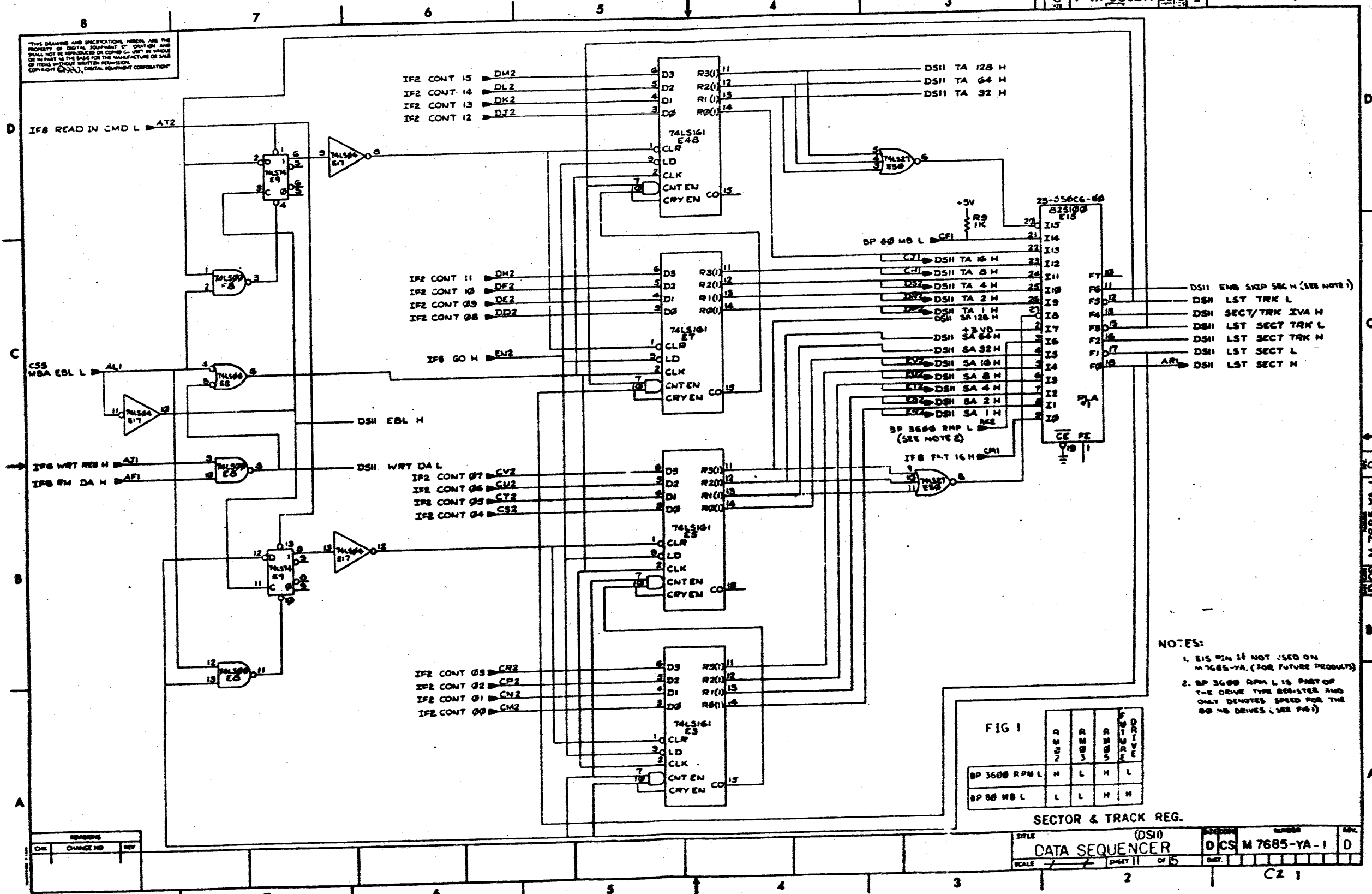


NOTES: 1. AA1 AND AB1 ARE CONNECTED ON THE BACKPLANE.
 2. AC1 AND AD1 ARE CONNECTED ON THE BACKPLANE.
 3. THESE RUNS WERE SPLIT ON THE MODULE TO SPEED UP TESTING OF THE MODULE.

POSITION COUNTER AND CORRECTION CONTROL LOGIC

REVISED	DATE	BY	CHK	APP	REV	DATE	BY	CHK	APP	REV

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- NOTES:
1. E15 PIN IS NOT USED ON M7685-YA. (FOR FUTURE PRODUCTS)
 2. BP 3600 RPM L IS PART OF THE DRIVE TYPE REGISTER AND ONLY DENOTES SPEED FOR THE 60 MB DEVICES (SEE PG 1)

FIG 1

	A	B	C	D
BP 3600 RPM L	H	L	H	L
BP 60 MB L	L	L	H	H

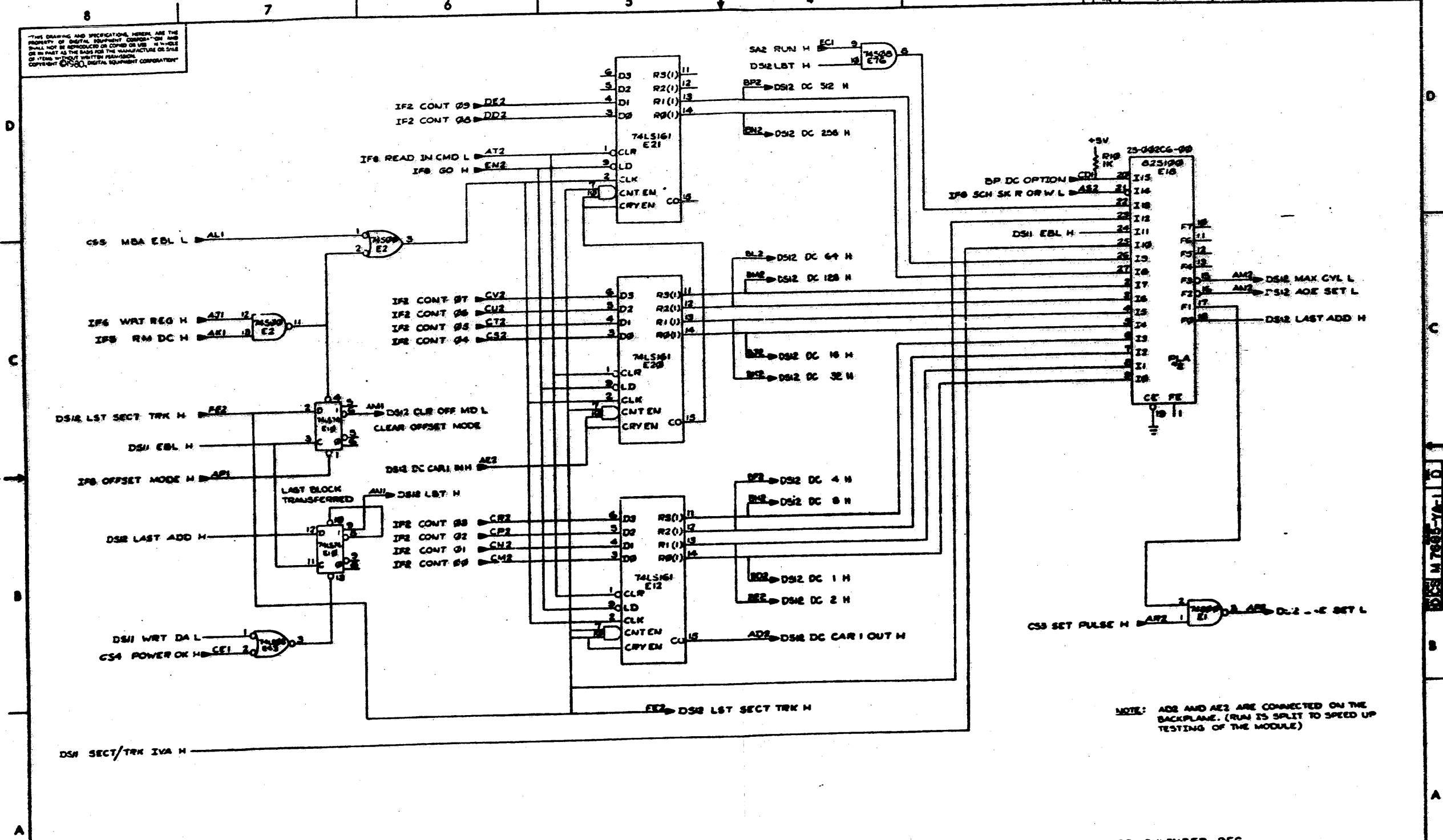
SECTOR & TRACK REG.

REV	CHANGE NO	REV

TITLE	SECTION	NUMBER	REV.
(DS1) DATA SEQUENCER	DCS	M7685-YA-1	D
SCALE	SHEET	OF	
	11	15	

CZ 1

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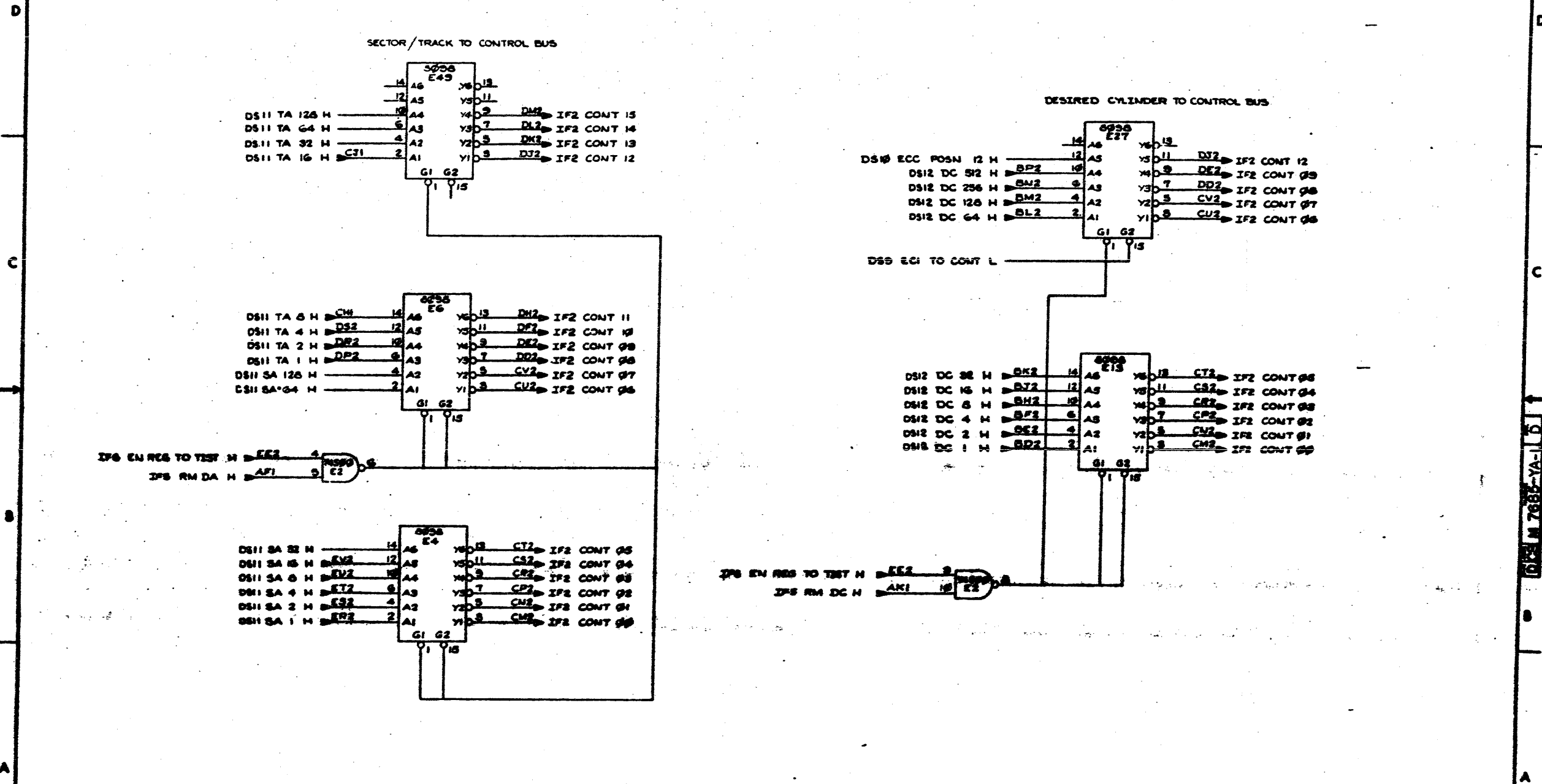


NOTE: AD2 AND AE2 ARE CONNECTED ON THE BACKPLANE. (RUM IS SPLIT TO SPEED UP TESTING OF THE MODULE)

REVISE		REV.		REV.		REV.	
TITLE			DESIGN		PART		REV.
DATA SEQUENCER (DS2)			DCS		M 7685-YA-1		D
SCALE			SHEET		OF		
			12		15		
			CZ		1		

REV.	CHANGE NO.	DATE

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REVISIONS		
CHK	CHANGE NO.	REV.

SECT/TRACK/DC GATED TO CONTROL BUS

TITLE (DS13) DATA SEQUENCER DCS M7685-YA-1 D

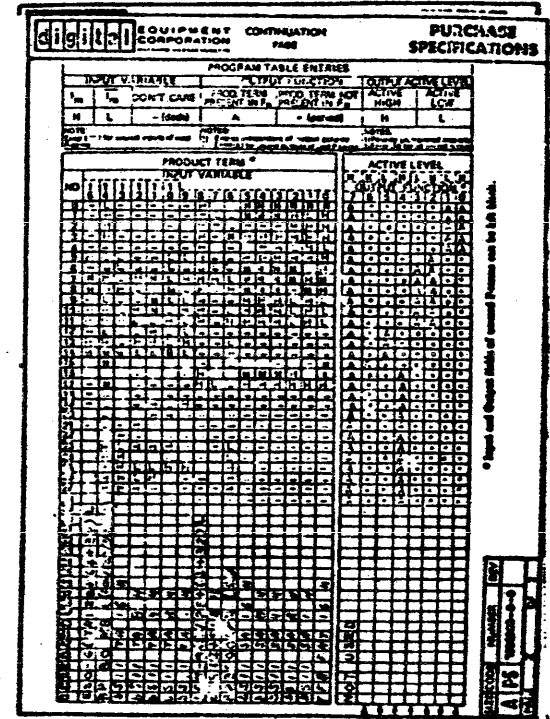
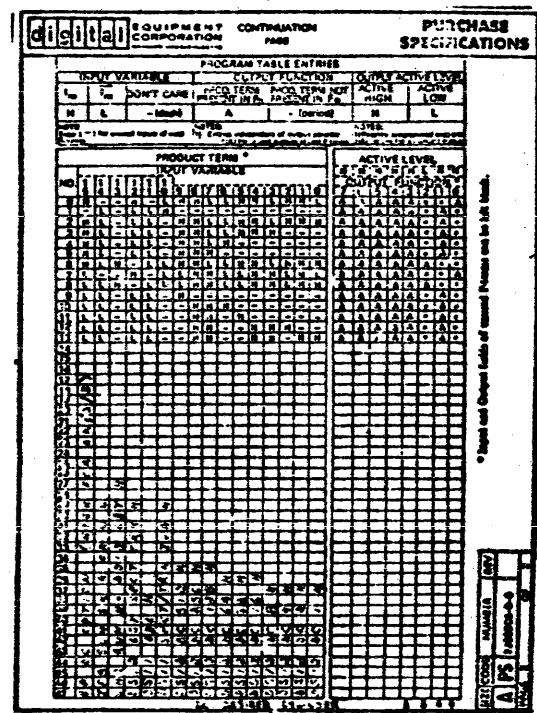
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Table with 6 columns of signal names and descriptions. Signals include AA1 through AV1, BA1 through BV1, CA1 through CV1, DA1 through DV1, EA1 through EV1, and FA1 through FV1.

Table with 6 columns of signal names and descriptions. Signals include AG2 through AV2, BG2 through BV2, CG2 through CV2, DG2 through DV2, EG2 through EV2, and FG2 through FV2.

IO SIGNAL LIST (DSM) DATA SEQUENCER DCS M 7685-YA-1 D. Includes a grid for signal names and a title block with drawing information.

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23-00206: FPLA AT E18
CYLINDER ADDRESS DECODE

23-050C6 FPLA AT E15
TRACK/SECTOR ADDRESS DECODE

0512 LAST ADDR H (PIN 12)
0511 ADDR H (PIN 11)
0510 ADDR L (PIN 10)
0509 ADDR L (PIN 9)

0512 LAST ADDR H (PIN 12)
0511 ADDR H (PIN 11)
0510 ADDR L (PIN 10)
0509 ADDR L (PIN 9)
0508 END SKIP SEC H (NOT USED ON M7685-YA, FOR FUTURE PRODUCTS) (PIN 8)

IF ALWAYS HIGH FOR M7685-YA, THIS PIN IS FOR FUTURE PRODUCTS.
IF BP 3600 RPM L IS PART OF THE DRIVE TYPE BBS AND ONLY DENOTES SPEED FOR 60 MB DRIVES (SEE FIG 1).

FIG 1

	R	R	R	F
	M	M	M	U
	B	B	B	T
	2	3	5	R
				V
				E
BP 3600 RPM L	H	L	H	L
BP 60 MB L	L	L	H	H

REVISIONS		
CHK	CHANGE NO.	REV.

DCS M7685-YA-1 D 15 of 15

LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION YA	REFERENCE DESIGNATOR
1	1	D-MD-5012485-0-0	5012485-00	M7685	1	
2	2		1001610-01	.01 MFD 100V -20+80 Z5U DISC	113	C1-C110,C118-C120
3	3		1017472-00	10 MFD 35V +50-10% AL EL	6	C111-C116
4	4		1000010-00	39.0 MMF 100V 5%200PPM DM15S	2	C117,C121
5	5		1210711-02	HANDLE,MODULE,HEX	1	
6	6		9000024-01	EYELET, ROLLED FLANGE, .121 OD X	12	
7	7		1300365-00	1.0 K .25 W 5.0 % CC	11	R1-R5,R7,R8,R9,R10,R11,R12
8	8		1302394-00	30.0 K .25 W 5.0 % CC	1	R6
9	9		1912799-00	LS00 NAND-GATE-QUAD 2IN,P	2	E8,E81
10	10		1910532-00	74S00 NAND GATE-QUAD 2IN	7	E1,E2,E39,E56,E63,E67,E109
11	11		1912388-00	74S02 NOR GATE-QUAD 2IN,PO	1	E110
12	12		1912803-00	LS04 INVERTER GATE-HEX 1I	5	E17,E19,E33,E44,E54
13	13		1910534-00	74S04 INVERTER GATE-HEX 1I	3	E89,E91,E95
14	14		1912805-00	LS08 AND GATE-QUAD 2IN,PO	2	E43,E45
15	15		1912389-00	74S08 AND GATE-QUAD 2IN,PO	3	E76,E84,E106
16	16		1910536-00	74S10 NAND GATE-TRIPLE 3IN	2	E97,E108
17	17		1910537-00	74S11 AND GATE-TRIPLE 3INP	3	E57,E72,E87
18	18		1912801-00	LS02 NOR-GATE-QUAD 2IN	1	E24
19	19		1910539-00	74S20 NAND GATE-DUAL 4INPU	2	E41,E80
20	20		1914086-00	74S30 NAND GATE-POS 8IN	2	E59,E92
21	21		1913340-00	74S32 OR GATE-QUAD 2IN	1	E111
22	22		1912820-00	LS51 A-O-I GATE 2-WIDE 2I	1	E96
23	23		1911712-00	74S51 AND-OR GATE-INVERT D	3	E86,E93,E102
24	24		1910544-00	74S74 FF-D DUAL,EDGE TRIGG	12	E32,E40,E51,E58,E70,E82,E83,E88
						CONT E98,E99,E101,E107
25	25		1912824-00	LS74 FF-D DUAL,EDGE TRIGG	4	E9,E10,E79,E85
26	26		1912828-00	L385 COMPARATOR,4BIT MAGN	4	E34,E35,E36,E47
27	27		1912096-00	DEC 74S86 XOR GATE,QUAD 2IN	2	E66,E77
28	28		1910545-00	74S112 FF-JK DUAL,EDGE TRIG	1	E100
29	29		1910436-00	DEC 74123 ONE SHOT-DUAL,RETRIG	1	E69

REVISION HISTORY		BASIC PART NO: M7685		DRN: <i>K. Davis</i>	DATE: 6-FEB-80	DIGITAL		
ENG:	ECO NUMBER	REV	SECTION A OF A	CHK'D: <i>R. Michaud</i>	DATE: 21-FEB-80	TITLE PARTS LIST		
<i>Rpt</i>	INITIAL	D	SECTION VARIATION INDEX	DES. ENG: C. DUNIGAN	DATE: 21-FEB-80	DATA SEQUENCER		
<i>26 Feb 80</i>			[A] YA	RESP. ENG.: C. DUNIGAN	DATE: 21-FEB-80	DOCUMENT NUMBER		
			[B]	MFG. ENG.: <i>D. Clarlin</i>	DATE: 21-FEB-80	SIZE	CODE	NUMBER
			[C]	ASSEMBLY NUMBER: D-UA-M7685-YA-0	TOP DOCUMENT NUMBER: #B-DD-M7685-YA	K	PL	M7685-YA-DBP
			[D]					REV
			[E]					D
			[F]					FILE NAME:
			[G]					Z1189D.PLS
			[H]					EDIT #
			[J]					8
			[K]					
			[L]					
			[M]					
			[N]					

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AUTOMATED BY FRTLST.3L(40)

PARTS LIST

SHEET A2 OF A2

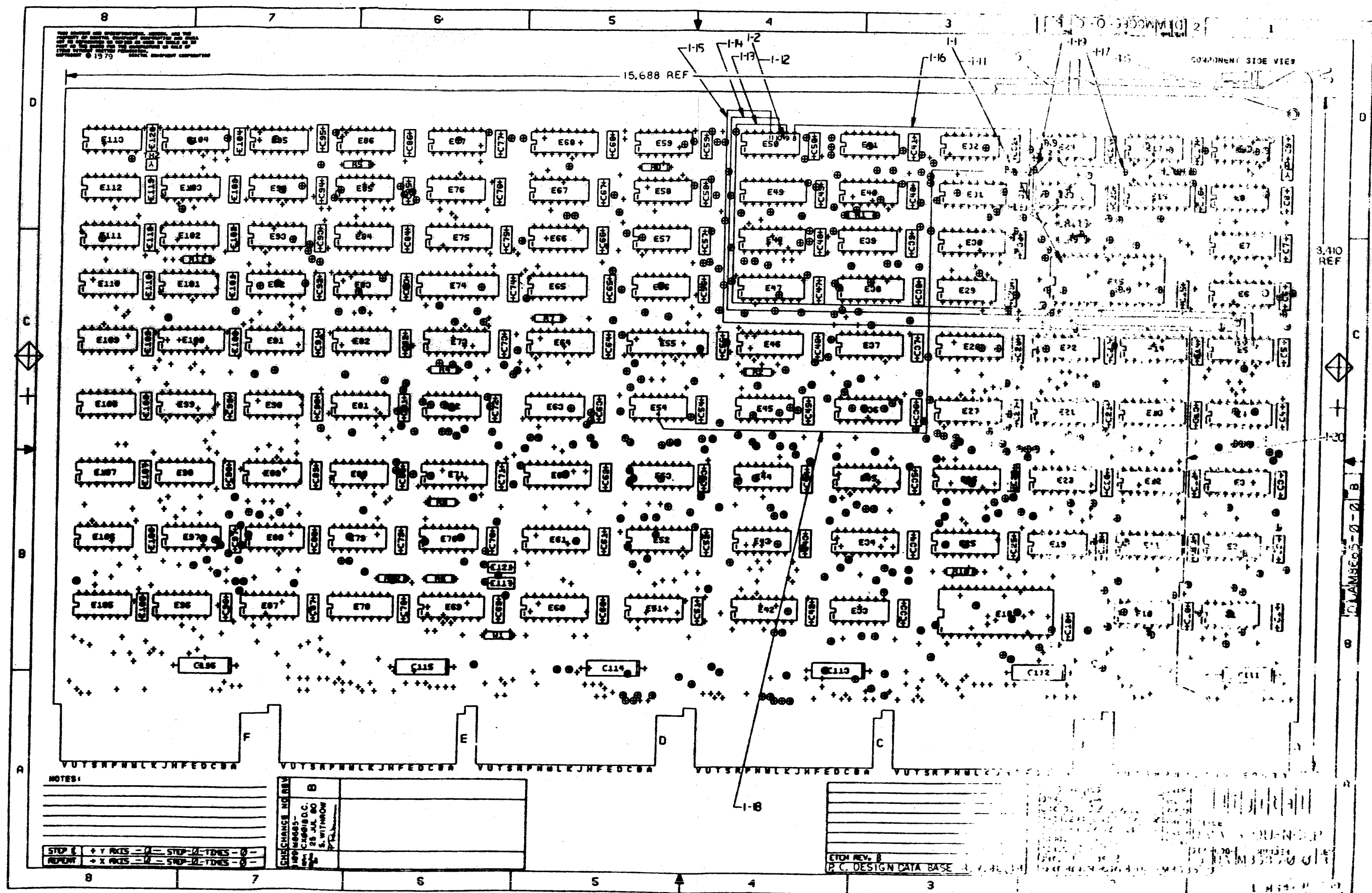
LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION YA	REFERENCE DESIGNATOR
30	30	1912847-00	LS157 MUX 1 OF 2(QUAD)	4	E25,E26,E28,E30
31	31	1914082-00	74S163 COUNTER,SYNCH UP/DOW	5	E11,E14,E22,E46,E61
32	32	1912850-00	LS164 SHIFT REG. DBIT SERI	3	E29,E103,E112
33	33	1912853-00	LS175 FF-D QUAD	3	E42,E68,E104
34	34	1910552-00	74S194 SHIFT REG.,4BIT RIGH	1	E75
35	35	1914085-00	74S260 NOR GATE-DUAL,POS	2	E94,E113
36	36	1914084-00	74S299 SHIFT REG.,8BIT RIGH	2	E55,E74
37	37	1911527-00	8097 BUFFER GATE-HEX 2INP	3	E71,E73,E78
38	38	1914087-00	8098 BUFFER GATE-HEX 2IN,	9	E4,E6,E13,E16,E27,E31,E37,E38, E49
39	39	1914083-00	8542 REGISTER,I/O-QUAD,TR	5	E52,E53,E60,E62,E64
40	40	1913474-00	9401 GENERATOR/CHECKER CR	1	E90
41	41	1912807-00	LS10 NAND GATE-TRIPLE 3IN	1	E105
42	42	1912849-00	LS161 COUNTER,SYNCHR,4BIT	7	E3,E5,E7,E12,E20,E21,E48
43	43	23050C6-00	C6-01	1	E15
44	44	1912813-00	LS27 NOR GATE-TRIPLE 3IN	1	E50
45	45	23002C6-00	C6-01	1	E18
46	46	9009185-00	JUMPER, WIRE, INSULATED, BLACK B	1	W1
47	47	9105740-55	WIRE(WRAP)30AWG UL1423	A/R	

CONT

D	I	G	I	T	A	L	TITLE	DATE	REV
							DATA SEQUENCER		D

1979

COMPONENT SIDE VIEW



NOTES:

STEP 1	+ Y PALS	- A	STEP 2	- TDES	- 0
REPORT	+ X PALS	- A	STEP 3	- TDES	- 0

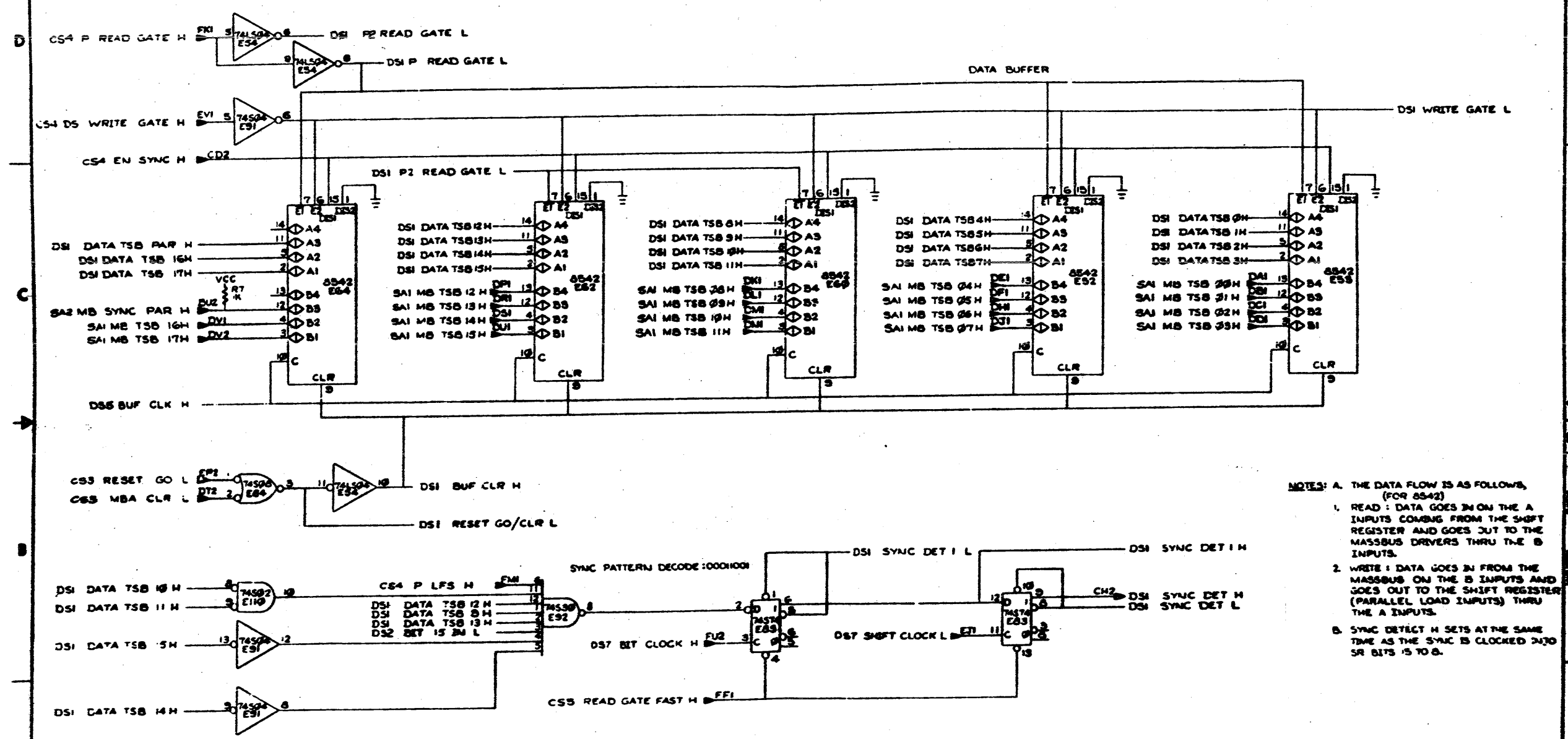
EXCHANGE NO. 011	B
199146685	
CAG0018.C	
25 JUL 80	
S. WITHROW	

ETON REV. B
P.C. DESIGN DATA BASE

199146685
CAG0018.C
25 JUL 80
S. WITHROW

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CS M8685-0-1 2



- NOTES: A. THE DATA FLOW IS AS FOLLOWS, (FOR 8542)
1. READ : DATA GOES IN ON THE A INPUTS COMING FROM THE SHIFT REGISTER AND GOES OUT TO THE MASSBUS DRIVERS THRU THE B INPUTS.
 2. WRITE : DATA GOES IN FROM THE MASSBUS ON THE B INPUTS AND GOES OUT TO THE SHIFT REGISTER (PARALLEL LOAD INPUTS) THRU THE A INPUTS.
- B. SYNC DETECT H SETS AT THE SAME TIME AS THE SYNC IS CLOCKED INTO SR BITS 15 TO 8.

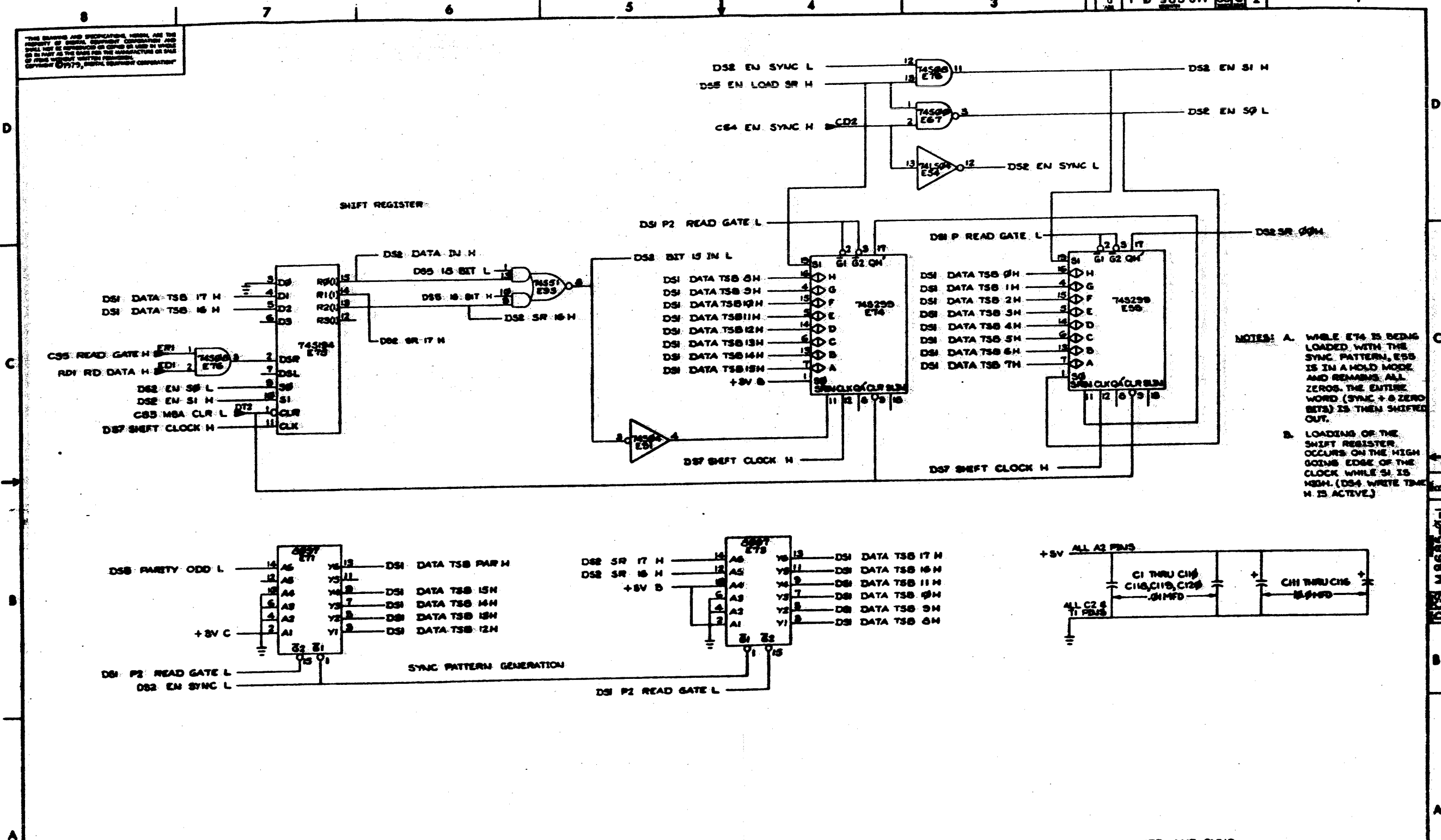
REV.	1
DATE	11
DESIGNED BY	
CHECKED BY	
APPROVED BY	
DATE	
BY	
REVISION	
NO.	
DATE	
BY	
REVISION	
NO.	
DATE	
BY	

DATA BUFFER AND SYNC DETECT	
DATE	11
DESIGNED BY	
CHECKED BY	
APPROVED BY	
DATE	
BY	
REVISION	
NO.	
DATE	
BY	
REVISION	
NO.	
DATE	
BY	

DATA SEQUENCER (CS)

SCALE	1	DATE	11
SHEET	2	REV.	3

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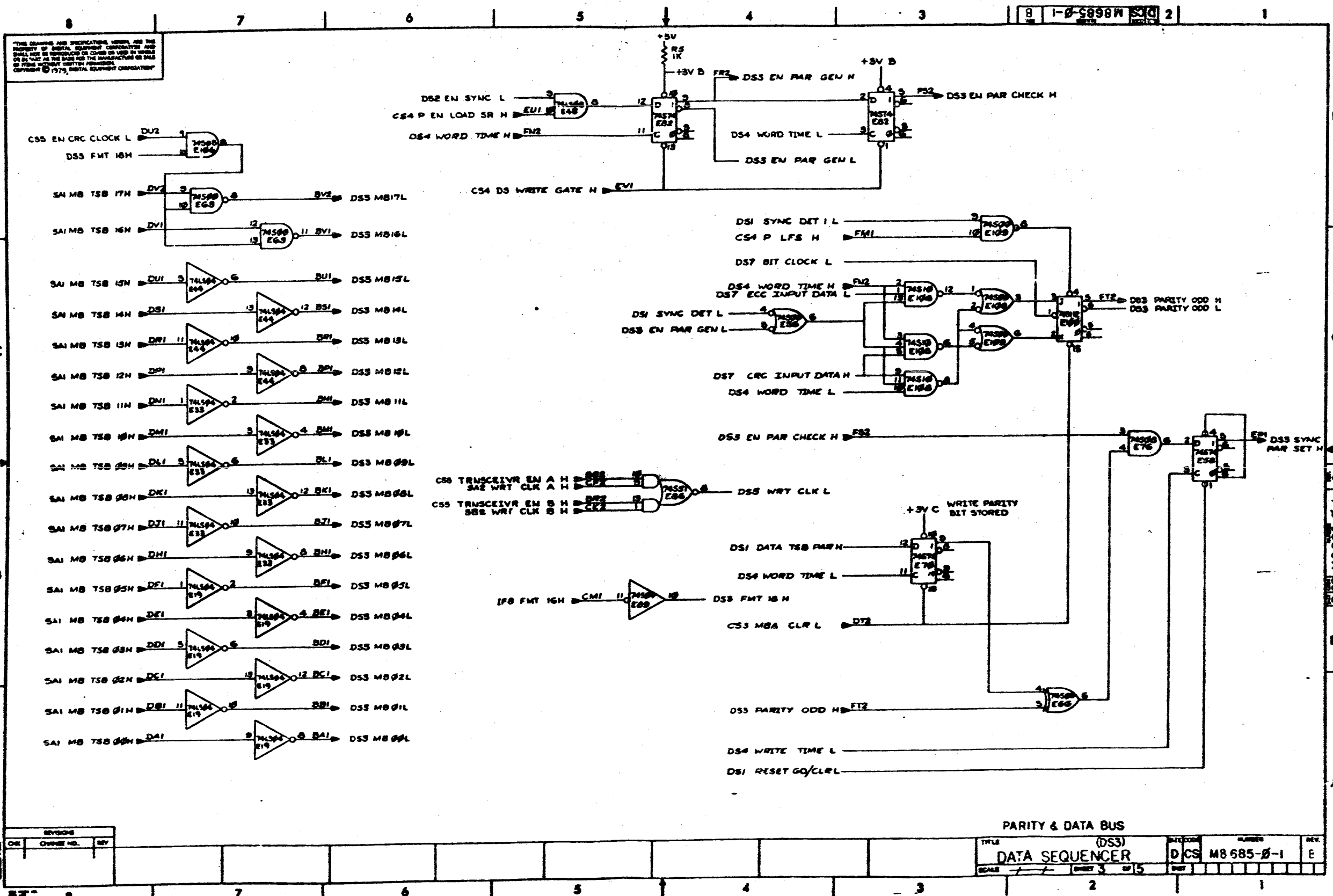


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 B. LOADING OF THE SHIFT REGISTER OCCURS ON THE HIGH GOING EDGE OF THE CLOCK WHILE S1 IS HIGH. (DS4 WRITE TIME N IS ACTIVE.)

SHIFT REGISTER AND SYNC PATTERN GENERATION (WRITE)
 DATA SEQUENCER (DS2)
 DCS M8685-0-1 B

REV	DATE	BY	CHKD	APPD

TITLE	REV	DATE	BY	CHKD	APPD
DATA SEQUENCER (DS2)	B				
SCALE		SHEET 2 OF 5			



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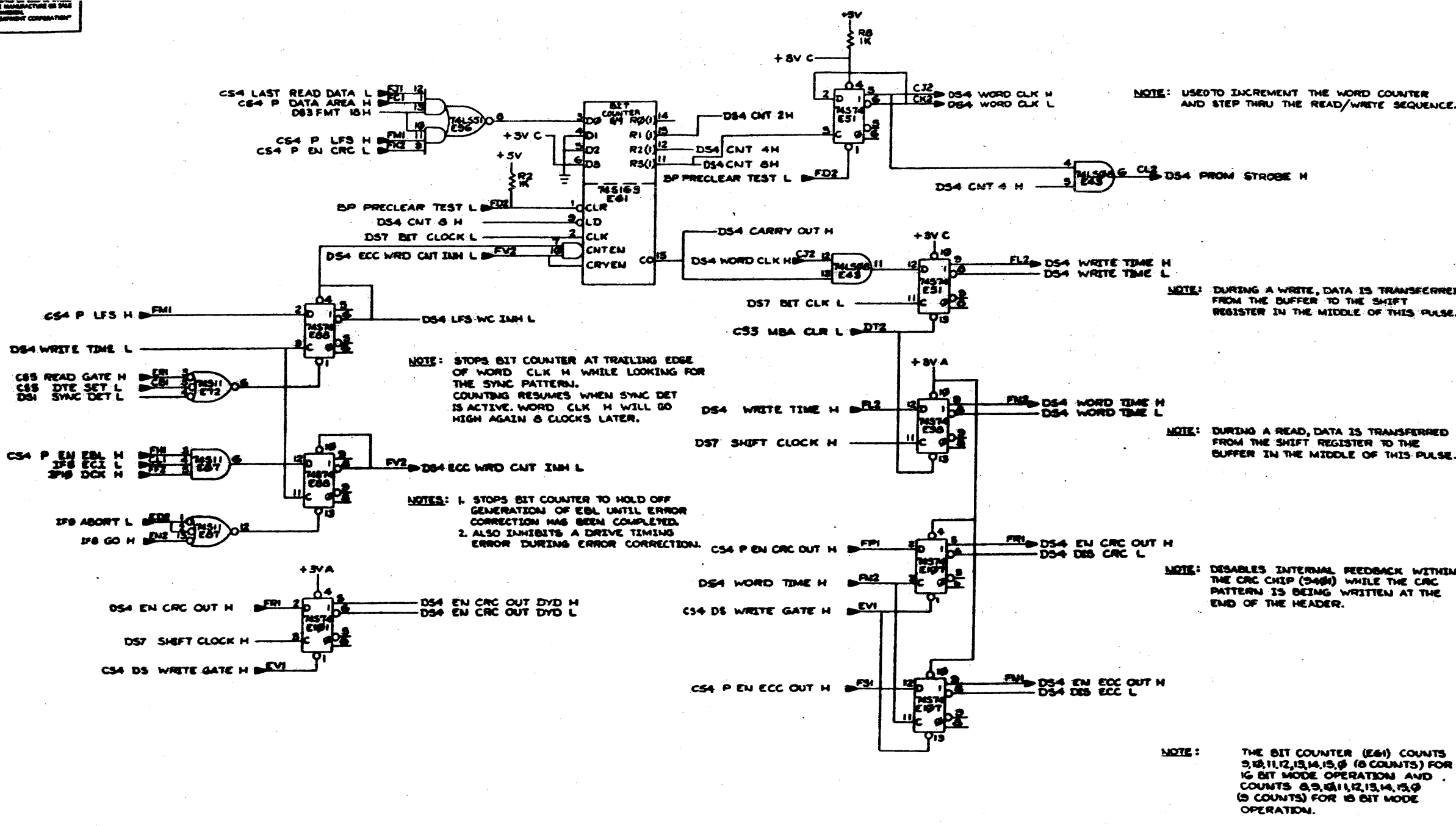
1-0-5898W SCD 2

PARITY & DATA BUS
 TITLE: DATA SEQUENCER (DS3)
 DRAWING NUMBER: DCS M8685-0-1
 REV: E

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D
C
B
A

D
C
B
A



NOTE: USED TO INCREMENT THE WORD COUNTER AND STEP THRU THE READ/WRITE SEQUENCE.

NOTE: DURING A WRITE, DATA IS TRANSFERRED FROM THE BUFFER TO THE SHIFT REGISTER IN THE MIDDLE OF THIS PULSE.

NOTE: STOPS BIT COUNTER AT TRAILING EDGE OF WORD CLK H WHILE LOOKING FOR THE SYNC PATTERN. COUNTING RESUMES WHEN SYNC DET IS ACTIVE. WORD CLK H WILL GO HIGH AGAIN 8 CLOCKS LATER.

NOTE: DURING A READ, DATA IS TRANSFERRED FROM THE SHIFT REGISTER TO THE BUFFER IN THE MIDDLE OF THIS PULSE.

NOTES: 1. STOPS BIT COUNTER TO HOLD OFF GENERATION OF EBL UNTIL ERROR CORRECTION HAS BEEN COMPLETED. 2. ALSO INHIBITS A DRIVE TIMING ERROR DURING ERROR CORRECTION.

NOTE: DISABLES INTERNAL FEEDBACK WITHIN THE CRC CHIP (2488) WHILE THE CRC PATTERN IS BEING WRITTEN AT THE END OF THE HEADER.

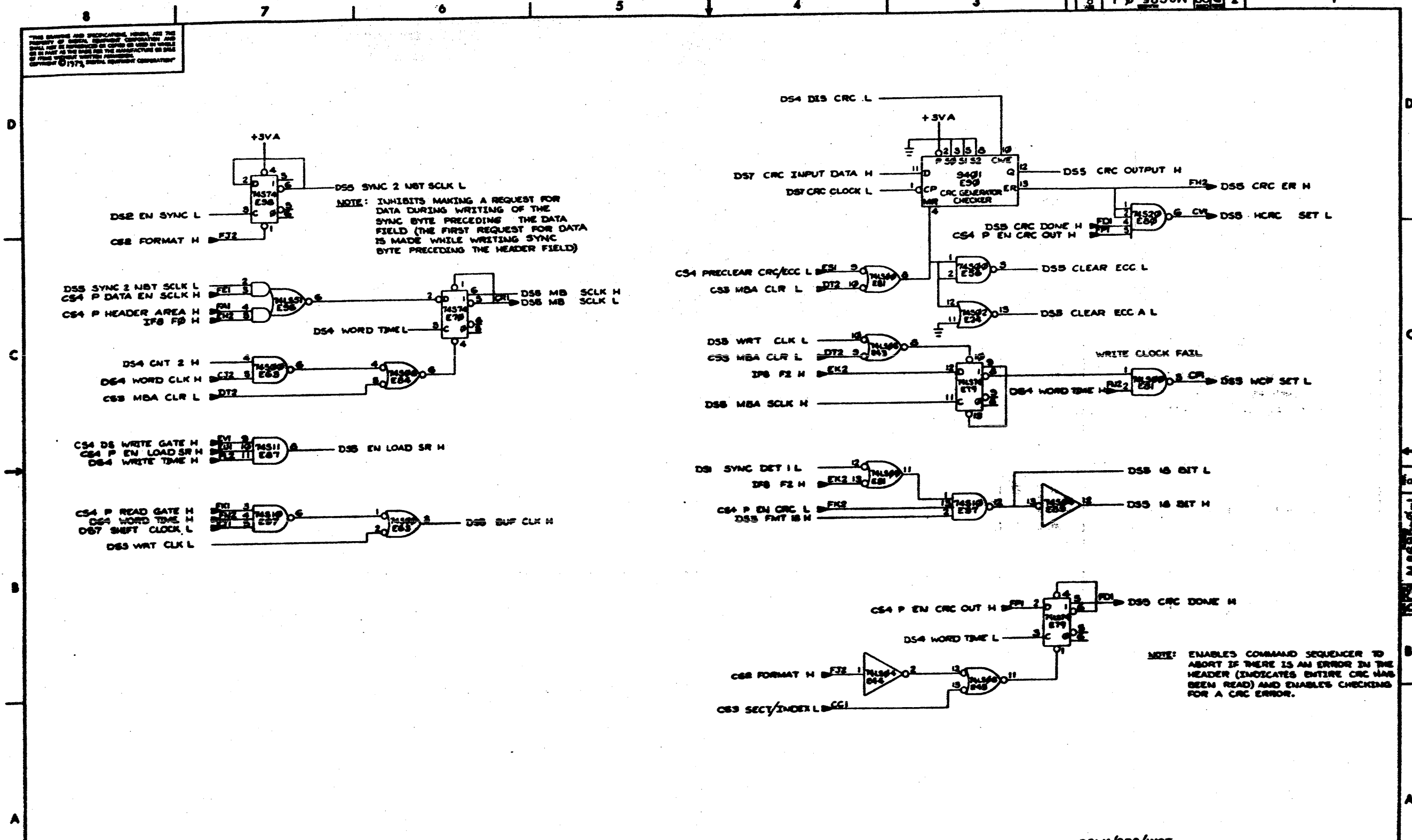
NOTE: THE BIT COUNTER (E61) COUNTS 2,10,11,12,13,14,15,0 (8 COUNTS) FOR 16 BIT MODE OPERATION AND COUNTS 0,9,10,11,12,13,14,15,0 (9 COUNTS) FOR 18 BIT MODE OPERATION.

DCS M8685-0-1 B

REVISIONS		
CHR	CHANGE NO.	REV

WORD CLOCK/TIMING		TITLE (DS4)		DATE CODE		NUMBER		REV.	
DATA SEQUENCER		DCS M8685-0-1		8		8		8	
SCALE 1-1		SHEET 4 OF 15		SHEET		SHEET		SHEET	

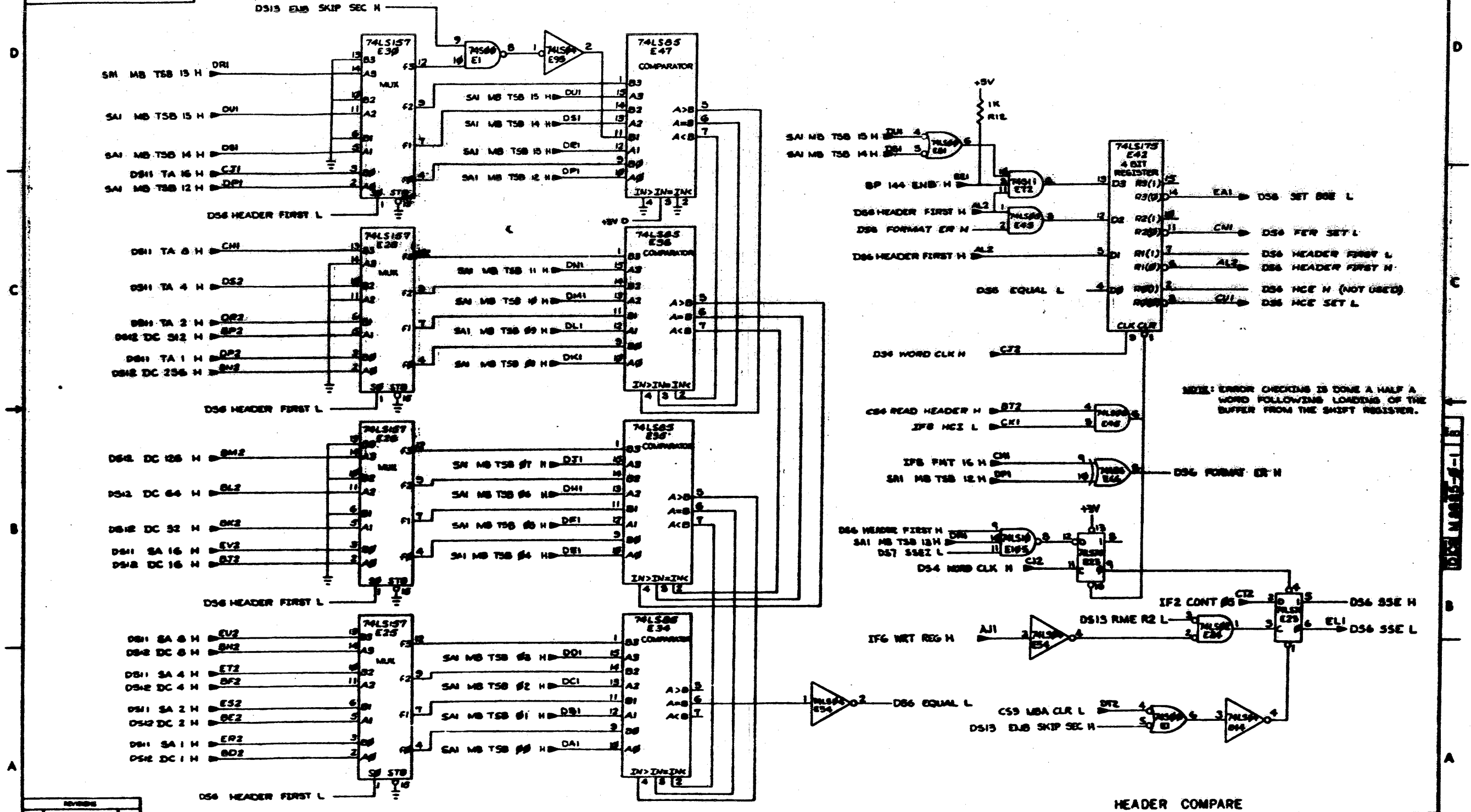
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REVISED		
CHK	CHANGE NO.	REV.

SCLK/CRC/WCF		TITLE		DRAWING NUMBER		REV.	
		(DSS) DATA SEQUENCER		DCS M8685-0-1		B	
SCALE		SHEET	5	OF	15		

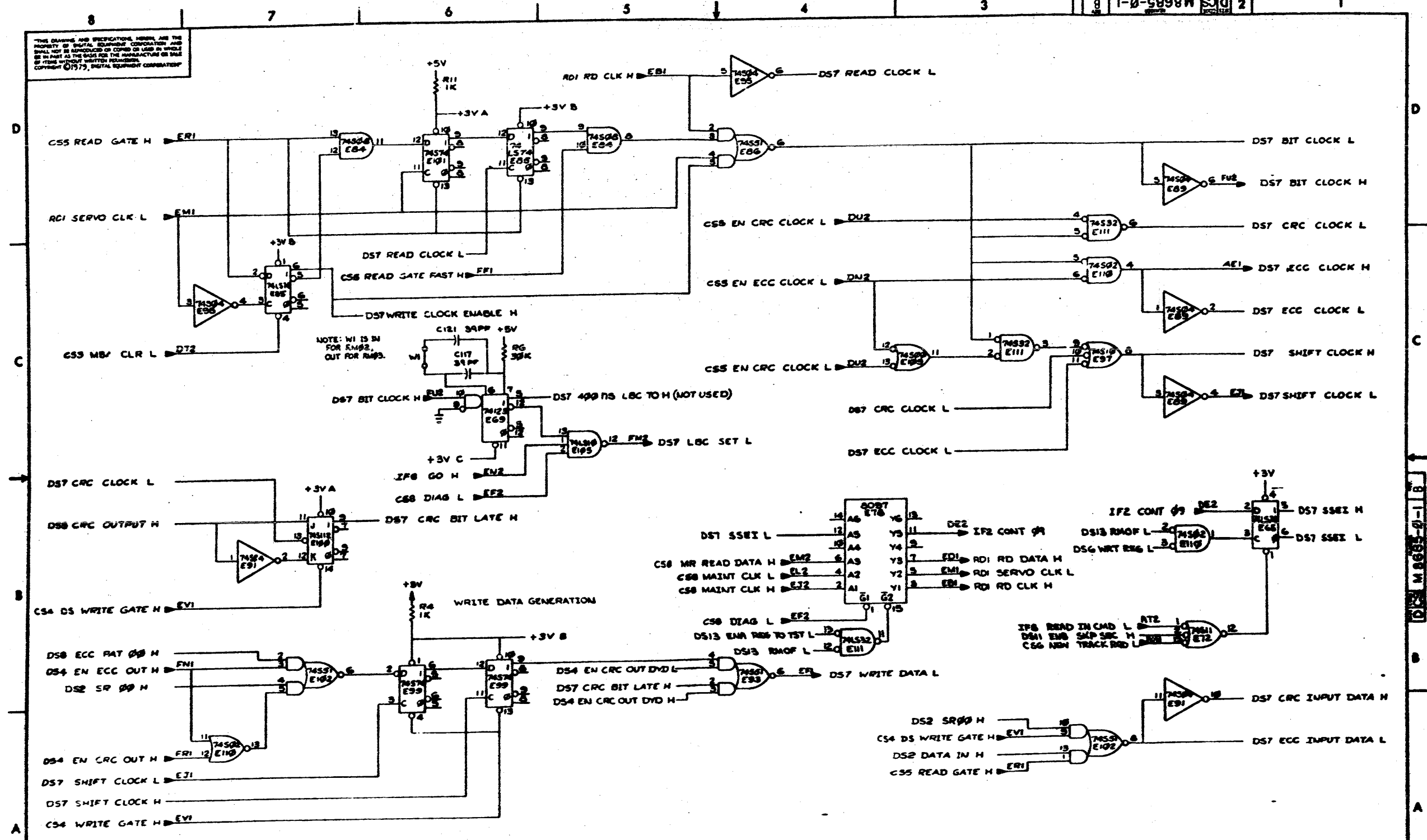
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NOTE: ERROR CHECKING IS DONE A HALF A WORD FOLLOWING LOADING OF THE BUFFER FROM THE SHIFT REGISTER.

REVISED		TITLE		DRAWING NUMBER		SCALE		SHEET		OF	
DATE	CHANGE NO.	DATE	DESCRIPTION	NO.	REV.	NO.	REV.	NO.	REV.	NO.	REV.
			(DS6) DATA SEQUENCER	D	CS	M8685- B -1		6	5		

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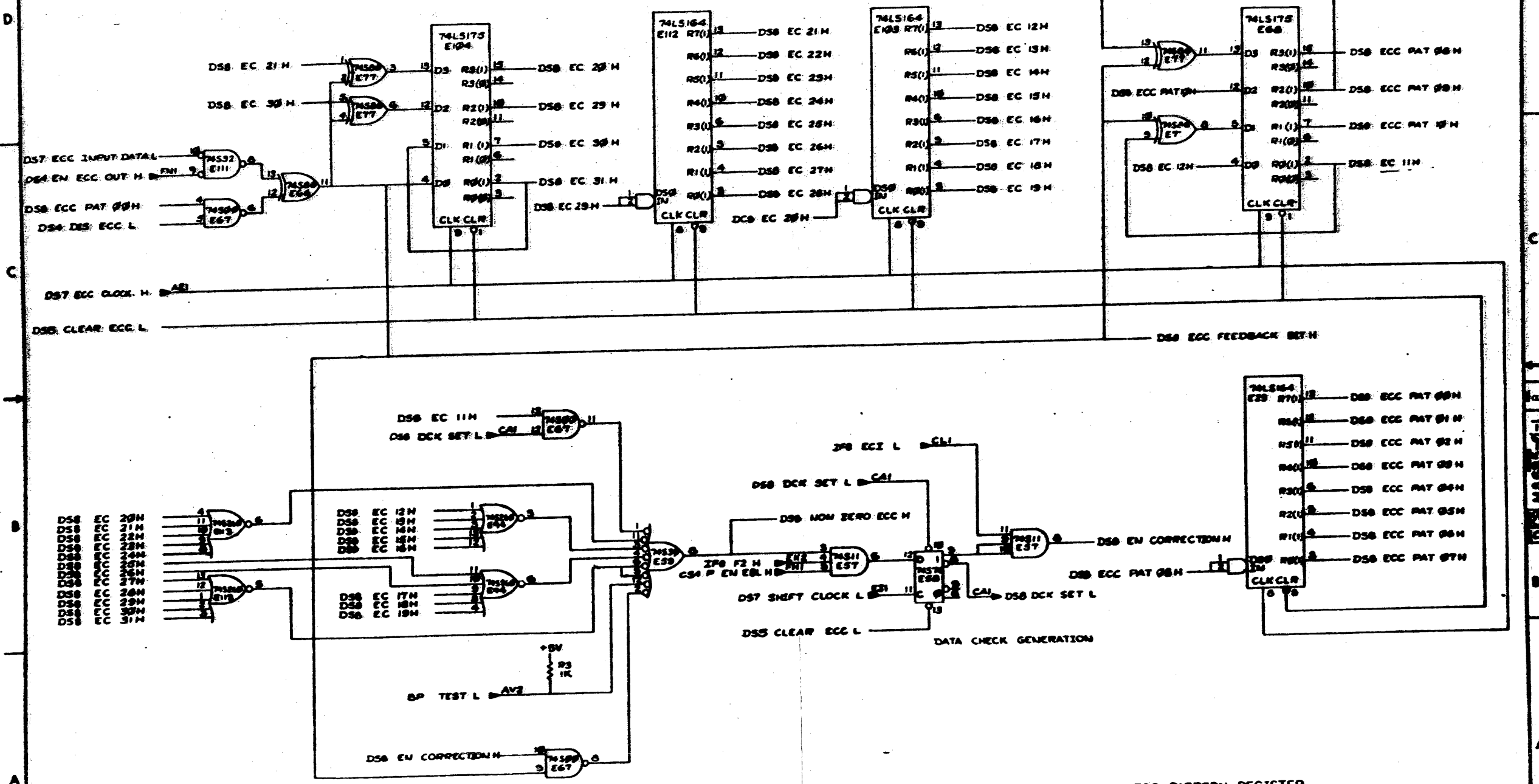


REVISIONS		
CHK	CHANGE NO	REV

BIT CLOCK GENERATION AND WRITE DATA			
TITLE (DS7)			
DATA SEQUENCER		DCS	M8685-0-1
SCALE	SHEET 7 OF 15	REV.	B

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ECC PATTERN REGISTER

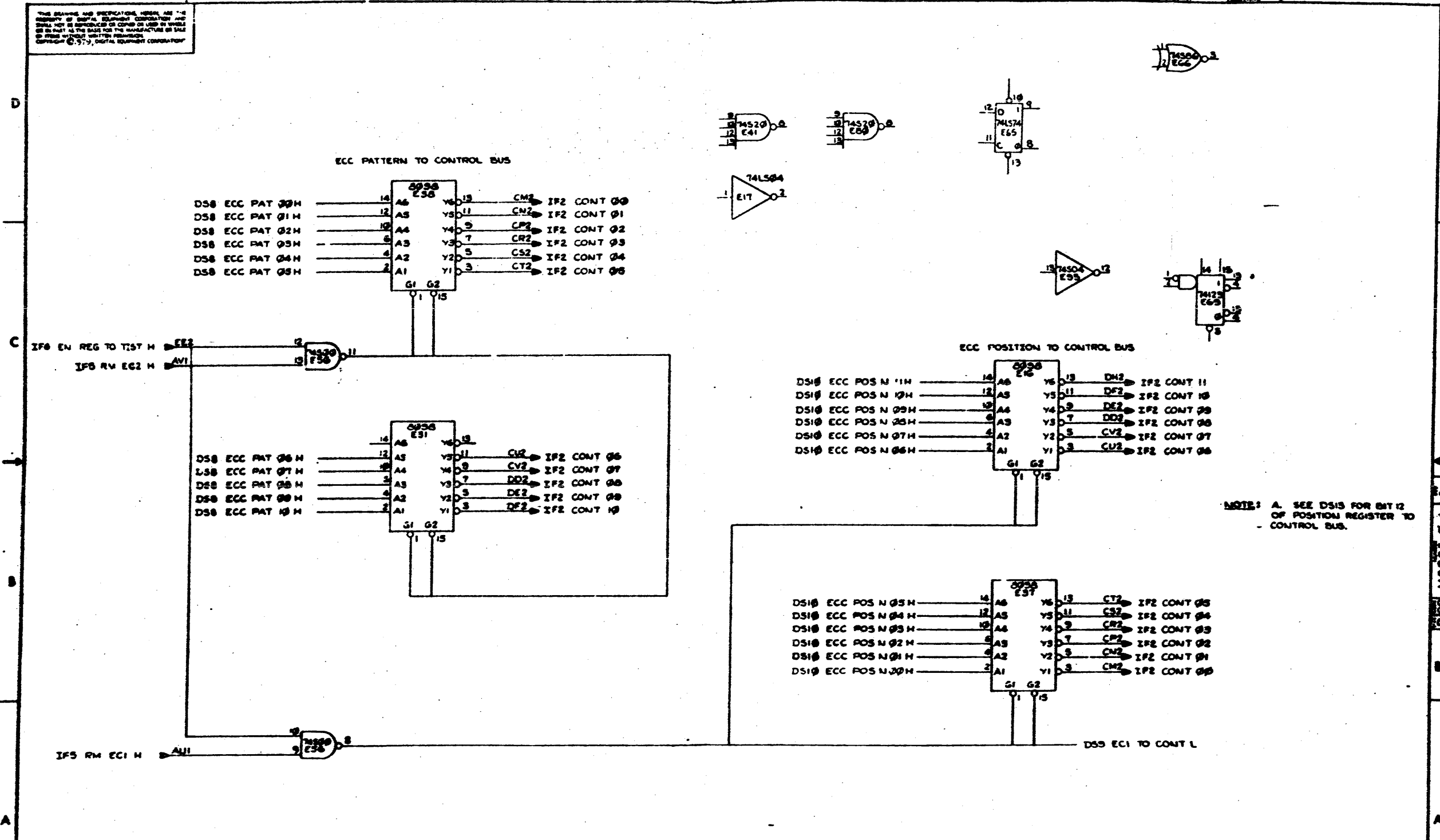


ECC PATTERN REGISTER AND DCK GENERATION

REVISIONS		
DATE	CHANGE NO.	REV.

TITLE	(DSB) DATA SEQUENCER	DATE CODE	DCS M8685-B-1	NUMBER	8	REV.	B
SCALE	1/1	SHEET	8	OF	15		

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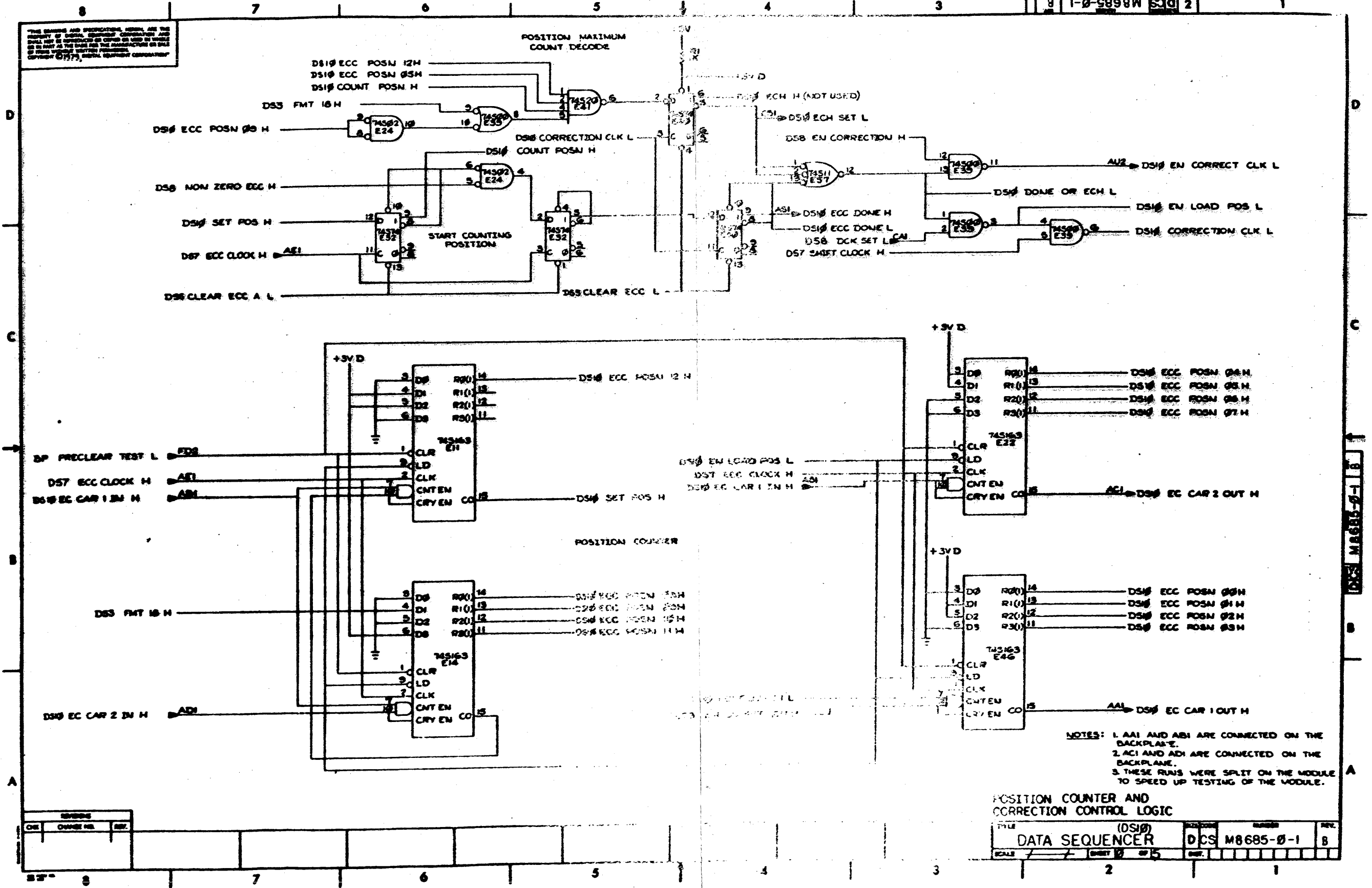
NOTE: A. SEE DSI0 FOR BIT 12 OF POSITION REGISTER TO CONTROL BUS.

REVISIONS		
CHK	CHANGE NO	REV

TITLE		(DS9)	NUMBER	REV.
DATA SEQUENCER		DCS	M8685-0-1	1
SCALE	SHEET	OF		
	9	15		

DCS M8685-0-1 P

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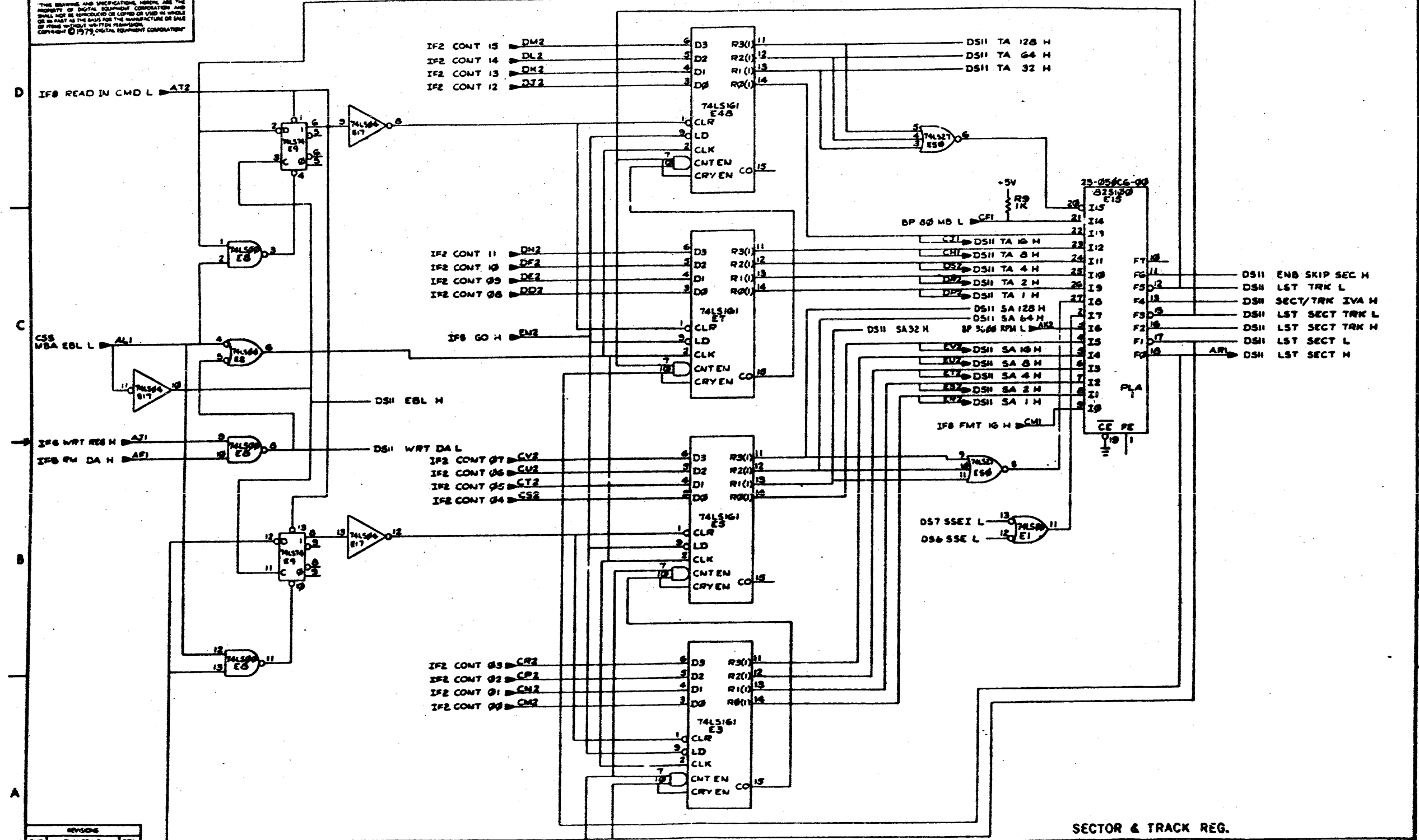
NOTES: 1. AAI AND ABI ARE CONNECTED ON THE BACKPLANE.
 2. ACI AND ADI ARE CONNECTED ON THE BACKPLANE.
 3. THESE RUNS WERE SPLIT ON THE MODULE TO SPEED UP TESTING OF THE MODULE.

POSITION COUNTER AND CORRECTION CONTROL LOGIC

FILE	(DS6)	REVISION	NUMBER	REV.
	DATA SEQUENCER	DCS	M8685-0-1	B
SCALE	1/1	SHEET	10	OF 15

ONE	CHANGE NO.	REV.

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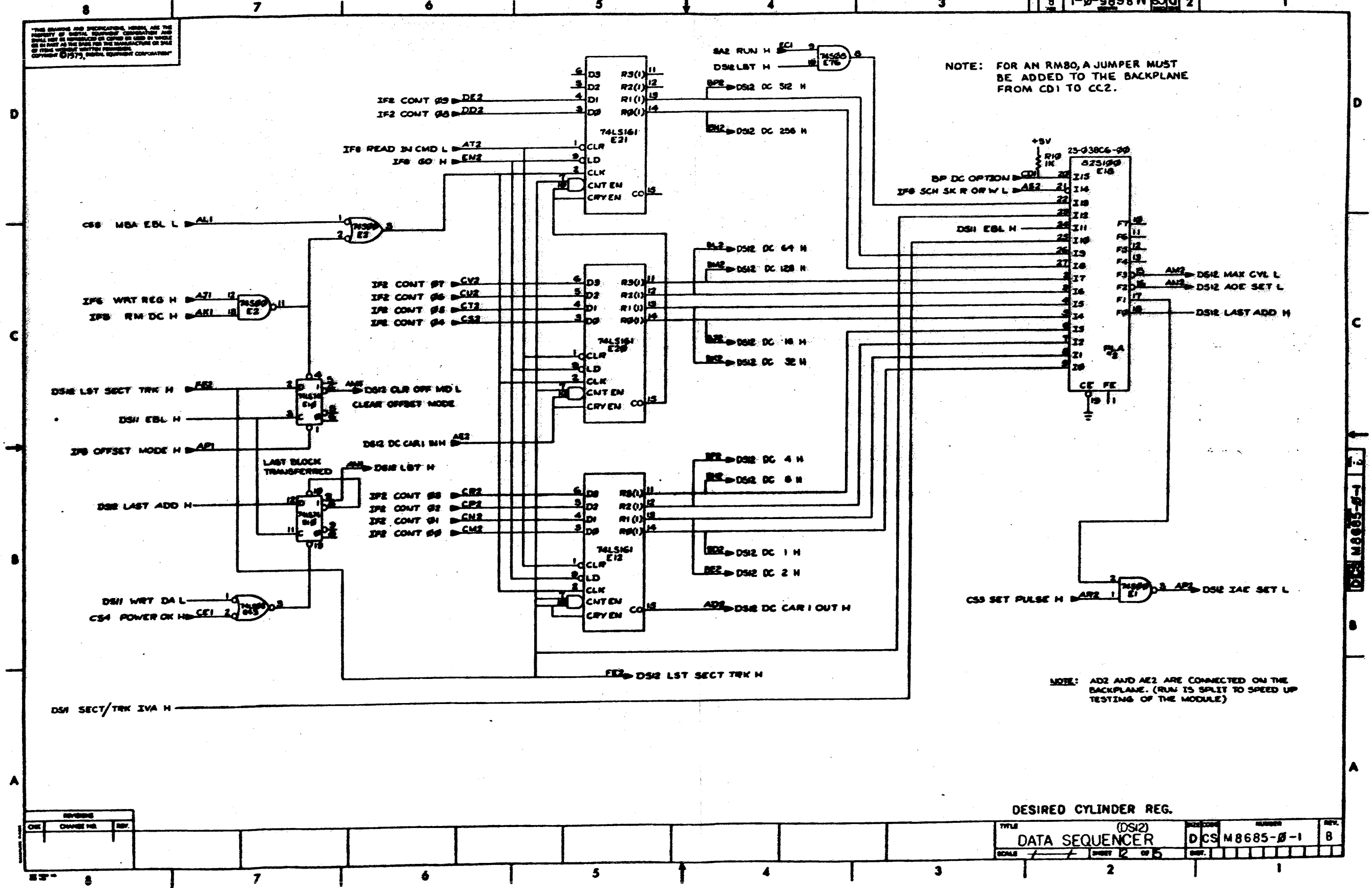


REVISIONS		
CHK	CHANGE NO	REV

TITLE		NUMBER		REV
DATA SEQUENCER		DCS M8685-B-1		E
SCALE	SHEET	OF		

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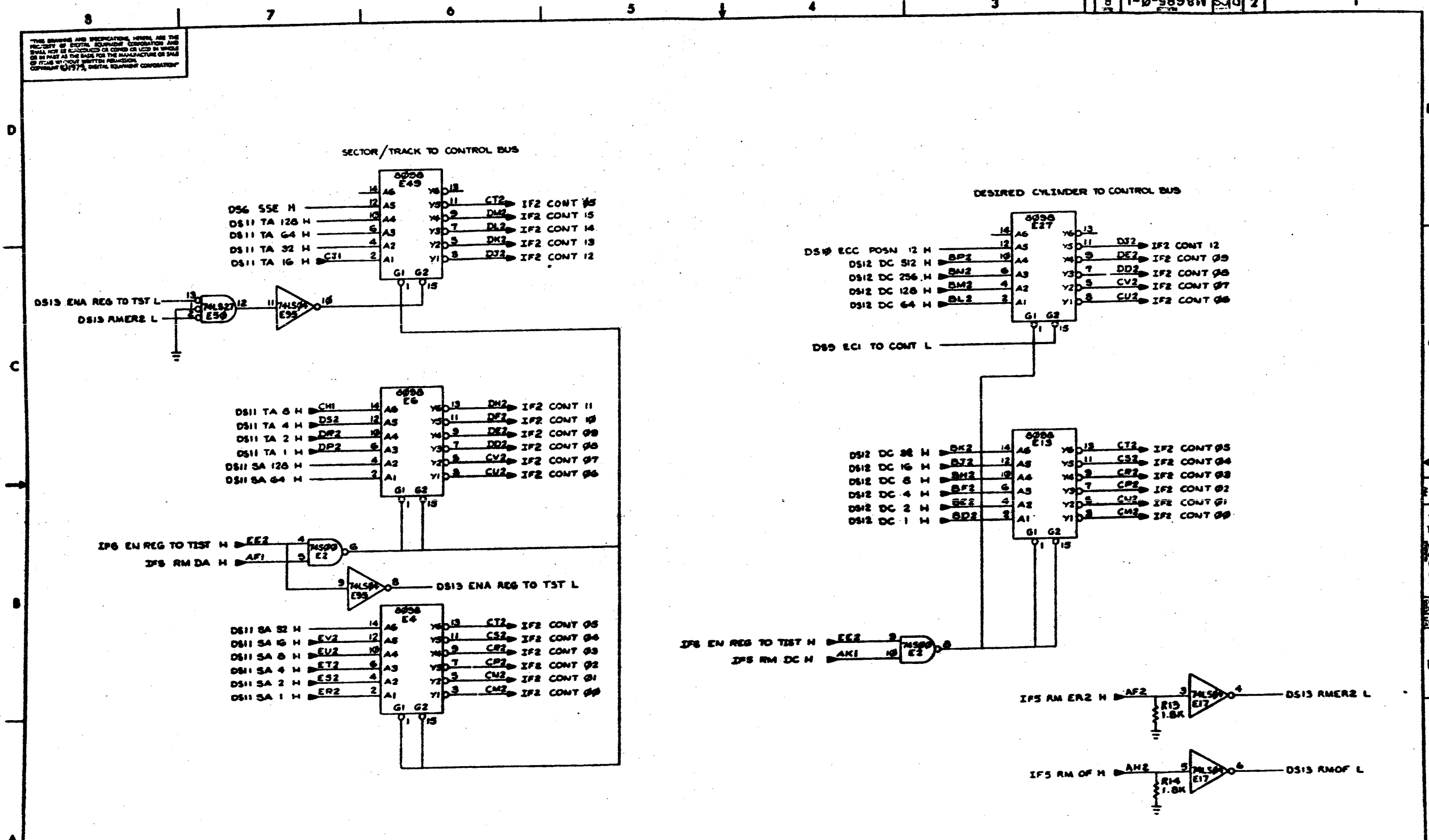
NOTE: FOR AN RM80, A JUMPER MUST BE ADDED TO THE BACKPLANE FROM CD1 TO CC2.



REV	CHG	NO	DATE

TITLE		DCS M8685-0-1		REV. B	
DATA SEQUENCER (DSI2)		DCS M8685-0-1		B	
SCALE 1/1		SHEET 2 OF 5		REV. 1	

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REVISIONS		
CHK	CHANGE NO.	REV.

SECT/TRACK/DC GATED TO CONTROL BUS				DCS M8685-0-1		REV. E
TITLE (DS13) DATA SEQUENCER			DATE CODE	NUMBER	REV.	
SCALE			SHEET 13	OF 15	DATE	

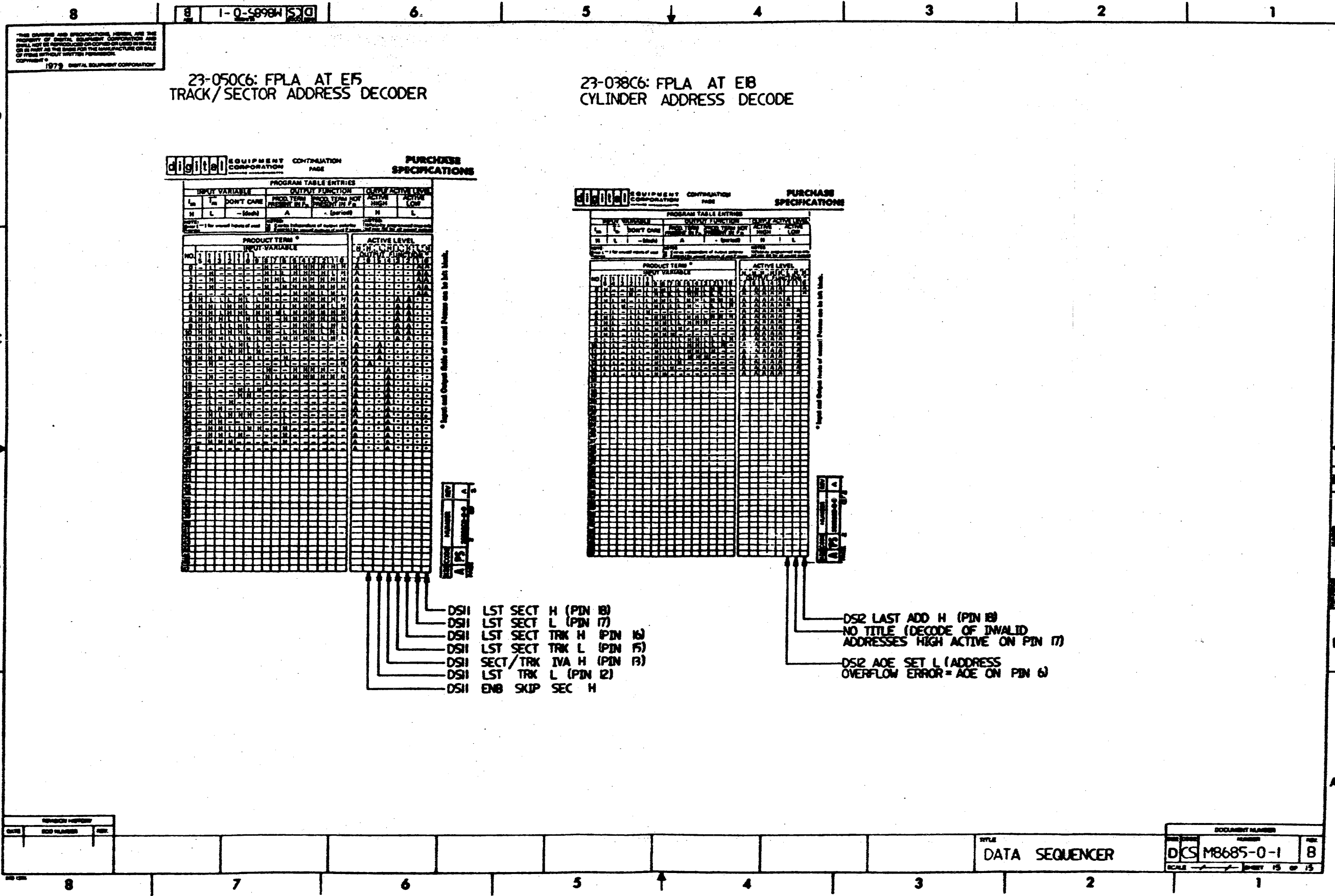
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AA1	DS#EC CAR 1 OUT H	BA1	DS3 MB 00 L	CA1	DS8 DCK SET L	DA1	SA1 MB TSB 00 H	EA1	DS8 SET BSE L	FA1	CS4 P HEADER AREA H
AB1	DS#EC CAR 1 IN H	BB1	DS3 MB 01 L	CB1	CS5 DTE SET L	DB1	SA1 MB TSB 01 H	EB1	ROI RO CLK H	FB1	SPARE
AC1	DS#EC CAR 2 OUT H	BC1	DS3 MB 02 L	CC1	CS3 SECT/INDEX L	DC1	SA1 MB TSB 02 H	EC1	SA2 RMN H	FC1	CS4 P DATA AREA H
AD1	DS#EC CAR 2 IN H	BD1	DS3 MB 03 L	CD1	BP DC OPTION	DD1	SA1 MB TSB 03 H	ED1	ROI RO DATA H	FD1	DS5 CRC DONE H
AE1	DS7 EDC CLOCK H	BE1	DS3 MB 04 L	CE1	CS4 POWER ON H	DE1	SA1 MB TSB 04 H	EE1	BP #44 ENB H	FE1	CS4 P DATA EN SCLK H
AF1	IFS RM DA H	BF1	DS3 MB 05 L	CF1	BP 00 MB L	DF1	SA1 MB TSB 05 H	EF1	DS7 WRITE DATA L	FF1	CS5 READ GATE FAST H
AH1	CS6 NEW TRM RQD L	BH1	DS3 MB 06 L	CH1	DSH TA 0 H	DH1	SA1 MB TSB 06 H	FH1	SPARE	FH1	CS4 P EN ERL H
AJ1	IFB WRT REG H	BJ1	DS3 MB 07 L	CJ1	DSH TA 16 H	DJ1	SA1 MB TSB 07 H	EJ1	DS7 SHIFT CLOCK L	FJ1	CS4 LAST READ DATA L
AK1	IFS RM DC H	BK1	DS3 MB 08 L	CK1	IFB HCI L	DK1	SA1 MB TSB 08 H	EK1	SPARE	FK1	CS4 P READ GATE H
AL1	CS6 MBA ERL L	BL1	DS3 MB 09 L	CL1	IFB ECI L	DL1	SA1 MB TSB 09 H	EL1	DS6 SSE L	FL1	SPARE
AM1	DS2 CLN OFF MB L	BM1	DS3 MB 10 L	CM1	IFB FMT 10 H	DM1	SA1 MB TSB 10 H	EM1	ROI ERJ CLK L	FM1	CS4 P LFS H
AN1	DS2 LBT H	BN1	DS3 MB 11 L	CN1	DS8 FER SET L	DN1	SA1 MB TSB 11 H	EN1	SPARE	FN1	DS4 EN ECC OUT H
AP1	IFB OFFSET MODE H	BP1	DS3 MB 12 L	CP1	DS6 WCF SET L	DP1	SA1 MB TSB 12 H	EP1	DS3 SYNC PAR SET H	FP1	CS4 P EN CRC OUT H
AR1	DSH LST SECT H	BR1	DS3 MB 13 L	CR1	DS6 MB SCLK L	DR1	SA1 MB TSB 13 H	ER1	CS6 READ GATE H	FR1	DS4 EN CRC OUT H
AS1	DS# ECG DOME H	BS1	DS3 MB 14 L	CS1	DS# ECH SET L	DS1	SA1 MB TSB 14 H	ES1	CS4 PRECLEAR CAC/ECC L	FS1	CS4 P EN ECC OUT H
AT1	GND	BT1	GND	CT1	GND	DT1	GND	ET1	GND	FT1	GND
AH1	IFS RM ECI H	BH1	DS3 MB 15 L	CH1	DS6 HCE SET L	DH1	SA1 MB TSB 15 H	EH1	CS4 P EN LOAD SR H	FH1	SPARE
AV1	IFS RM EC2 H	BV1	DS3 MB 16 L	CV1	DS6 MCPC SET L	DV1	SA1 MB TSB 16 H	EV1	CS4 DS WRITE GATE H	FV1	SPARE

AA2	+5V	BA2	+5V	CA2	+5V	DA2	+5V	EA2	+5V	FA2	+5V
AB2	-15V	BB2	-15V	CB2	-15V	DB2	-15V	EB2	-15V	FB2	-15V
AC2	GND	BC2	GND	CC2	GND	DC2	GND	EC2	GND	FC2	GND
AD2	DS2 DC CAR 1 OUT H	BD2	DS2 DC 1 H	CD2	CS4 EN SYNC H	DD2	IF2 CONT 00	ED2	IFB ABORT L	FD2	BP PRECLEAR TEST L
AE2	DS2 DC CAR 1 IN H	BE2	DS2 DC 2 H	CE2	SB2 WRT CLK B H	DE2	IF2 CONT 01	EE2	IFS EN REG TO TEST H	FE2	DS2 LST SECT TRM H
AF2	IFS RM ER2 H	BF2	DS2 DC 4 H	CF2	SA2 WRT CLK A H	DF2	IF2 CONT 10	EF2	CS6 DMG L	FF2	IFB DCK H
AH2	IFS RM OF L	BH2	DS2 DC 8 H	CH2	DS8 SYNC DET H	DH2	IF2 CONT 11	EH2	IFB F0 H	FH2	DS5 CRC EN H
AJ2	SPARE	BJ2	DS2 DC 16 H	CJ2	DS4 WORD CLK H	DJ2	IF2 CONT 12	EJ2	CS6 MAINT CLK H	FJ2	CS2 FORMAT H
AK2	BP 3600 RPM L	BK2	DS2 DC 32 H	CK2	DS4 WORD CLK L	DK2	IF2 CONT 13	EK2	IFB F2 H	FK2	CS4 P EN CRC L
AL2	DS8 HEADER FIRST H	BL2	DS2 DC 64 H	CL2	DS4 PROM STROBE H	DL2	IF2 CONT 14	EL2	CS6 MAINT CLK L	FL2	DS4 WRITE TIME H
AM2	DS2 MAX CYL L	BM2	DS2 DC 128 H	CM2	IF2 CONT 00	DM2	IF2 CONT 15	EM2	CS6 MR READ DATA H	FM2	DS7 LBC SET L
AN2	DS2 AQE SET L	BN2	DS2 DC 256 H	CN2	IF2 CONT 01	DN2	CS6 EN ECC CLOCK L	EN2	IFB G0 H	FN2	DS4 WORD TIME H
AP2	DS2 IAE SET L	BP2	DS2 DC 512 H	CP2	IF2 CONT 02	DP2	DSH TA 1 H	EP2	CS3 RESET G0 L	FP2	SPARE
AR2	CS3 SET PULSE H	BR2	CS5 TRNSCEIVR EN B H	CR2	IF2 CONT 03	DR2	DSH TA 2 H	ER2	DSH SA 1 H	FR2	DS3 EN PAR GEN H
AS2	IFB SCH SK R DR W L	BS2	CS5 TRNSCEIVR EN A H	CS2	IF2 CONT 04	DS2	DSH TA 4 H	ES2	DSH SA 2 H	FS2	DS3 EN PAR CHECK H
AT2	IFB READ IN CMD L	BT2	CS4 READ HEADER H	CT2	IF2 CONT 05	DT2	CS3 MBA CLA L	ET2	DSH SA 4 H	FT2	DS3 PARITY COO H
AU2	DS# EN CORRECT CLK L	BV2	SA2 MB SYNC PAR H	CV2	IF2 CONT 06	DV2	CS5 EN CRC CLOCK L	EV2	DSH SA 8 H	FV2	DS7 BLT CLOCK H
AV2	BP TEST L	BV2	DS3 MB 17 L	CV2	IF2 CONT 07	DV2	SA1 MB TSB 17 H	EV2	DSH SA 16 H	FV2	DS4 ECC WRD CNT INH L

IO SIGNAL LIST

TITLE	10S14	DATE	NOV 1977	NUMBER	1
DATA SEQUENCER		DIST	DCS	M8685-0-1	
SCALE	1:1	SHEET	14	OF	15



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23-050C6: FPLA AT E5
TRACK/SECTOR ADDRESS DECODER

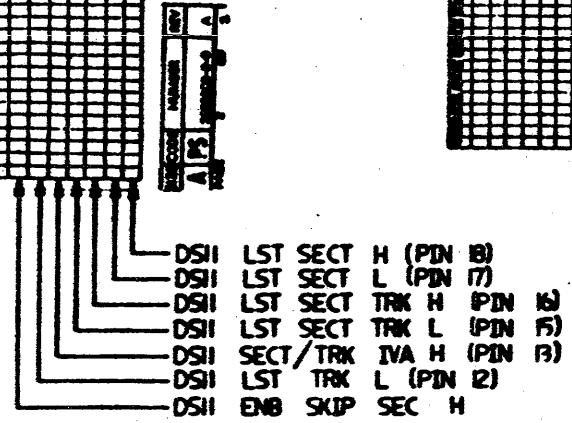
23-038C6: FPLA AT E8
CYLINDER ADDRESS DECODE

digital EQUIPMENT CORPORATION PURCHASE SPECIFICATIONS

INPUT VARIABLE		OUTPUT FUNCTION		HEAVY ACTIVE LEVEL	
I_n	I_m	PROG. TERM. PRESENT IN F _n	PROG. TERM. PRESENT IN F _m	HIGH	LOW
H	L	- (Don't)	A	- (Don't)	L

PRODUCT TERM ⁰		ACTIVE LEVEL	
NO.	INPUT VARIABLE	OUTPUT FUNCTION	HEAVY ACTIVE LEVEL
1	A	A	H
2	A	A	L
3	A	A	H
4	A	A	L
5	A	A	H
6	A	A	L
7	A	A	H
8	A	A	L
9	A	A	H
10	A	A	L
11	A	A	H
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95	A	A	H
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98	A	A	L
99	A	A	H
100	A	A	L

⁰ Input and Output Paths of unused Pins are to Left Mark.

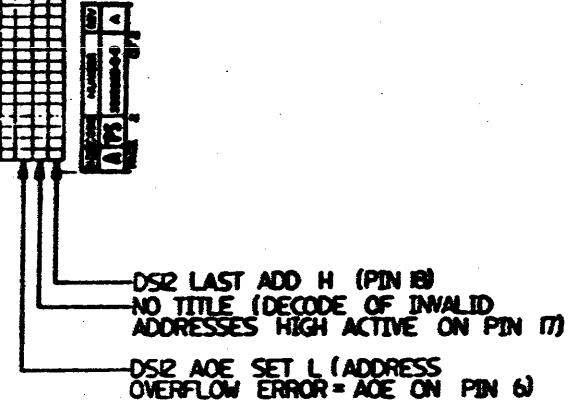


digital EQUIPMENT CORPORATION PURCHASE SPECIFICATIONS

INPUT VARIABLE		OUTPUT FUNCTION		HEAVY ACTIVE LEVEL	
I_n	I_m	PROG. TERM. PRESENT IN F _n	PROG. TERM. PRESENT IN F _m	HIGH	LOW
H	L	- (Don't)	A	- (Don't)	L

PRODUCT TERM ⁰		ACTIVE LEVEL	
NO.	INPUT VARIABLE	OUTPUT FUNCTION	HEAVY ACTIVE LEVEL
1	A	A	H
2	A	A	L
3	A	A	H
4	A	A	L
5	A	A	H
6	A	A	L
7	A	A	H
8	A	A	L
9	A	A	H
10	A	A	L
11	A	A	H
12	A	A	L
13	A	A	H
14	A	A	L
15	A	A	H
16	A	A	L
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94	A	A	L
95	A	A	H
96	A	A	L
97	A	A	H
98	A	A	L
99	A	A	H
100	A	A	L

⁰ Input and Output Paths of unused Pins are to Left Mark.



REVISION HISTORY		
DATE	REV. NUMBER	REV.

TITLE
DATA SEQUENCER

DOCUMENT NUMBER		
DCS	NUMBER	REV.
DCS	M8685-0-1	B
SCALE		SHEET 15 OF 15

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION	REFERENCE DESIGNATOR
1	D-MD-5014029-0-0	5014029-00	DRILL AND ETCH BRD	1	
2		1000010-00	39.0 MMF 100V 5X200PPM MICA	2	C117,C121
3		1001610-00	.01 MFD 50V +80-20% 25U CER	113	C1-C110,C118-C120
4		1017472-00	10 MFD 35V +50-10% AL EL	6	C111-C116
5		1210711-02	/REPLACED BY 12-16988-02	1	
6		1300365-00	1.0 K .25 W 5.0 % CC	11	R1-R5,R7-R12
7		1300398-00	1.80 K .25 W 5.0 % CC	2	R13,R14
8		1302394-00	30.0 K .25 W 5.0 % CC	1	R6
9		1910436-00	DEC 74123 ONE SHOT-DUAL,RETRIG	1	E69
10		1910532-00	74S00 NAND GATE-QUAD 2IN	7	E1,E2,E39,E56,E63,E67,E109
11		1910534-00	74S04 INVERTER GATE-HEX 1I	3	E89,E91,E95
12		1910536-00	74S10 NAND GATE-TRIPLE 3IN	2	E97,E108
13		1910537-00	74S11 AND GATE-TRIPLE 3INP	3	E57,E87,E72
14		1910539-00	74S20 NAND GATE-DUAL 4INPU	2	E41,E80
15		1910544-00	74S74 FF-D DUAL,EDGE TRIGG	12	E32,E40,E51,E58,E70,E82,E83,E88, CONT E98,E101,E107,E99
16		1910545-00	74S112 FF-JK DUAL,EDGE TRIG	1	E100
17		1910552-00	74S194 SHIFT REG.,4BIT RIGH	1	E75
18		1911527-00	8097 BUFFER GATE-HEX 2INP	3	E71,E73,E78
19		1911712-00	74S51 AND-OR GATE-INVERT D	3	E86,E93,E102
20		1912096-00	DEC 74S86 XOR GATE,QUAD 2IN	2	E66,E77
21		1912388-00	74S02 NOR GATE-QUAD 2IN,PO	1	E110
22		1912389-00	74S08 AND GATE-QUAD 2IN,PO	3	E76,E84,E106
23		1912799-00	LS00 NAND-GATE-QUAD 2IN,P	2	E8,E81
24		1912801-00	LS02 NOR-GATE-QUAD 2IN	1	E24
25		1912803-00	LS04 INVERTER GATE-HEX 1I	5	E17,E19,E33,E44,E54
26		1912805-00	LS08 AND GATE-QUAD 2IN,PO	2	E43,E45
27		1912807-00	LS10 NAND GATE-TRIPLE 3IN	1	E105
28		1912813-00	LS27 NOR GATE-TRIPLE 3IN	1	E50
29		1912820-00	LS51 A-O-I GATE 2-WIDE 2I	1	E96

REVISION HISTORY			BASIC PART NO: M8685		DRN:	DATE:	DIGITAL			
ENG:	ECO NUMBER	REV	SECTION A OF A	CHK'D:	DATE:	PARTS LIST				
REL	INITIAL	A	SECTION VARIATION INDEX	DATE:	DATA SEQUENCER					
RPM	M8685-CX001B	B	[A] 00	DES.ENG:	DATE:	DOCUMENT NUMBER				
15 SEP			[B]	RESP.ENG.:	DATE:	SIZE CODE NUMBER REV				
			[C]	MFG.ENG.:	DATE:	K	PL	M8685-0-DBP	B	
			[D]	ASSEMBLY NUMBER:	TOP DOCUMENT NUMBER:	FILE NAME:		EDIT		
			[E]	D-UA-M8685-0-0	B-DD-M8685-0	Z0912B.PLS		18		
			[F]							
			[G]							
			[H]							
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			[K]							
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			[M]							
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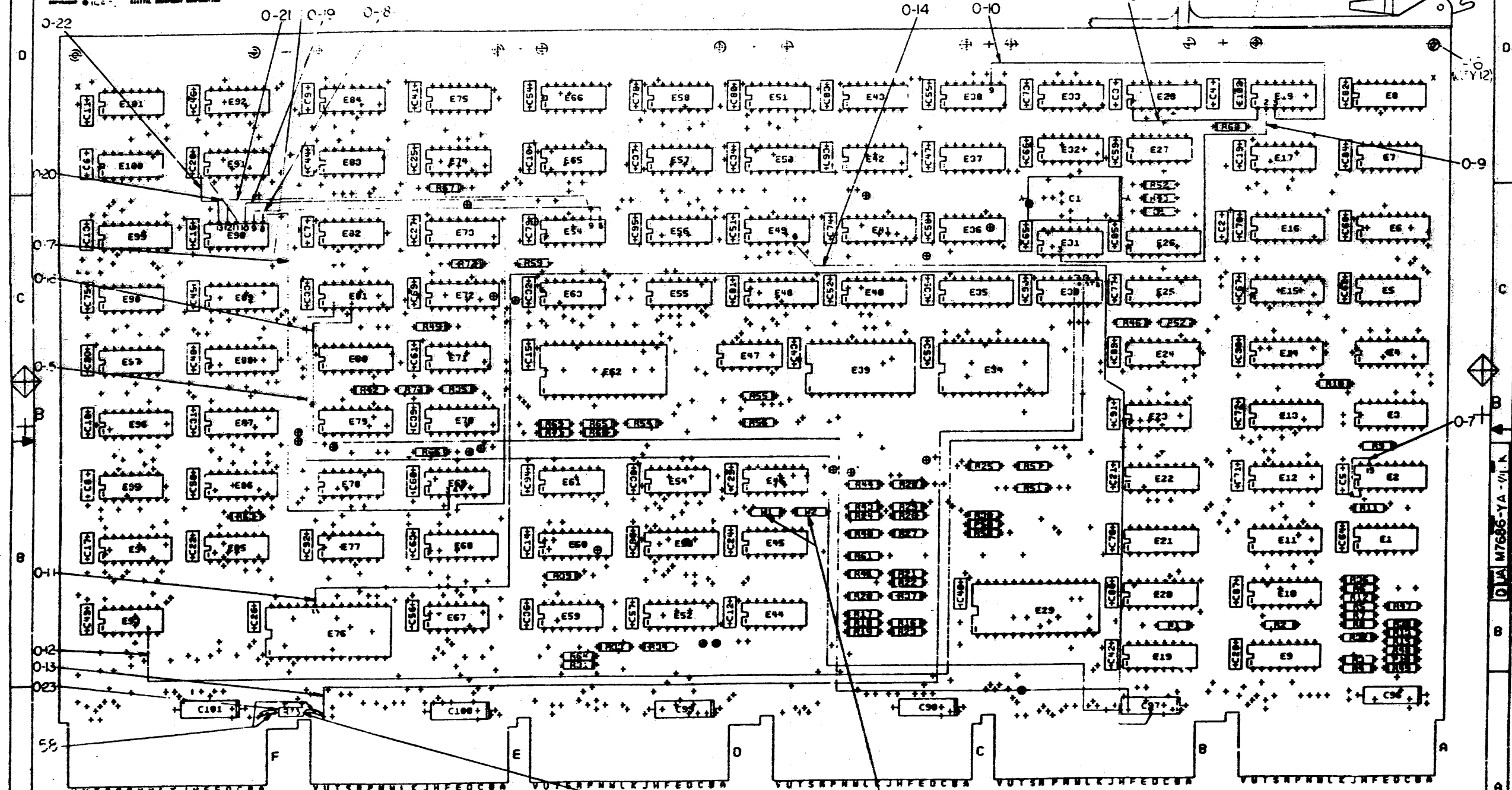
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LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION 00	REFERENCE DESIGNATOR
30	30	1912824-00	LS74 FF-D DUAL,EDGE TRIGG	6	E9,E10,E23,E65,E79,E85
31	31	1912828-00	LS85 COMPARATOR,4BIT MAGN	4	E34-E36,E47
32	32	1912847-00	LS157 MUX 1 OF 2(QUAD)	4	E25,E26,E28,E30
33	33	1912849-00	LS161 COUNTER,SYNCHR,4BIT	7	E3,E5,E7,E12,E20,E21,E48
34	34	1912850-00	LS164 SHIFT REG. 8BIT SERI	3	E29,E103,E112
35	35	1912853-00	LS175 FF-D QUAD	3	E42,E68,E104
36	36	1913340-00	74S32 OR GATE-QUAD 2IN	1	E111
37	37	1913474-00	9401 GENERATOR/CHECKER CR	1	E90
38	38	1914082-00	74S163 COUNTER,SYNCH UP/DOW	5	E11,E14,E22,E46,E61
39	39	1914083-00	8542 REGISTER,I/O-QUAD,TR	5	E52,E53,E60,E62,E64
40	40	1914084-00	74S299 SHIFT REG.,8BIT RIGH	2	E55,E74
41	41	1914085-00	74S260 NOR GATE-DUAL,POS	2	E94,E113
42	42	1914086-00	74S30 NAND GATE-POS 8IN	2	E59,E92
43	43	1914087-00	8098 BUFFER GATE-HEX 2IN,	9	E4,E6,E13,E16,E27,E31,E37,E38, CONT E49
44	44	23050C6-00	C6-01	1	E15
45	45	23038C6-00	C6-01	1	E18
46	46	9000024-01	EYELET, ROLLED FLANGE, .121 OD X	12	
47	47	9009185-00	JUMPER, WIRE, INSULATED, BLACK B	1	W1
48	48	9105740-55	WIRE(WRAP)30AWG UL1423	A/R	
49	49	9009157-00	ADH,LIQ,RM.TEMP CURING COLORLESS	A/R	

! D ! I ! G ! I ! T ! A ! L !	! TITLE !	! SECTION A OF A !	! SIZE ! CODE !	! DOCUMENT NUMBER !	! REV !
	DATA SEQUENCER		K ! PL !	M8685-0-DBP	B !

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COMPONENT SIDE VIEW



NOTES:
 1. EAGLES 033(-JCG-0045) MAYNARD
 ONLY
 2. IT MUST BE INSTALLED AND WZ
 MUST BE REMOVED AFTER GA TEST
 3. COMPONENT P73 IS HAND INSERTED

RESPONSE ONLY

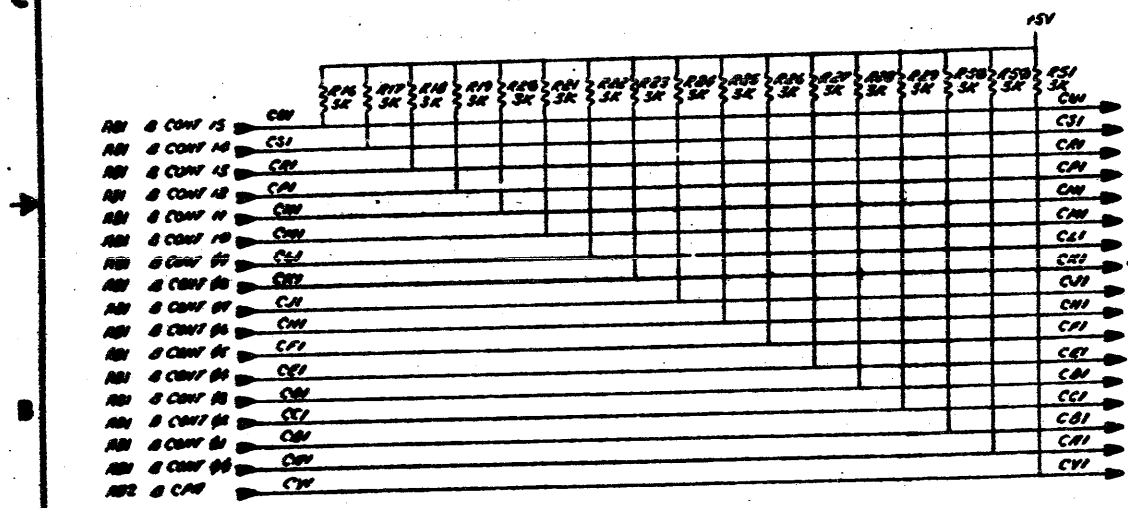
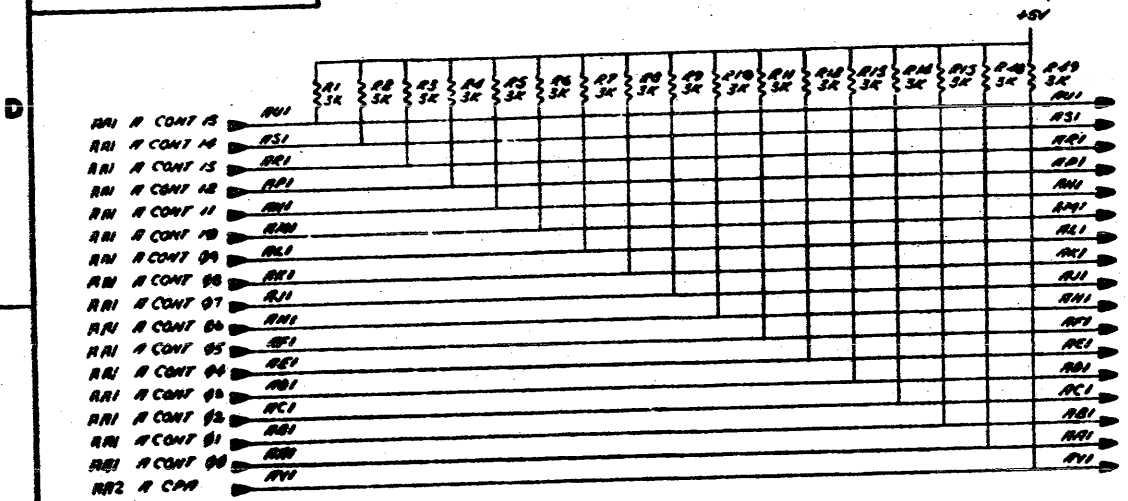
SEE NOTE 3

SEE NOTE 2

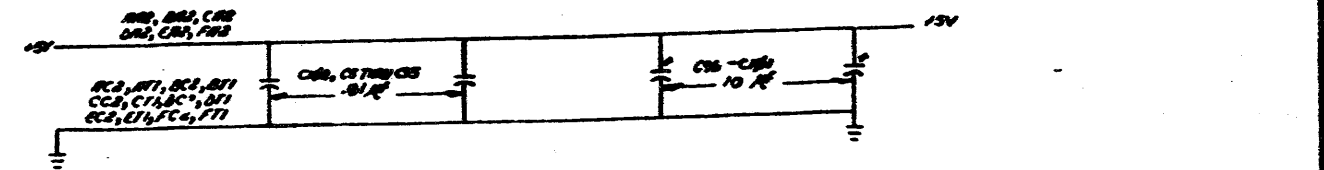
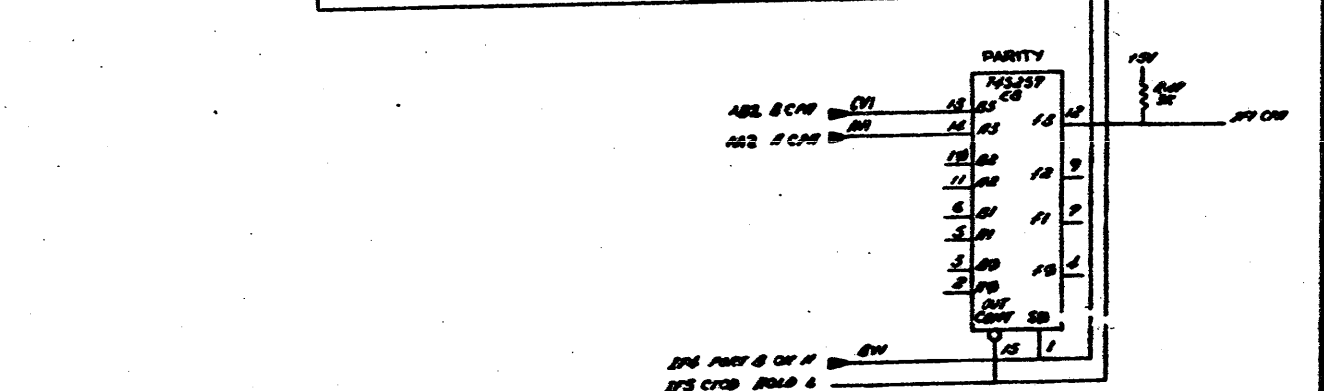
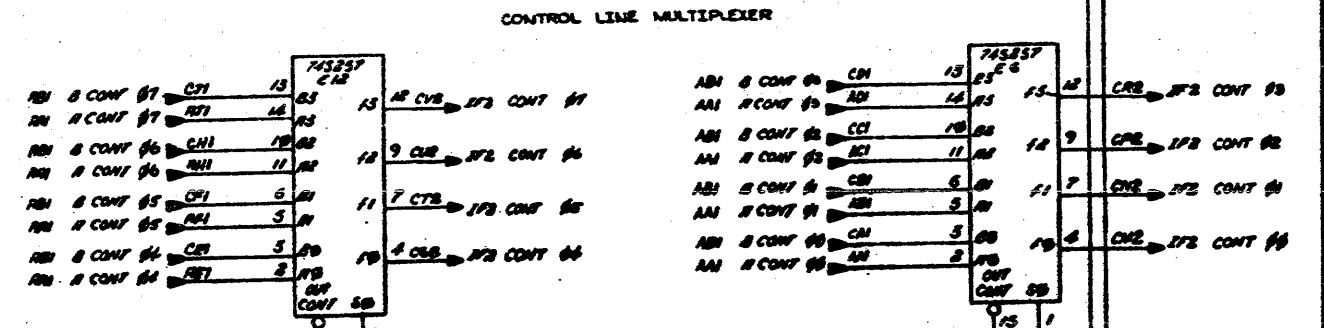
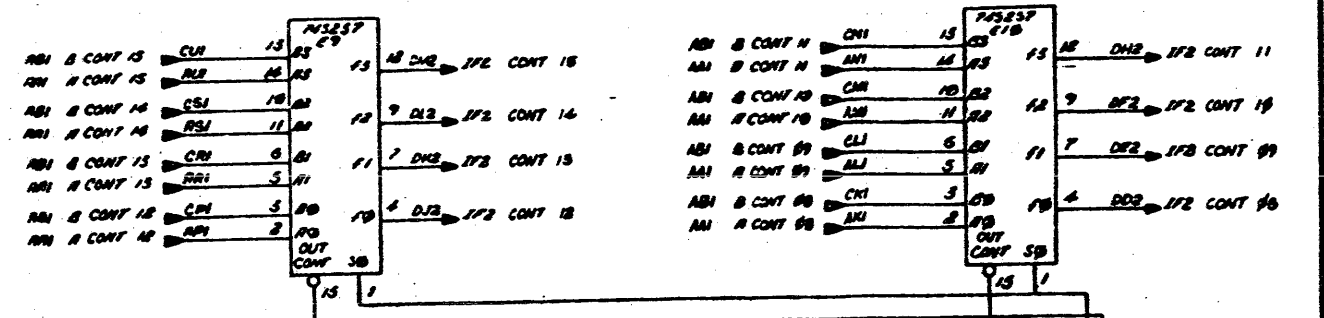
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DRG. / E. J.		12/24/68	
CHK 3. / J. J.			
ENG.			
PROJ. ENG.			TITLE
PROD.			CONTROL INTERFACE
SCALE: 2/1			SIZE CODE
SHT. 1 OF 1			NUMBER
NEXT HIGHER REV. P. (U) M7686-YA-			REV
			0 UA M7686-YA-0 K

ETCH REV.	
P.C. DESIGN DATA BRD REV 12356 L-F1	

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SPARE PINS
 RA1, RA2, RA3, CA1, CA2, CA3, CA4, CA5, CA6, CA7, CA8



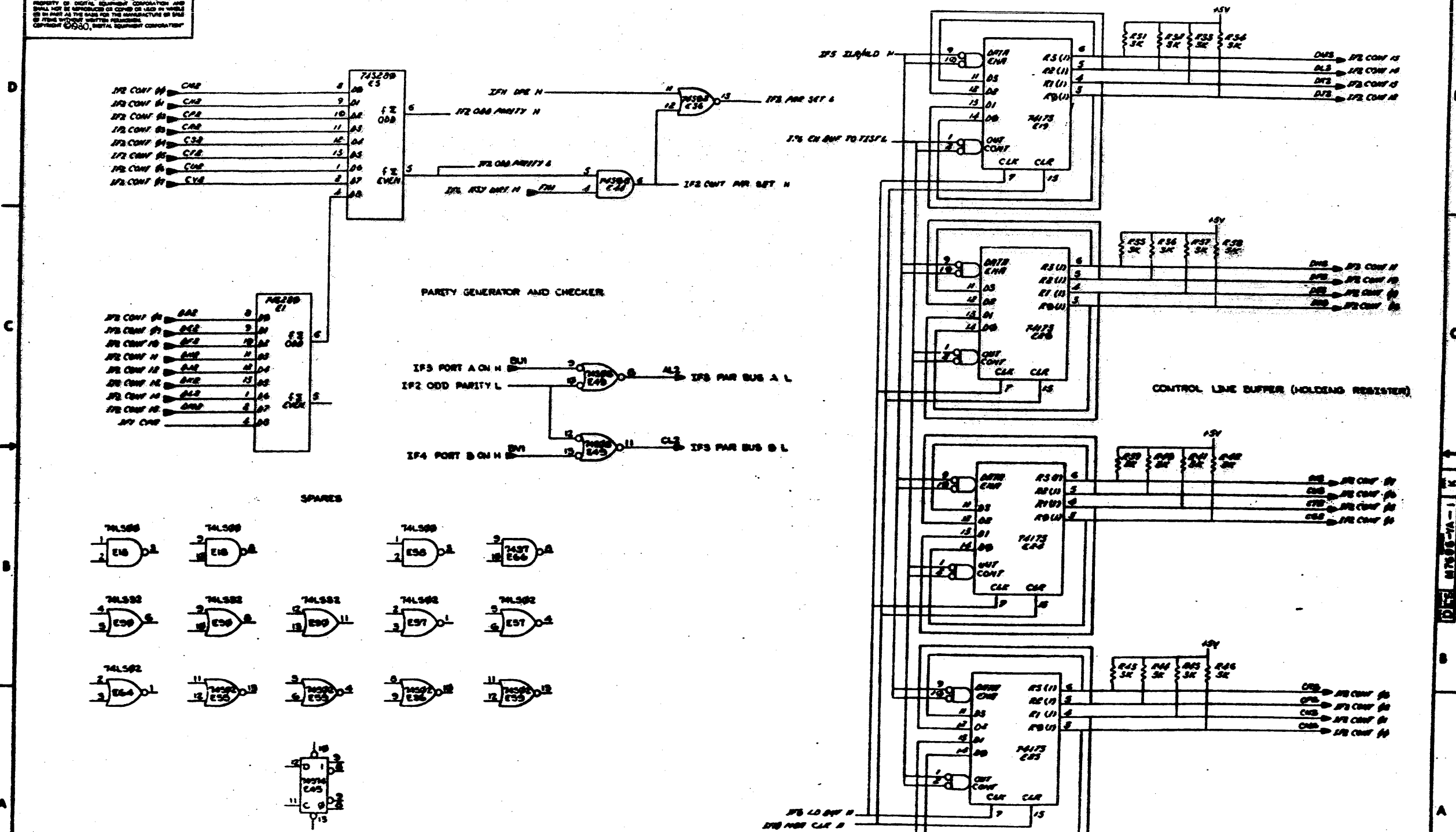
NOTE: SIGNALS NOT SUPPLIED WITH "H" OR "L" ARE MADE BUS SIGNALS WHICH ARE RECEIVED ASSERTED HIGH, AND TRANSMITTED ASSERTED LOW

CONTROL LINES AND INPUT MULTIPLEXERS

REV. 1	DATE 10/1/71	DESIGNED BY	RM05
CHKD BY	10/1/71	TITLE	CONTROL INTERFACE (IF1)
PROJ. ENG.			
PROJ. MGR.			
NEXT WORKER ASSY.			
REV. 1	DATE 10/1/71	SCALE	NONE
SHEET 1 OF 14	REV. 1	ISSUED BY	D CS
		ISSUED DATE	M7686-YA-1
		REV.	K

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DCS M7686-YA-1 K 2

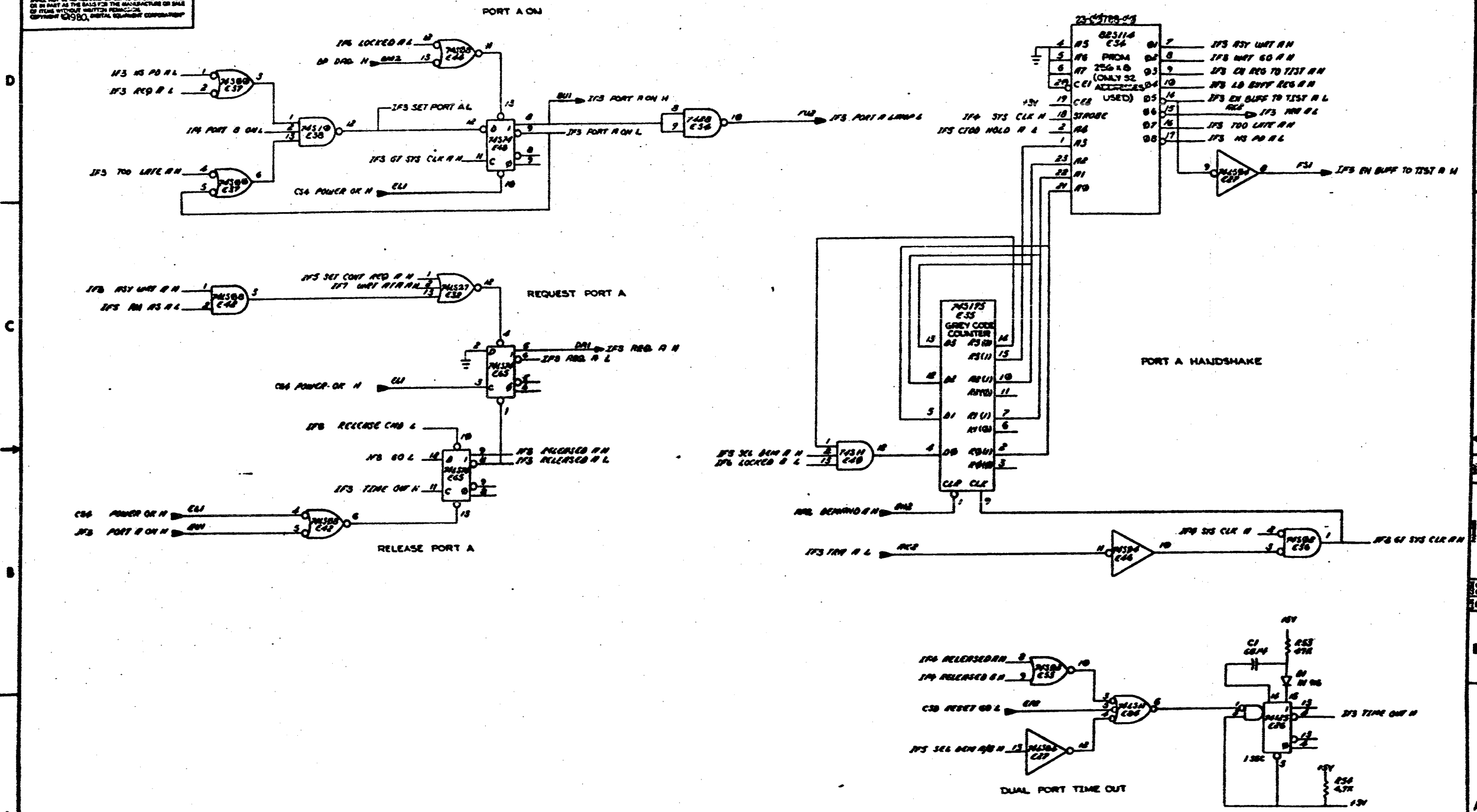


REV	DESCRIPTION	DATE

TITLE		(IF2)	REVISION	NUMBER	REV.
CONTROL INTERFACE		DCS	M7686-YA-1	K	
SCALE	NONE	SHEET	2	OF	24

8 7 6 5 4 3 2 1 CZ

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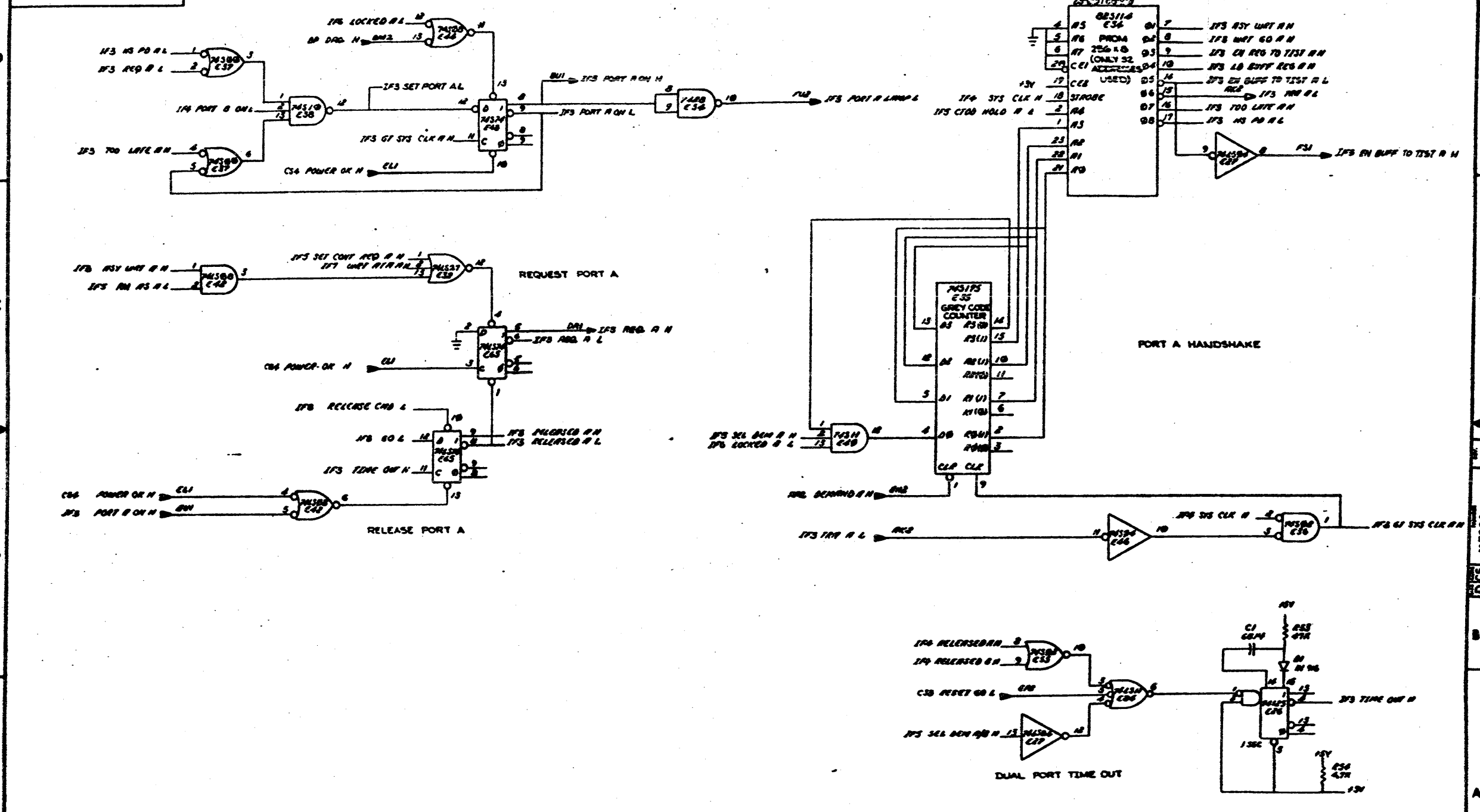


PORT A HANDSHAKE SELECTION, AND REQUEST CIRCUITS. DUAL PORT TIME OUT

REVISIONS		
ONE	CHANGE NO.	REV.

TITLE	(IF 3)	REV. 1
CONTROL INTERFACE	D/CS	M7686-YA-1
SCALE	NONE	SHEET 3 OF 16

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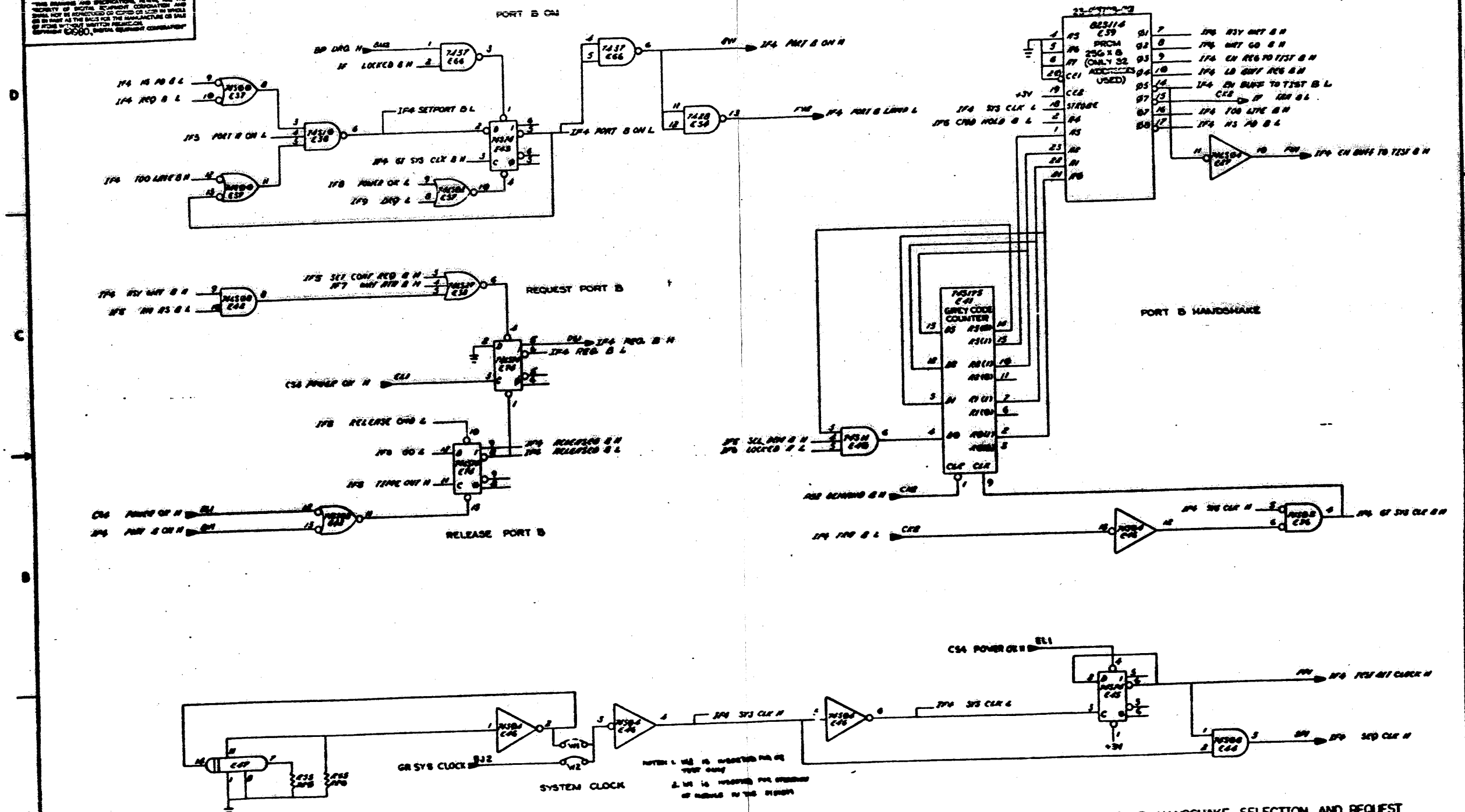


PORT A HANDSHAKE SELECTION, AND REQUEST CIRCUITS. DUAL PORT TIME OUT

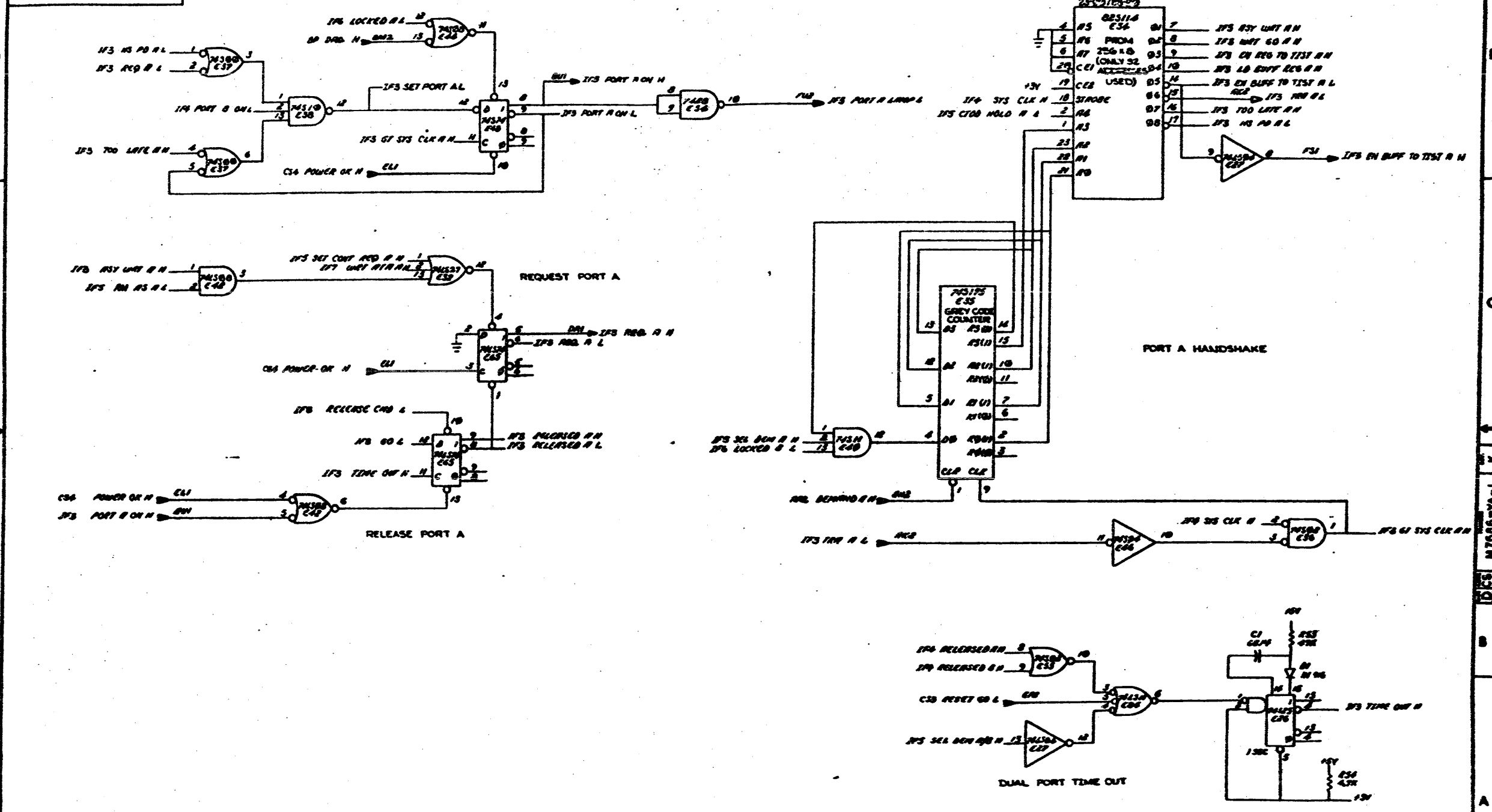
REV	DATE	BY

TITLE	(IF 3) CONTROL INTERFACE	REV. K
SCALE	NONE	SHEET 3 OF 14
DATE		

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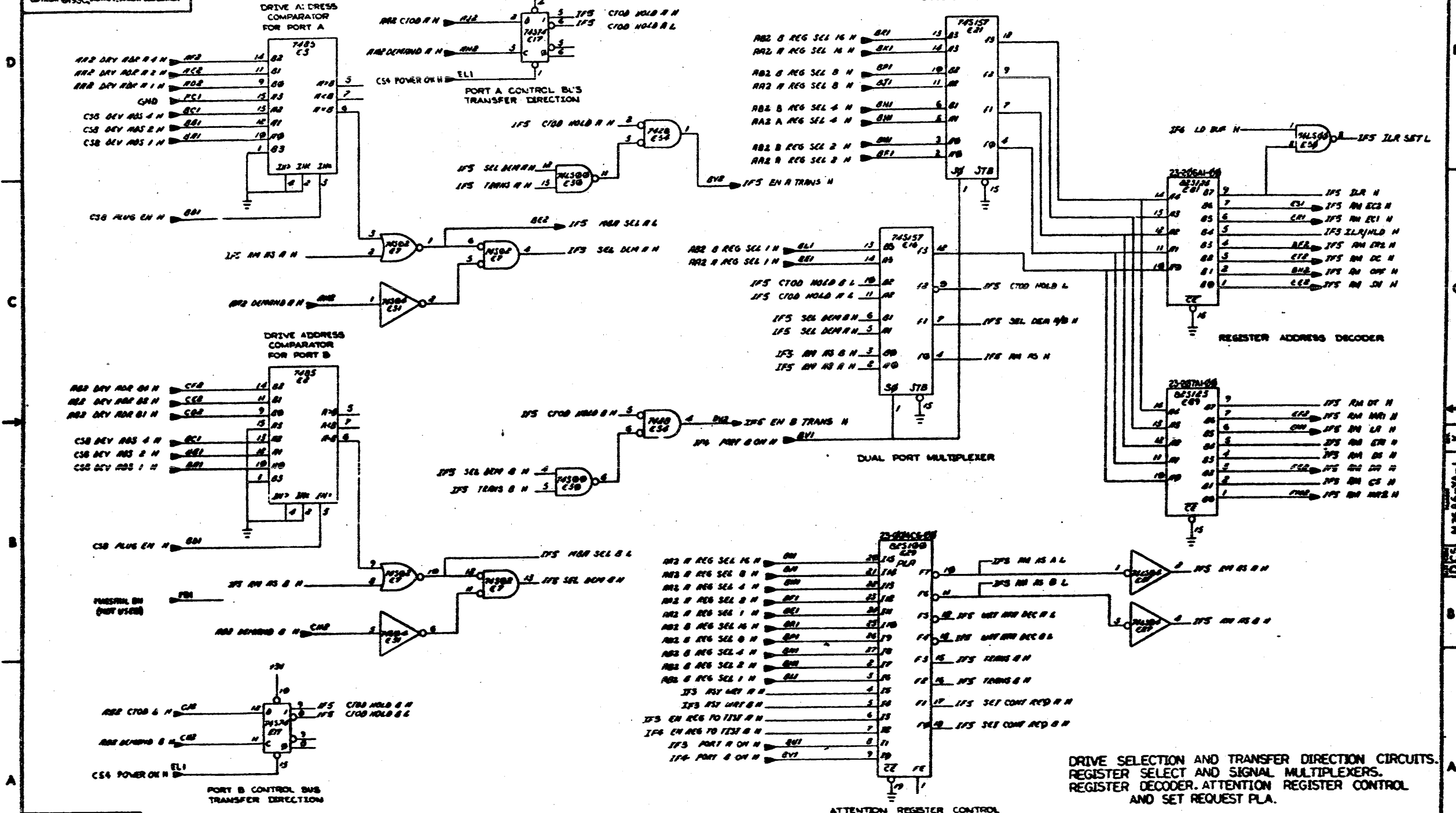
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REVISIONS		
REV.	CHANGE NO.	REV.

TITLE		(LF 3)	REV.	DATE
CONTROL INTERFACE		D CS	M7686-YA-1	K
SCALE	NOISE	SHEET 3	OF 14	DRW.

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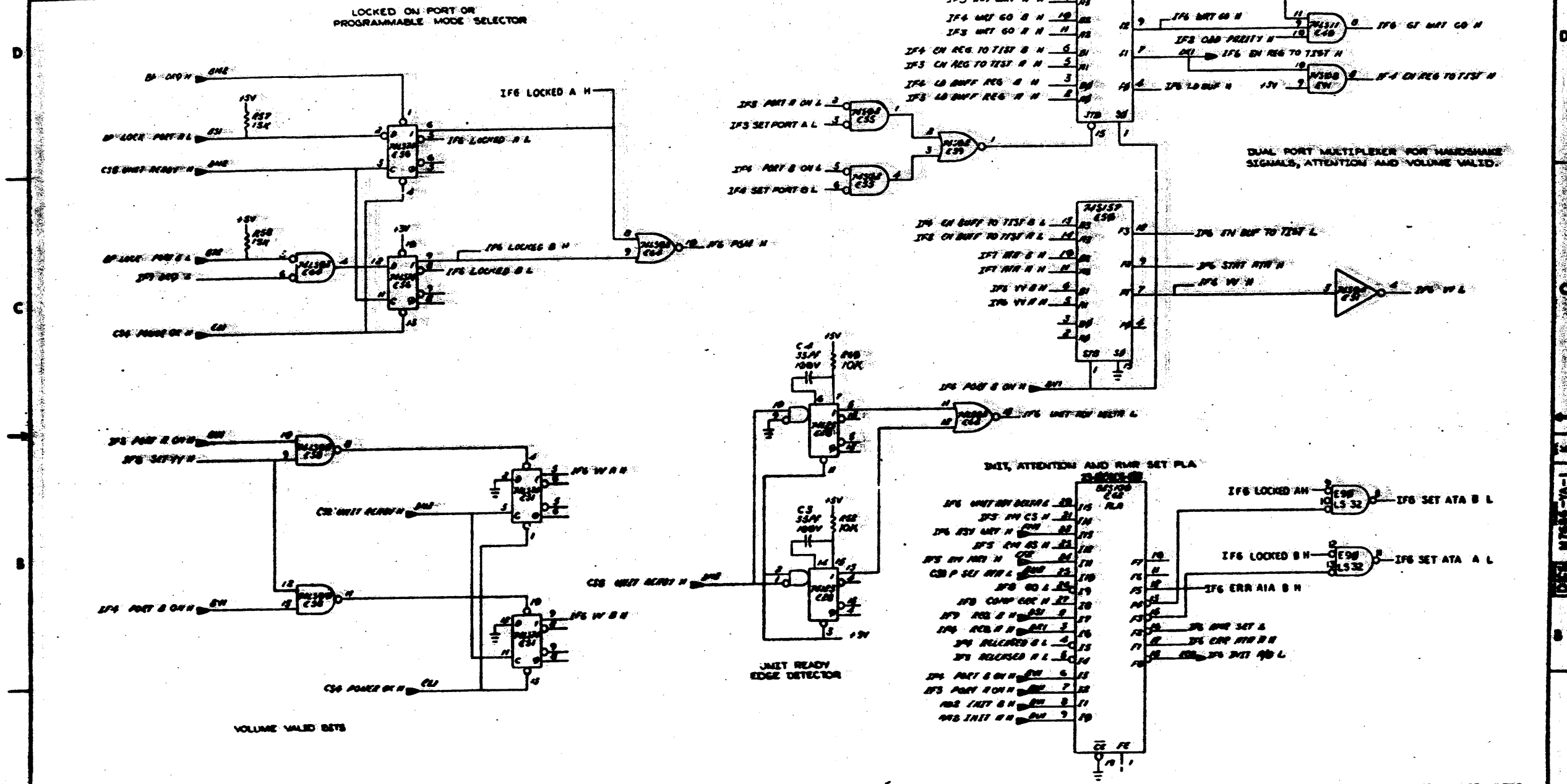


DRIVE SELECTION AND TRANSFER DIRECTION CIRCUITS. REGISTER SELECT AND SIGNAL MULTIPLEXERS. REGISTER DECODER. ATTENTION REGISTER CONTROL AND SET REQUEST PLA.

REV	CHG	CHG NO	REV

TITLE		(IF5)		DRAWING NO.		REV.	
CONTROL INTERFACE		DCS		M7686-YA-1		K	
SCALE	DATE	DRAWN	CHECKED	DATE	BY	DATE	BY

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DUAL PORT MULTIPLEXER FOR HANDSHAKE SIGNALS, ATTENTION AND VOLUME VALID.

ATTN, ATTENTION AND RMR SET PLA

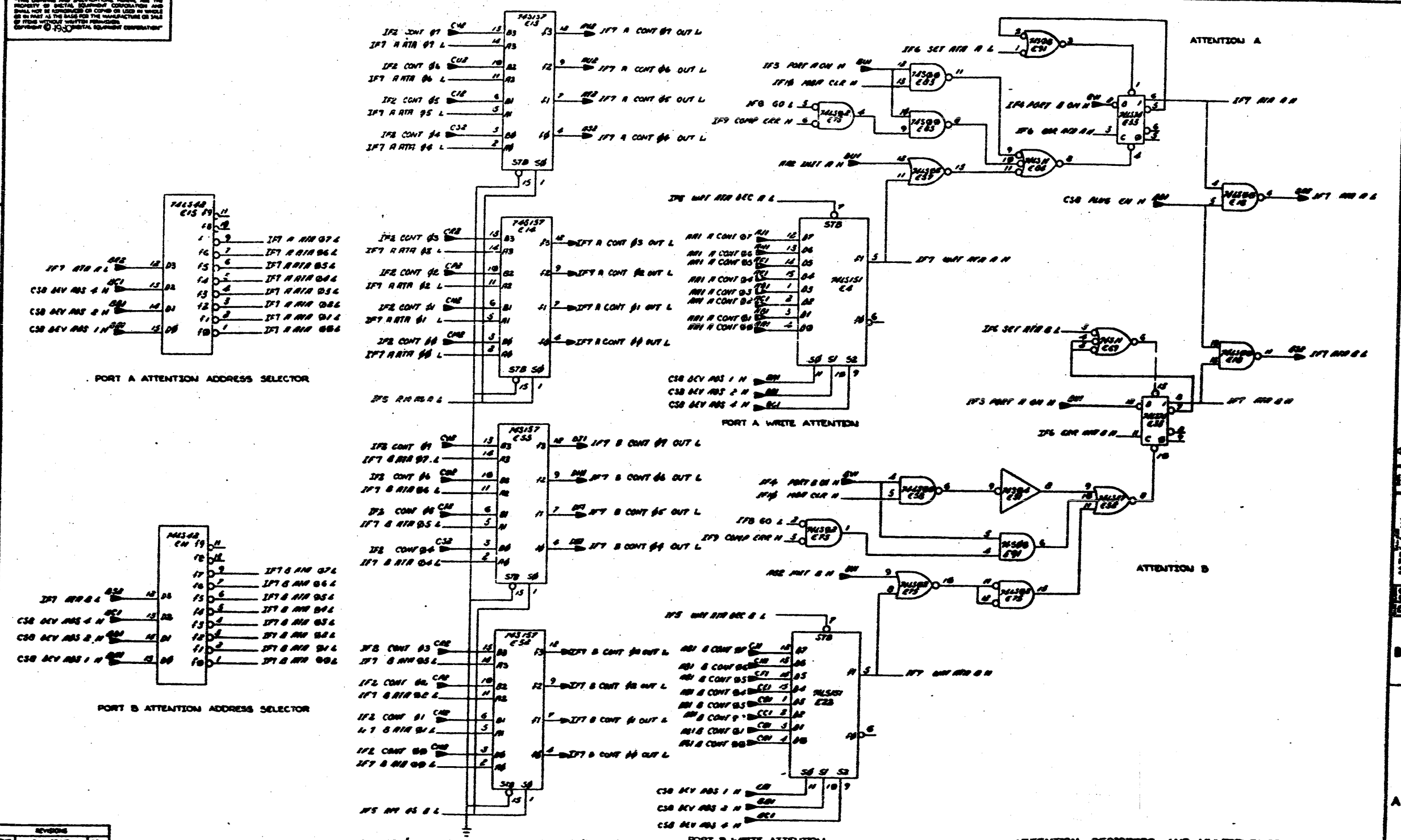
SINGLE/DUAL PORT SELECTION. VOLUME VALID BITS. UNIT READY EDGE DETECTOR. DUAL PORT MULTIPLEXER. ATTENTION AND RMR SET PLA. DUAL/SINGLE PORT SELECTION. SIGNAL MULTIPLEXERS. UNIT READY CIRCUIT.

REV	DATE	BY

TITLE	(IF6)	REV	
CONTROL INTERFACE	DCS	M7686-YA-1	K
SCALE	NONE	SHEET 6 OF 14	

DIGITAL EQUIPMENT CORPORATION

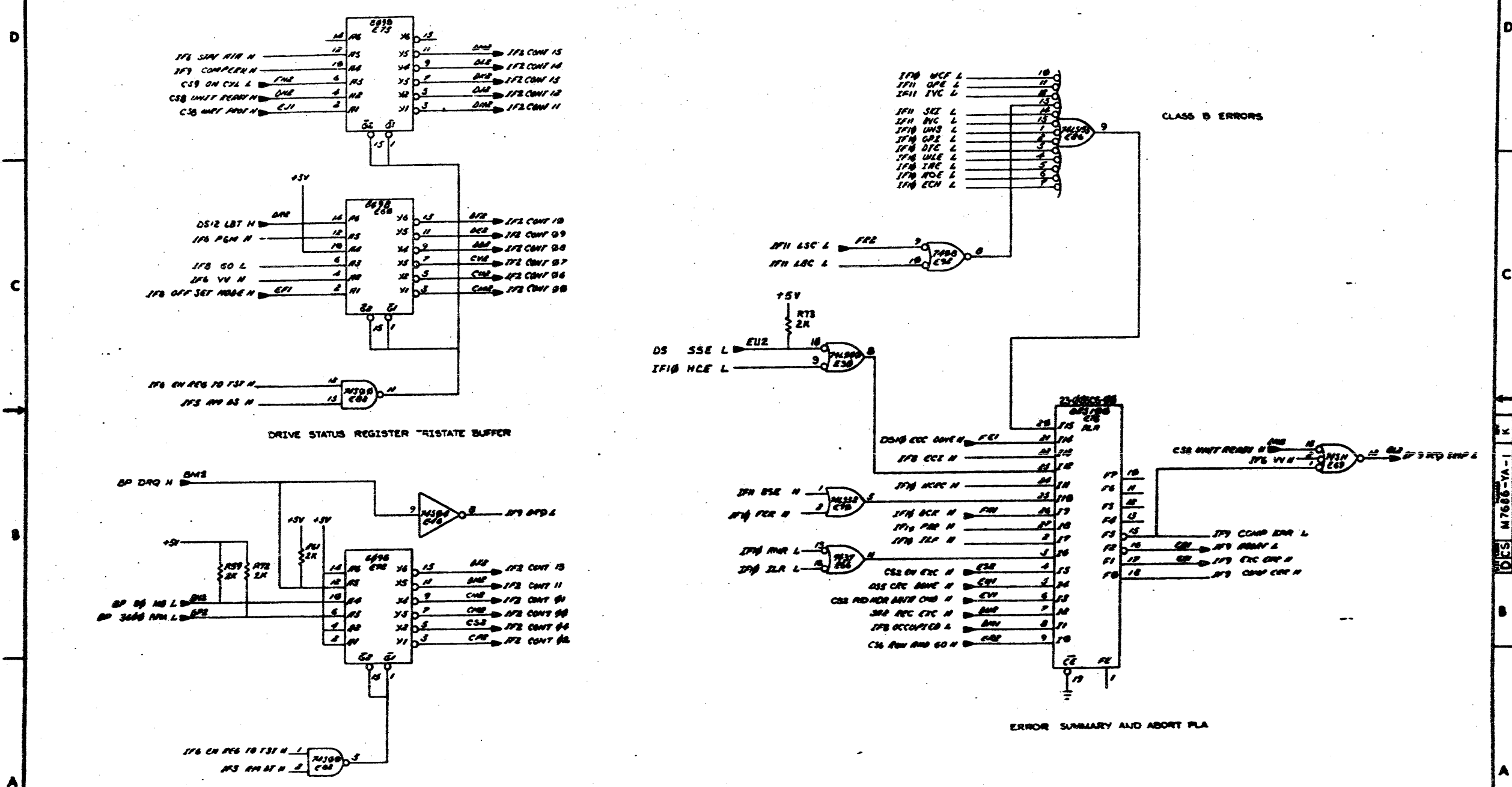
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REVISIONS		
NO.	CHANGE NO.	REV.

ATTENTION REGISTERS AND MULTIPLEXERS			
TITLE		NUMBER	
CONTROL INTERFACE (IF 7)		DCS M7686-YA-1	
SCALE	NONE	SHEET	7 OF 14

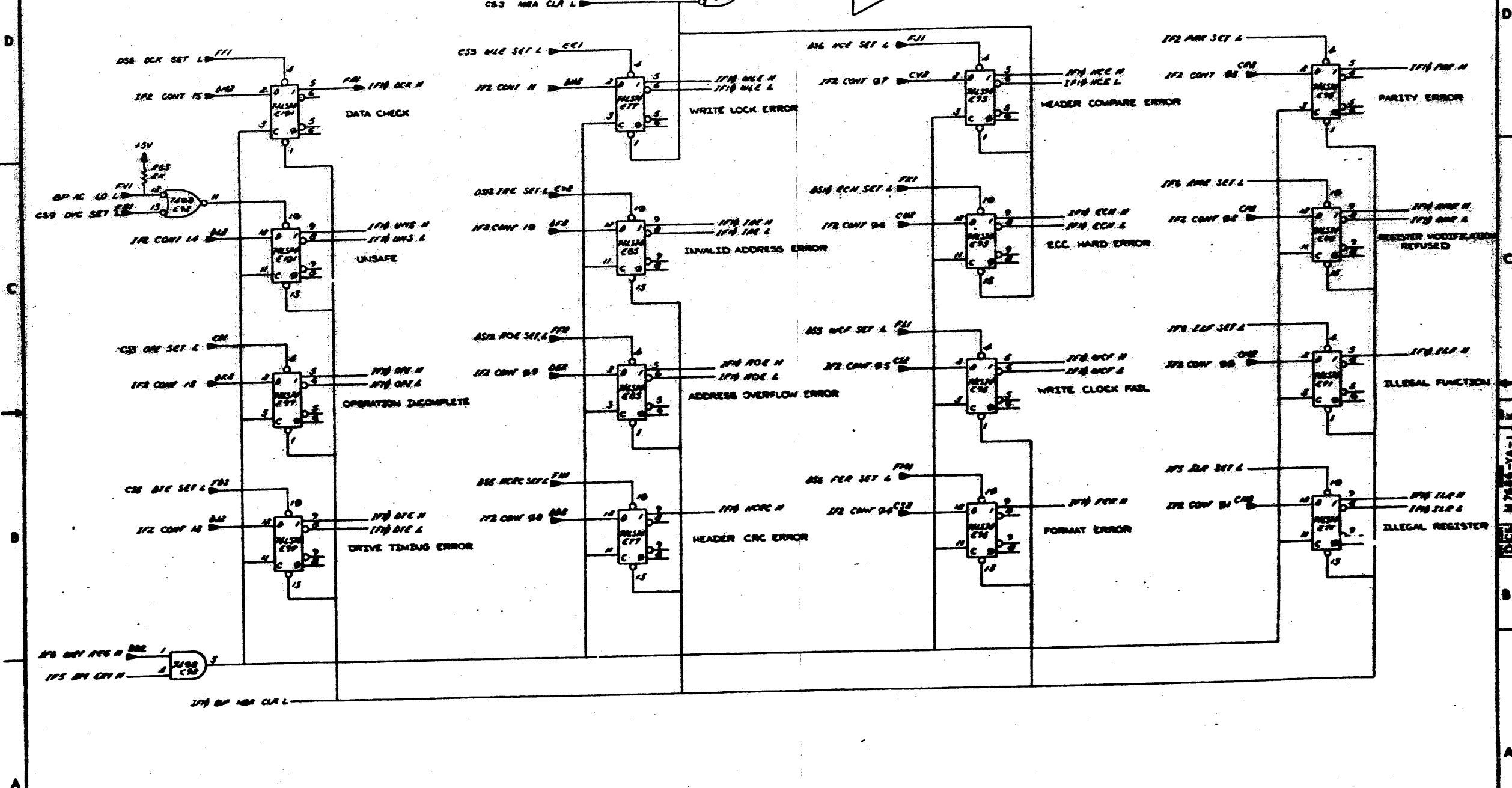
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REVISED													TITLE		DRAWING NO.		NUMBER		REV.	
CHK	CHG	NO.	REV.											CONTROL INTERFACE		DES M7686-YA-1		K		
														SCALE NONE		SHEET 9 OF 16		CZ 1		

DRIVE STATUS AND DRIVE TYPE REGISTERS.
ERROR SUMMARY.

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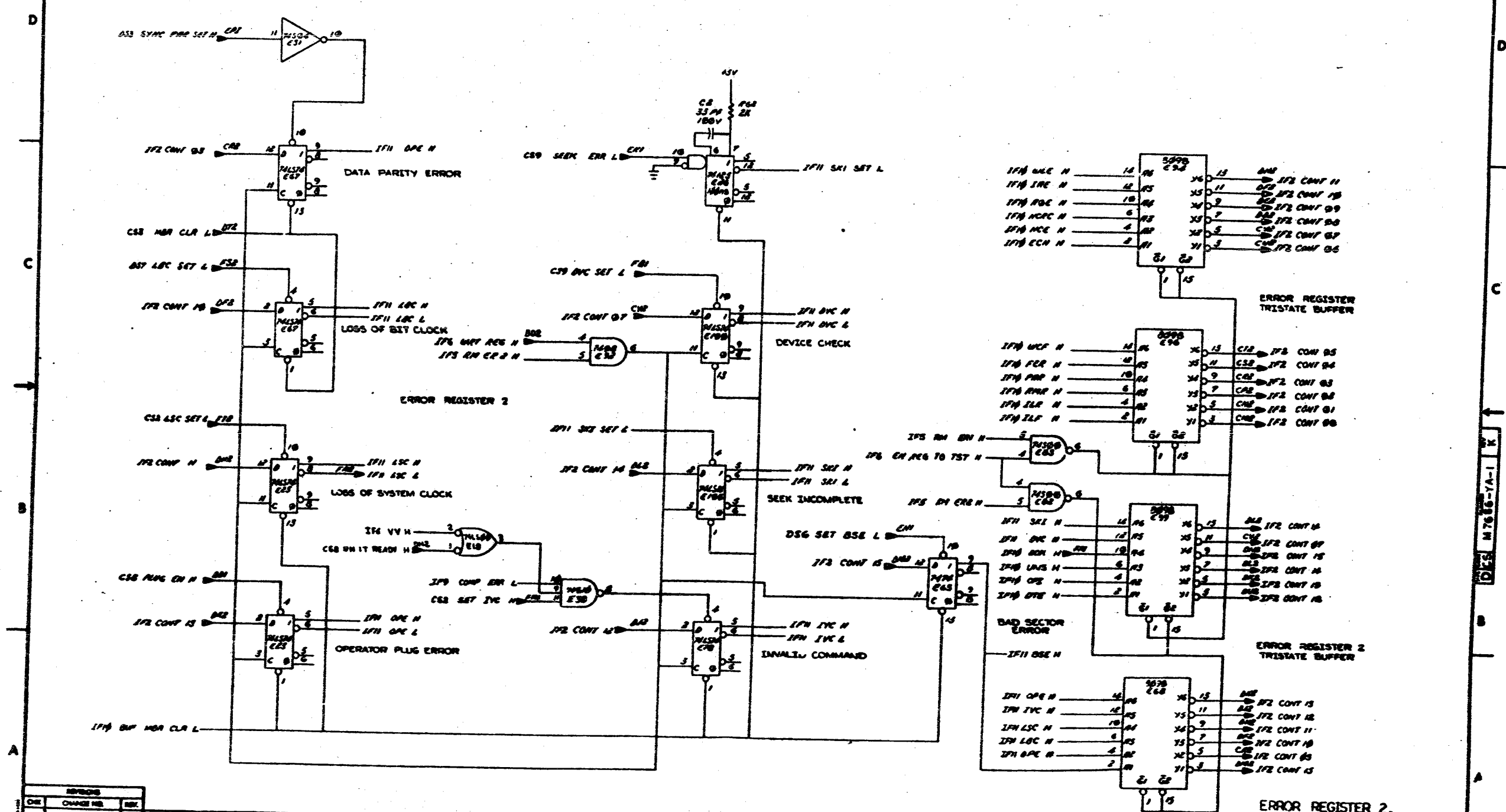
ERROR REGISTER 1

TITLE		(1F10)		NUMBER	
CONTROL INTERFACE		DCS		M7686-YA-1	
SCALE	NO. OF SHEETS	SHEET NO. OF 14	REV.		
				CZ 1	

REV.	DATE	BY

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1-VA-999LW 2



REV	DATE	CHANGE NO.	BY

ERROR REGISTER 2.
ERROR REGISTER BUFFERS.

TITLE: CONTROL INTERFACE (IF1)
 NUMBER: DCS M7686-YA-1
 SCALE: NONE
 SHEET: 11 OF 14
 REV: K

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AA1	AA1 A CONT 00
AB1	AA1 A CONT 01
AC1	AA1 A CONT 02
AD1	AA1 A CONT 03
AE1	AA1 A CONT 04
AF1	AA1 A CONT 05
AG1	AA1 A CONT 06
AH1	AA1 A CONT 07
AI1	AA1 A CONT 08
AJ1	AA1 A CONT 09
AK1	AA1 A CONT 10
AL1	AA1 A CONT 11
AM1	AA1 A CONT 12
AN1	AA1 A CONT 13
AO1	AA1 A CONT 14
AP1	GND
AQ1	AA1 A CONT 15
AV1	AA2 A CPA

BA1	CS8 DEV ADS 1 H
BB1	CS8 DEV ADS 2 H
BC1	CS8 DEV ADS 4 H
BD1	CS8 PLUG EN H
BE1	AA2 A REG SEL 1 H
BF1	AA2 A REG SEL 2 H
BH1	AA2 A REG SEL 4 H
BJ1	AA2 A REG SEL 8 H
BK1	AA2 A REG SEL 16 H
BL1	AB2 B REG SEL 1 H
BM1	AB2 B REG SEL 2 H
BN1	AB2 B REG SEL 4 H
BP1	AB2 B REG SEL 8 H
BQ1	AB2 B REG SEL 16 H
BS1	BP LOCK PORT A L
BT1	GND
BU1	IF3 PORT A ON H
BV1	IF4 PORT B ON H

CA1	AB1 B CONT 00
CB1	AB1 B CONT 01
CC1	AB1 B CONT 02
CD1	AB1 B CONT 03
CE1	AB1 B CONT 04
CF1	AB1 B CONT 05
CH1	AB1 B CONT 06
CJ1	AB1 B CONT 07
CK1	AB1 B CONT 08
CL1	AB1 B CONT 09
CM1	AB1 B CONT 10
CN1	AB1 B CONT 11
CP1	AB1 B CONT 12
CQ1	AB1 B CONT 13
CS1	AB1 B CONT 14
CT1	GND
CU1	AB1 B CONT 15
CV1	AB2 B CPA

DA1	IF7 B CONT 00 OUT L
DB1	IF7 B CONT 01 OUT L
DC1	IF7 B CONT 02 OUT L
DD1	IF7 B CONT 03 OUT L
DE1	IF7 B CONT 04 OUT L
DF1	IF7 B CONT 05 OUT L
DH1	IF7 B CONT 06 OUT L
DJ1	IF7 B CONT 07 OUT L
DK1	IF8 EN REG TO TIST H
DL1	DS6 SSE L
DM1	IF8 OCCUPIED L
DN1	CS5 MBA EBL L
DP1	IF4 SEQ CLK H
DR1	IF3 REG A H
DS1	IF4 REG B H
DT1	GND
DU1	AA2 INIT A H
DV1	AB2 INIT B H

EAI	IF9 EXC ERR H
EB1	IF9 ABORT L
EC1	DS2 CLR OFF MD L
ED1	CS3 OPI SET L
EE1	CS3 WLE SET L
EF1	IF8 OFFSET MODE H
EH1	DS6 SET BSE L
EJ1	CS8 WRT PROT H
EK1	CS8 SEEK ERR H
EL1	CS4 POWER OK H
EM1	IF5 RM LA H
EN1	IF8 OFF DR H
EP1	DS3 SYNC PAR SET H
ER1	IF5 RM EC1 H
ES1	IF5 RM EC2 H
ET1	GND
EU1	DS5 CRC DONE H
EV1	CS2 RD HDR DATA CMD H

FA1	IF9 DCK H
FB1	CS9 DVC SET L
FC1	GND
FD1	MASS FAIL B H
FE1	DS4 ECC DONE H
FF1	DS6 DCK SET L
FH1	DS9 MCRC SET L
FJ1	DS6 MCE SET L
FK1	DS4/ECH SET L
FL1	DS5 MCF SET L
FM1	DS6 FER SET L
FN1	IF8 ASY WRT H
FP1	IF4 TEST BIT CLOCK H
FQ1	CS2 SET NG H
FR1	IF3 EN BUF TO TIST A H
FS1	GND
FT1	IF4 EN BUF TO TIST B H
FV1	BP AC LO L

AA2	+5V
AB2	-15V
AC2	GND
AD2	AA2 DRV ADR A1 H
AE2	AA2 DRV ADR A2 H
AF2	AA2 DRV ADR A4 H
AG2	AA2 DEMAND A H
AJ2	AA2 CTOD A H
AK2	IF3 TRA A L
AL2	IF3 PAR BUS A L
AM2	IF7 A CONT 00 OUT L
AN2	IF7 A CONT 01 OUT L
AP2	IF7 A CONT 02 OUT L
AR2	IF7 A CONT 03 OUT L
AS2	IF7 A CONT 04 OUT L
AT2	IF7 A CONT 05 OUT L
AU2	IF7 A CONT 06 OUT L
AV2	IF7 A CONT 07 OUT L

BA2	+5V
BB2	-15V
BC2	GND
BD2	IF6 WRT REG H
BE2	IF5 MBA SEL A L
BF2	IF5 RM EA2 H
BH2	IF5 RM OF H
BJ2	GR SYS CLOCK
BK2	IF8 DECODE L
BL2	IF9 SEQ SHP L
BM2	BP DRQ H
BN2	BP 80 MB L
BP2	BP 3600 RPM L
BR2	IF7 ATA A L
BS2	IF7 ATA B L
BT2	BP LOCK PORT B L
BU2	CS2 P SET ATA L
BV2	IF5 EN A TRANS H

CA2	+5V
CB2	-15V
CC2	GND
CD2	AB2 DRV ADR B 1 H
CE2	AB2 DRV ADR B 2 H
CF2	AB2 DRV ADR B 4 H
CH2	AB2 DEMAND B H
CJ2	AB2 CTOD B H
CK2	IF4 TRA B L
CL2	IF3 PAR BUS B L
CM2	IF2 CONT 00
CN2	IF2 CONT 01
CP2	IF2 CONT 02
CQ2	IF2 CONT 03
CR2	IF2 CONT 04
CS2	IF2 CONT 05
CT2	IF2 CONT 06
CU2	IF2 CONT 07
CV2	IF2 CONT 08

DA2	+5V
DB2	-15V
DC2	GND
DD2	IF2 CONT 09
DE2	IF2 CONT 10
DF2	IF2 CONT 11
DH2	IF2 CONT 12
DJ2	IF2 CONT 13
DK2	IF2 CONT 14
DL2	IF2 CONT 15
DM2	IF2 CONT 15
DN2	CS8 UNIT READY H
DP2	IF8 SCH BK R DR W L
DR2	DS1 LBT H
DS2	IF8 READ IN CMD L
DT2	CS3 MBA CLR L
DU2	SA2 REC EXC H
DV2	IF5 EN B TRANS H

EA2	+5V
EB2	-15V
EC2	GND
ED2	IF8 INIT A/B L
EE2	IF5 RM SM H
EF2	IF5 RM MR 1 H
EH2	IF8 F0 H
EJ2	IF8 F1 H
EK2	IF8 F2 H
EL2	IF8 F3 H
EM2	IF8 F4 H
EN2	IF8 F0 H
EP2	CS3 RESET GO L
ER2	CS8 RUN AND GO H
ES2	CS2 EN EXC H
ET2	IF5 RM DC H
EU2	SPARE
EV2	DS2 IAE SET L

FA2	+5V
FB2	-15V
FC2	GND
FD2	CS3 DTE SET L
FE2	IF5 RM DA H
FF2	DS2 AOE SET L
FH2	IF8 FMT IS H
FJ2	IF8 EC1 L
FK2	IF8 MC1 L
FL2	CS3 PWR/INT L
FM2	IF5 RM MR2 H
FN2	CS8 ON CYL L
FP2	CS3 SET PULSE H
FQ2	IF4 LSC L
FR2	DS7 LBC SET L
FS2	CS2 LSC SET L
FT2	IF3 PORT A LAMP L
FV2	-IF4 PORT B LAMP L

(I/O SIGNAL LIST)

REVISED	DATE	CHANGED BY	REASON

CONTROL INTERFACE (IF12)

DCS M7686-YA-1 K

SIGNALS: NONE 12 OF 14

2 1

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STEADY DATA *1* = HIGH
 STEADY DATA *0* = LOW

DEC PART NUMBER: 23-20501
 LEFT COLUMN OF BIT DATA IS HIGH

DEC HEX OCT	DEC HEX BIT
LOC LOC LOC	DAT DAT DAT
0 00 00	377 07 11111111
1 01 01	373 05 11111011
2 02 02	177 07 01111011
3 03 03	337 07 11011111
4 04 04	373 07 11111111
5 05 05	367 07 11110111
6 06 06	367 07 11101111
7 07 07	337 07 11011111
8 08 08	235 05 10011111
9 09 09	375 07 11111101
10 0A 10	373 07 11111010
11 0B 11	373 07 11111011
12 0C 12	177 07 01111111
13 0D 13	373 07 11111011
14 0E 14	373 07 11111011
15 0F 15	373 07 11111011
16 10 20	373 07 11111011
17 11 21	373 07 11111011
18 12 22	373 07 11111011
19 13 23	373 07 11111011
20 14 24	176 07 01111010
21 15 25	372 07 11111010
22 16 26	372 07 11111011
23 17 27	373 07 11111011
24 18 28	136 06 10011110
25 19 29	136 06 01011110
26 1A 30	373 07 11111011
27 1B 31	373 07 11111011
28 1C 32	373 07 11111011
29 1D 33	176 07 01111010
30 1E 34	373 07 11111011
31 1F 37	373 07 11111011

DEC PART NUMBER: 23-207A1
 LEFT COLUMN OF BIT DATA IS HIGH

DEC HEX OCT	DEC HEX BIT
LOC LOC LOC	DAT DAT DAT
0 00 00	072 01 00000010
1 01 01	010 01 00001000
2 02 02	100 10 00010000
3 03 03	176 06 01000000
4 04 04	074 01 00000000
5 05 05	074 01 00000000
6 06 06	200 10 10000000
7 07 07	040 01 10000000
8 08 08	100 01 00000000
9 09 09	100 01 00000000
10 0A 10	000 00 00000000
11 0B 11	000 00 00000000
12 0C 12	001 01 00000000
13 0D 13	000 00 00000000
14 0E 14	000 00 00000000
15 0F 15	000 00 00000000
16 10 20	000 00 00000000
17 11 21	000 00 00000000
18 12 22	000 00 00000000
19 13 23	000 00 00000000
20 14 24	000 00 00000000
21 15 25	000 00 00000000
22 16 26	000 00 00000000
23 17 27	000 00 00000000
24 18 28	000 00 00000000
25 19 29	000 00 00000000
26 1A 30	000 00 00000000
27 1B 31	000 00 00000000
28 1C 32	000 00 00000000
29 1D 33	000 00 00000000
30 1E 34	000 00 00000000
31 1F 37	000 00 00000000

TITLE
 23-20501
 HANDSHAKE FROM AT E70
 23-20501 AT E70
 COMMAND DECODE

DEC PART NUMBER: 23-206A1
 LEFT COLUMN OF BIT DATA IS HIGH

DEC HEX OCT	DEC HEX BIT
LOC LOC LOC	DAT DAT DAT
0 00 00	000 00 00000000
1 01 01	000 00 00000000
2 02 02	000 00 00000000
3 03 03	000 00 00000000
4 04 04	000 00 00000000
5 05 05	000 00 00000000
6 06 06	000 00 00000000
7 07 07	000 00 00000000
8 08 08	001 01 00000000
9 09 09	001 01 00000000
10 0A 10	000 00 00000000
11 0B 11	000 10 00010000
12 0C 12	000 00 00000000
13 0D 13	010 01 00000000
14 0E 14	100 10 00010000
15 0F 15	100 10 00010000
16 10 20	220 10 10010000
17 11 21	220 10 10010000
18 12 22	220 10 10010000
19 13 23	220 10 10010000
20 14 24	220 10 10010000
21 15 25	220 10 10010000
22 16 26	220 10 10010000
23 17 27	220 10 10010000
24 18 28	220 10 10010000
25 19 29	220 10 10010000
26 1A 30	220 10 10010000
27 1B 31	220 10 10010000
28 1C 32	220 10 10010000
29 1D 33	220 10 10010000
30 1E 34	220 10 10010000
31 1F 37	220 10 10010000

TITLE
 23-20601
 HANDSHAKE FROM AT E81
 REGISTER ADDRESS DECODE

DEC PART NUMBER: 23-20700
 LEFT COLUMN OF BIT DATA IS HIGH

DEC HEX OCT	DEC HEX BIT
LOC LOC LOC	DAT DAT DAT
0 00 000	260 00 10110000
1 01 021	000 00 20110000
2 02 032	260 00 10110000
3 03 043	104 00 01110000
4 04 054	260 00 10110000
5 05 065	260 00 10110000
6 06 076	260 00 10110000
7 07 087	171 00 01110001
8 08 098	260 00 10110000
9 09 109	260 00 10110000
10 0A 120	260 00 10110000
11 0B 131	260 00 10110000
12 0C 142	120 10 10100000
13 0D 153	260 00 10110000
14 0E 164	160 00 01110000
15 0F 175	102 00 01110000
16 10 186	260 00 10110000
17 11 197	260 00 10110000
18 12 208	260 00 10110000
19 13 219	260 00 10110000
20 14 230	260 00 10110000
21 15 241	260 00 10110000
22 16 252	260 00 10110000
23 17 263	176 00 01111000
24 18 274	260 00 10110000
25 19 285	260 00 10110000
26 1A 296	260 00 10110000
27 1B 307	260 00 10110000
28 1C 318	260 00 10110000
29 1D 329	260 00 10110000
30 1E 340	176 00 01110000
31 1F 351	260 00 10110000

ORIGINATOR: IRENE BELLEFLEUR
 DATE ORIGINATED: 6-29-77

STEADY DATA *1* = HIGH
 STEADY DATA *0* = LOW

DEC HEX OCT	DEC HEX BIT
LOC LOC LOC	DAT DAT DAT
32 20 000	000 00 00000000
33 21 011	000 00 00000000
34 22 022	000 00 00000000
35 23 033	000 00 00000000
36 24 044	000 00 00000000
37 25 055	000 00 00000000
38 26 066	000 00 00000000
39 27 077	000 00 00000000
40 28 088	000 00 00000000
41 29 099	000 00 00000000
42 2A 110	000 00 00000000
43 2B 121	000 00 00000000
44 2C 132	000 00 00000000
45 2D 143	000 00 00000000
46 2E 154	000 00 00000000
47 2F 165	000 00 00000000
48 30 176	000 00 00000000
49 31 187	000 00 00000000
50 32 198	000 00 00000000
51 33 209	000 00 00000000
52 34 220	000 00 00000000
53 35 231	000 00 00000000
54 36 242	000 00 00000000
55 37 253	000 00 00000000
56 38 264	000 00 00000000
57 39 275	000 00 00000000
58 3A 286	000 00 00000000
59 3B 297	000 00 00000000
60 3C 308	000 00 00000000
61 3D 319	000 00 00000000
62 3E 330	000 00 00000000
63 3F 341	000 00 00000000

DEC PART NUMBER: 23-20700
 LEFT COLUMN OF BIT DATA IS HIGH

DEC HEX OCT	DEC HEX BIT
LOC LOC LOC	DAT DAT DAT
64 40 100	072 01 00000000
65 41 101	072 01 00000000
66 42 102	072 01 00000000
67 43 103	072 01 00000000
68 44 104	072 01 00000000
69 45 105	072 01 00000000
70 46 106	072 01 00000000
71 47 107	072 01 00000000
72 48 108	072 01 00000000
73 49 109	072 01 00000000
74 4A 110	072 01 00000000
75 4B 111	072 01 00000000
76 4C 112	072 01 00000000
77 4D 113	072 01 00000000
78 4E 114	072 01 00000000
79 4F 115	072 01 00000000
80 50 116	072 01 00000000
81 51 117	072 01 00000000
82 52 118	072 01 00000000
83 53 119	072 01 00000000
84 54 120	072 01 00000000
85 55 121	072 01 00000000
86 56 122	072 01 00000000
87 57 123	072 01 00000000
88 58 124	072 01 00000000
89 59 125	072 01 00000000
90 5A 126	072 01 00000000
91 5B 127	072 01 00000000
92 5C 128	072 01 00000000
93 5D 129	072 01 00000000
94 5E 130	072 01 00000000
95 5F 131	072 01 00000000

DEC PART NUMBER: 23-20700
 LEFT COLUMN OF BIT DATA IS HIGH

DEC HEX OCT	DEC HEX BIT
LOC LOC LOC	DAT DAT DAT
96 60 100	000 00 00000000
97 61 101	000 00 00000000
98 62 102	000 00 00000000
99 63 103	000 00 00000000
100 64 104	000 00 00000000
101 65 105	000 00 00000000
102 66 106	000 00 00000000
103 67 107	000 00 00000000
104 68 108	000 00 00000000
105 69 109	000 00 00000000
106 70 110	000 00 00000000
107 71 111	000 00 00000000
108 72 112	000 00 00000000
109 73 113	000 00 00000000
110 74 114	000 00 00000000
111 75 115	000 00 00000000
112 76 116	000 00 00000000
113 77 117	000 00 00000000
114 78 118	000 00 00000000
115 79 119	000 00 00000000
116 80 120	000 00 00000000
117 81 121	000 00 00000000
118 82 122	000 00 00000000
119 83 123	000 00 00000000
120 84 124	000 00 00000000
121 85 125	000 00 00000000
122 86 126	000 00 00000000
123 87 127	000 00 00000000
124 88 128	000 00 00000000
125 89 129	000 00 00000000
126 90 130	000 00 00000000
127 91 131	000 00 00000000
128 92 132	000 00 00000000
129 93 133	000 00 00000000
130 94 134	000 00 00000000
131 95 135	000 00 00000000
132 96 136	000 00 00000000
133 97 137	000 00 00000000

DEC PART NUMBER: 23-20700
 LEFT COLUMN OF BIT DATA IS HIGH

DEC HEX OCT	DEC HEX BIT
LOC LOC LOC	DAT DAT DAT
134 98 100	072 01 00000000
135 99 101	072 01 00000000
136 00 102	072 01 00000000
137 01 103	072 01 00000000
138 02 104	072 01 00000000
139 03 105	072 01 00000000
140 04 106	072 01 00000000
141 05 107	072 01 00000000
142 06 108	072 01 00000000
143 07 109	072 01 00000000
144 08 110	072 01 00000000
145 09 111	072 01 00000000
146 0A 112	072 01 00000000
147 0B 113	072 01 00000000
148 0C 114	072 01 00000000
149 0D 115	072 01 00000000
150 0E 116	072 01 00000000
151 0F 117	072 01 00000000
152 10 118	072 01 00000000
153 11 119	072 01 00000000
154 12 120	072 01 00000000
155 13 121	072 01 00000000
156 14 122	072 01 00000000
157 15 123	072 01 00000000
158 16 124	072 01 00000000
159 17 125	072 01 00000000
160 18 126	072 01 00000000
161 19 127	072 01 00000000
162 20 128	072 01 00000000
163 21 129	072 01 00000000
164 22 130	072 01 00000000
165 23 131	072 01 00000000
166 24 132	072 01 00000000
167 25 133	072 01 00000000
168 26 134	072 01 00000000
169 27 135	072 01 00000000
170 28 136	072 01 00000000
171 29 137	072 01 00000000
172 30 138	072 01 00000000
173 31 139	072 01 00000000
174 32 140	072 01 00000000
175 33 141	072 01 00000000
176 34 142	072 01 00000000

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STEADY DATA '1' = HIGH
STEADY DATA '0' = LOW

DEC PART NUMBER: 23-20561
LEFT COLUMN OF BIN DATA IS HIGH

DEC HEX OCT
LOC LOC LOC DAT DAT DAT

Table with 6 columns: DEC HEX OCT, LOC LOC LOC, DAT DAT DAT. Contains binary data for part 23-20561.

ORIGINATOR: IRENE BELLETIERE
DATE ORIGINATED: 6-27-77

TITLE
23-20561
HANDSHAKE FROM AT E78
COMMAND DECODE

DEC PART NUMBER: 23-20561
LEFT COLUMN OF BIN DATA IS HIGH

DEC HEX OCT
LOC LOC LOC DAT DAT DAT

Table with 6 columns: DEC HEX OCT, LOC LOC LOC, DAT DAT DAT. Contains binary data for part 23-20561.

ORIGINATOR: IRENE BELLETIERE
DATE ORIGINATED: 6-27-77

TITLE
23-20561
HANDSHAKE FROM AT E81
REGISTER ADDRESS DECODE

DEC PART NUMBER: 23-20711
LEFT COLUMN OF BIN DATA IS HIGH

DEC HEX OCT
LOC LOC LOC DAT DAT DAT

Table with 6 columns: DEC HEX OCT, LOC LOC LOC, DAT DAT DAT. Contains binary data for part 23-20711.

ORIGINATOR: IRENE BELLETIERE
DATE ORIGINATED: 6-27-77

TITLE
23-20711
HANDSHAKE FROM AT E89
REGISTER ADDRESS DECODE

DEC PART NUMBER: 23-00700
LEFT COLUMN OF BIN DATA IS HIGH

DEC HEX OCT
LOC LOC LOC DAT DAT DAT

Table with 6 columns: DEC HEX OCT, LOC LOC LOC, DAT DAT DAT. Contains binary data for part 23-00700.

ORIGINATOR: IRENE BELLETIERE
DATE ORIGINATED: 6-27-77

STEADY DATA '1' = HIGH
STEADY DATA '0' = LOW

PAGE 1 OF 3

Table with 6 columns: DEC HEX OCT, LOC LOC LOC, DAT DAT DAT. Contains binary data for part 23-00700.

DEC PART NUMBER: 23-00700
LEFT COLUMN OF BIN DATA IS HIGH

DEC HEX OCT
LOC LOC LOC DAT DAT DAT

Table with 6 columns: DEC HEX OCT, LOC LOC LOC, DAT DAT DAT. Contains binary data for part 23-00700.

ORIGINATOR: IRENE BELLETIERE
DATE ORIGINATED: 6-27-77

STEADY DATA '1' = HIGH
STEADY DATA '0' = LOW

PAGE 2 OF 3

Table with 6 columns: DEC HEX OCT, LOC LOC LOC, DAT DAT DAT. Contains binary data for part 23-00700.

TITLE
23-00700
HANDSHAKE FROM AT E34 AND E39

Table with 3 columns: REV, CHANGE NO, REV. Contains revision information.

Table with 4 columns: TITLE, SCALE, SHEET, NUMBER. Contains drawing metadata.

DCS M7686-YA-1 K

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STEADY DATA "1" = HIGH
STEADY DATA "0" = LOW

DEC PART NUMBER 23-20541
LEFT COLUMN OF BIT DATA IS HIGH

Table with columns: DEC HEX OCT, OCT HEX BIN, LOC LOC LOC, DAT DAT DAT. Rows 0-31.

DEC PART NUMBER 23-20741
LEFT COLUMN OF BIT DATA IS HIGH

Table with columns: DEC HEX OCT, OCT HEX BIN, LOC LOC LOC, DAT DAT DAT. Rows 0-31.

OPERATIONAL LOGIC BELLEPHONE
DATE ORIGINATED 6-9-77

TITLE: 23-20541
NON-PROM PATTERN SPEC
23-20541 AT E7B
COMMAND DECODE

DEC PART NUMBER 23-20641
LEFT COLUMN OF BIT DATA IS HIGH

Table with columns: DEC HEX OCT, OCT HEX BIN, LOC LOC LOC, DAT DAT DAT. Rows 0-31.

OPERATIONAL LOGIC BELLEPHONE
DATE ORIGINATED 6-9-77

TITLE: 23-20641
NON-PROM PATTERN SPEC
23-20641 AT E89
REGISTER ADDRESS DECODE

OPERATIONAL LOGIC BELLEPHONE
DATE ORIGINATED 6-9-77

TITLE: 23-20641
NON-PROM PATTERN SPEC
23-20641 AT E81
REGISTER ADDRESS DECODE

DEC PART NUMBER 23-20700
LEFT COLUMN OF BIT DATA IS HIGH

Table with columns: DEC HEX OCT, OCT HEX BIN, LOC LOC LOC, DAT DAT DAT. Rows 0-31.

OPERATIONAL LOGIC BELLEPHONE
DATE ORIGINATED 6-9-77

Table with columns: DEC HEX OCT, OCT HEX BIN, LOC LOC LOC, DAT DAT DAT. Rows 0-31.

STEADY DATA "1" = HIGH
STEADY DATA "0" = LOW

DEC PART NUMBER 23-20700
LEFT COLUMN OF BIT DATA IS HIGH

Table with columns: DEC HEX OCT, OCT HEX BIN, LOC LOC LOC, DAT DAT DAT. Rows 0-31.

OPERATIONAL LOGIC BELLEPHONE
DATE ORIGINATED 6-9-77

Table with columns: DEC HEX OCT, OCT HEX BIN, LOC LOC LOC, DAT DAT DAT. Rows 0-31.

STEADY DATA "1" = HIGH
STEADY DATA "0" = LOW

TITLE: 23-20700
NON-PROM PATTERN SPEC
HANDSHAKE FROM AT E14 AND E39

Table with columns: REV, LONG, CHANGE NO, REV.

Table with columns: TITLE, SCALE, SHEET, NUMBER, REV. Includes 'CONTROL INTERFACE', 'DCS M7686-YA-1', 'K'.

LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION YA	REFERENCE DESIGNATOR
1	1	D-MD-5012486-0-0	5012486-00	M7686	1	
2	2		1000082-00	68 MFD 15V 10% 150D S.TA	1	C1
3	3		1001610-01	.01 MFD 100V -20+80 ZSU DISC	92	C5-C95,C102
4	4		1017472-00	10 MFD 35V +50-10% AL EL	6	C96,C97,C98,C99,C100,C101
5	5		1000009-00	33.0 MMF 100V 5X200PPM DM15S	3	C2,C3,C4
6	6		1105275-00	D 672 TR= 15NS PIV= 60V SI	1	D1
7	7		1210711-02	HANDLE,MODULE,HEX	1	
8	8		1302388-00	2.0 K .25 W 5.0 % CC	14	R59,R61-R73
9	9		1300271-00	220.0 .25 W 5.0 % CC	2	R55,R56
10	10		1300432-00	3.0 K .25 W 5.0 % CC	51	R1-R51
11	11		1300447-00	4.70 K .25 W 5.0 % CC	1	R54
12	12		1302177-00	47.0 K .25 W 5.0 % CC	1	R53
13	13		1611300-00	DELAY= 10-125NS,5TAPS FAST RISE	1	E47
14	14		1910224-00	DEC 7485 COMPARATOR-4BIT	2	E2,E3
15	15		1910476-00	DEC 74123 ONE SHOT-DUAL,RETRIG	2	E26,E28
16	16		1910532-00	74S00 NAND GATE-QUAD 2IN	3	E37,E82,E83
17	17		1910534-00	74S04 INVERTER GATE-HEX 1I	2	E31,E4A
18	18		1910536-00	74S10 NAND GATE-TRIPLE 3IN	1	E38
19	19		1910537-00	74S11 AND GATE-TRIPLE 3INP	2	E40,E69
20	20		1910544-00	74S74 FF-D DUAL,EDGE TRIGG	3	E17,E43,E45
21	21		1910548-00	74S157 MUX 1 OF 2 (QUAD)	8	E13,E14,E16,E21,E48,E50,E52,E53
22	22		1910957-00	74S175 FF-D QUAD COMMON CLO	2	E35,E41
23	23		1911330-00	74173 FF-D QUAD,TRISTATE	4	E19,E20,E24,E25
24	24		1911573-00	74S280 PARITY GEN/CHKR,9BIT	2	E1,E5
25	25		1911641-00	SN 74S257 MUX,QUAD 2 TO 1	5	E6,E8,E9,E10,E12
26	26		1912388-00	74S02 NOR GATE-QUAD 2IN,PO	4	E7,E36,E55,E59
27	27		1912389-00	74S08 AND GATE-QUAD 2IN,PO	3	E44,E49,E91
28	28		1912697-00	LS174 FF-D HEX W/CLEAR	1	E87
29	29		1912799-00	LS00 NAND-GATE-QUAD 2IN,P	3	E18,E30,E58
30	30		1912801-00	LS02 NOR-GATE-QUAD 2IN	3	E57,E64,E75

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REVISION HISTORY		BASIC PART NO: M7686		DRN:	DATE: 6-FEB-80	DIGITAL			
ENG:	ECO NUMBER	REV	SECTION A OF A	<i>K. Davis</i>		TITLE PARTS LIST			
	INITIAL	K	SECTION VARIATION INDEX	<i>R. Michaud</i>	DATE: 21-FEB-80	CONTROL INTERFACE			
			[A] YA			DOCUMENT NUMBER			
			[B]			SIZE	CODE	NUMBER	REV
			[C]	DES. ENG: C. DUNIGAN	DATE: 21-FEB-80	K	PL	M7686-YA-DBP	K
			[D]						
			[E]	RESP. ENG.: C. DUNIGAN	DATE: 21-FEB-80				
			[F]						
			[G]						
			[H]	MFG. ENG.: D. CLAFLIN	DATE: 21-FEB-80				
			[I]						
			[J]						
			[K]						
			[L]						
			[M]	ASSEMBLY NUMBER:	TOP DOCUMENT NUMBER:	FILE NAME:		EDIT #	
			[N]	D-UA-M7686-YA-0	#B-DD-M7686-YA	Z1188K.PLS		13	

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AUTOMATED BY PRTLST.3L(40)

PARTS LIST

SHEET A2 OF A2

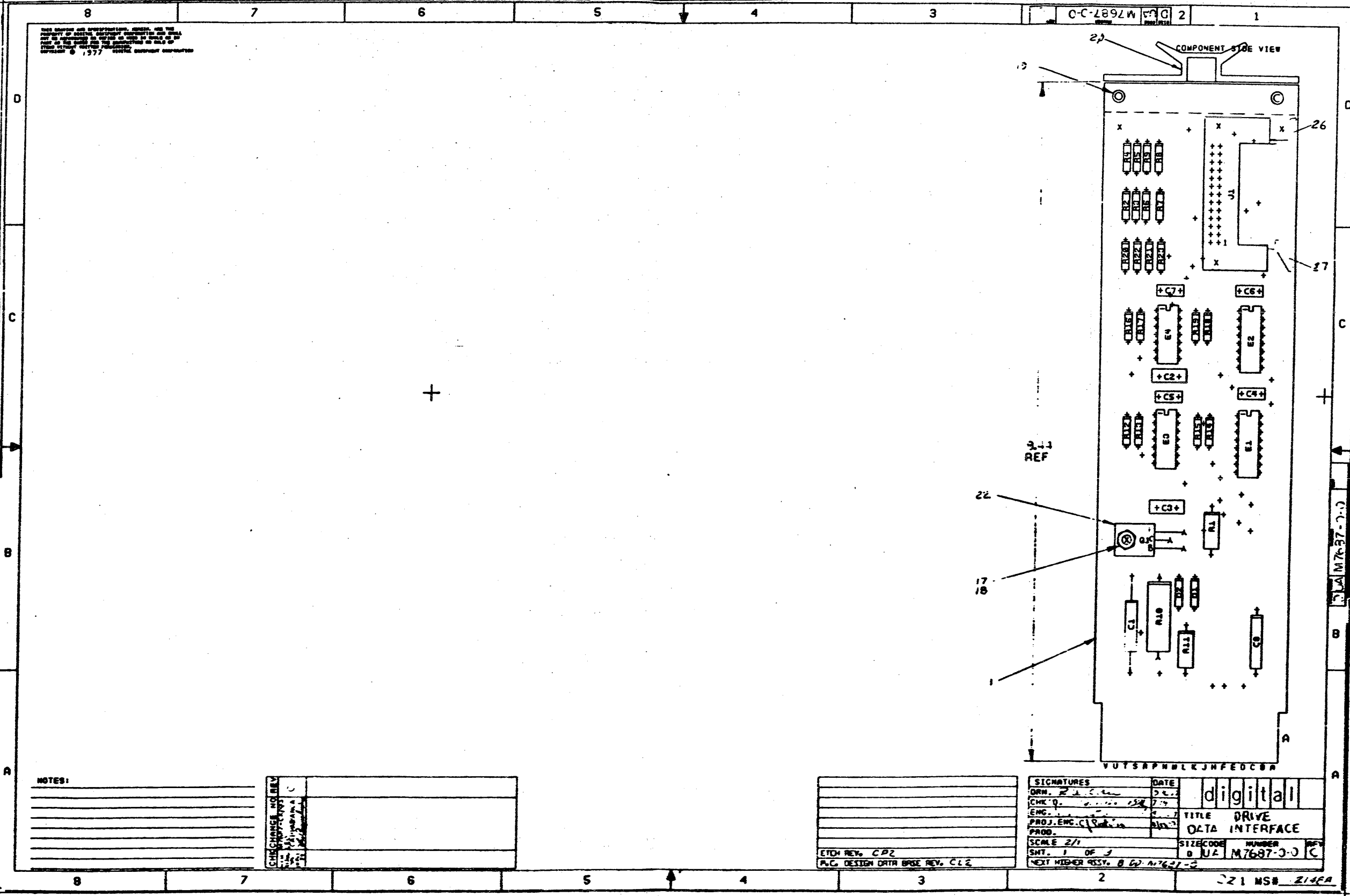
LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION YA	REFERENCE DESIGNATOR
31	31	1912803-00	LS04 INVERTER GATE-HEX 1I	1	E27
32	32	1912805-00	LS08 AND GATE-QUAD 2IN,FO	1	E42
33	33	1912808-00	LS11 AND GATE-TRIPLE 3IN	1	E84
34	34	1912813-00	LS27 NOR GATE-TRIPLE 3IN	1	E32
35	35	1912819-00	LS42 DECODER,BCD-DECIMAL	2	E11,E15
36	36	1912824-00	LS74 FF-D DUAL,EDGE TRIGG	18	E23,E33,E51,E56,E63,E65,E67,E71, CONT E74,E77,E78,E85,E93,E95,E97,E98, CONT E100,E101
37	37	1912839-00	LS133 NAND GATE-POS	1	E86
38	38	1912844-00	LS151 MUX 1 OF 8 & DATA	2	E4,E22
39	39	1912853-00	LS175 FF-D QUAD	1	E88
40	40	1913312-00	7428P NOR GATE-QUAD 2IN POS	1	E54
41	41	23004C6-00	C6-01	1	E29
42	42	23007B8-00	B8-01	2	E34,E39
43	43	23206A1-00	A1-07	1	E81
44	44	23205A1-00	A1-03,A1-04,A1-05	1	E70
45	45	1914087-00	8098 BUFFER GATE-HEX 2IN,	9	E60,E68,E72,E73,E79,E80,E94,E96, CONT E99
46	46	9000024-01	EYELET, ROLLED FLANGE, .121 OD X	12	E90
47	47	1912816-00	LS32 OR GATE-QUAD 2IN,POS	1	E61
48	48	1903547-00	7474 FF-D DUAL,EDGE TRIGG	1	E92
49	49	1910155-00	DEC 7408 AND GATE,POS.QUAD 2I	1	E66
50	50	1910091-00	DEC 7437 AND GATE-QUAD 2IN,BU	1	
51	51	9105740-55	WIRE(WRAP)30AWG UL1423	A/R	
52	52	1300496-00	15.0 K .25 W 5.0 X CC	2	R57,R58
53	53	23005C6-00	C6-01	1	E76
54	54	23207A1-00	A1-C7	1	E89
55	55	23006C6-00	C6-01	1	E62
56	56	9009185-00	JUMPER, WIRE, INSULATED, BLACK B	2	W1,W2
57	57	1300479-00	10.0 K .25 W 5.0 X CC	2	R52,R60
58	58	9107267-11	TUBING,THIN WALL,.034ID UL	A/R	

59 NOTE: NOTE 1: W1 (9009185-00) SHOULD NOT BE AUTOMATICALLY INSERTED.

D	I	G	I	T	A	L	TITLE	SECTION A OF A	SIZE	CODE	DOCUMENT NUMBER	REV
							CONTROL INTERFACE		K	PL	M7686-YA-DBP	K

ALL DIMENSIONS AND SPECIFICATIONS, UNLESS NOTED OTHERWISE, ARE TO BE IN ACCORDANCE WITH THE MIL-STD-883C TEST METHOD 2000. THE PART IS TO BE MANUFACTURED TO THE DIMENSIONS AND SPECIFICATIONS SHOWN ON THIS DRAWING. THE PART IS TO BE MANUFACTURED TO THE DIMENSIONS AND SPECIFICATIONS SHOWN ON THIS DRAWING. THE PART IS TO BE MANUFACTURED TO THE DIMENSIONS AND SPECIFICATIONS SHOWN ON THIS DRAWING.

C-C-2892W P.C. 2



NOTES:

CHANGE NO.	REV.	DATE	BY

SIGNATURES	DATE

digital	TITLE	DRIVE

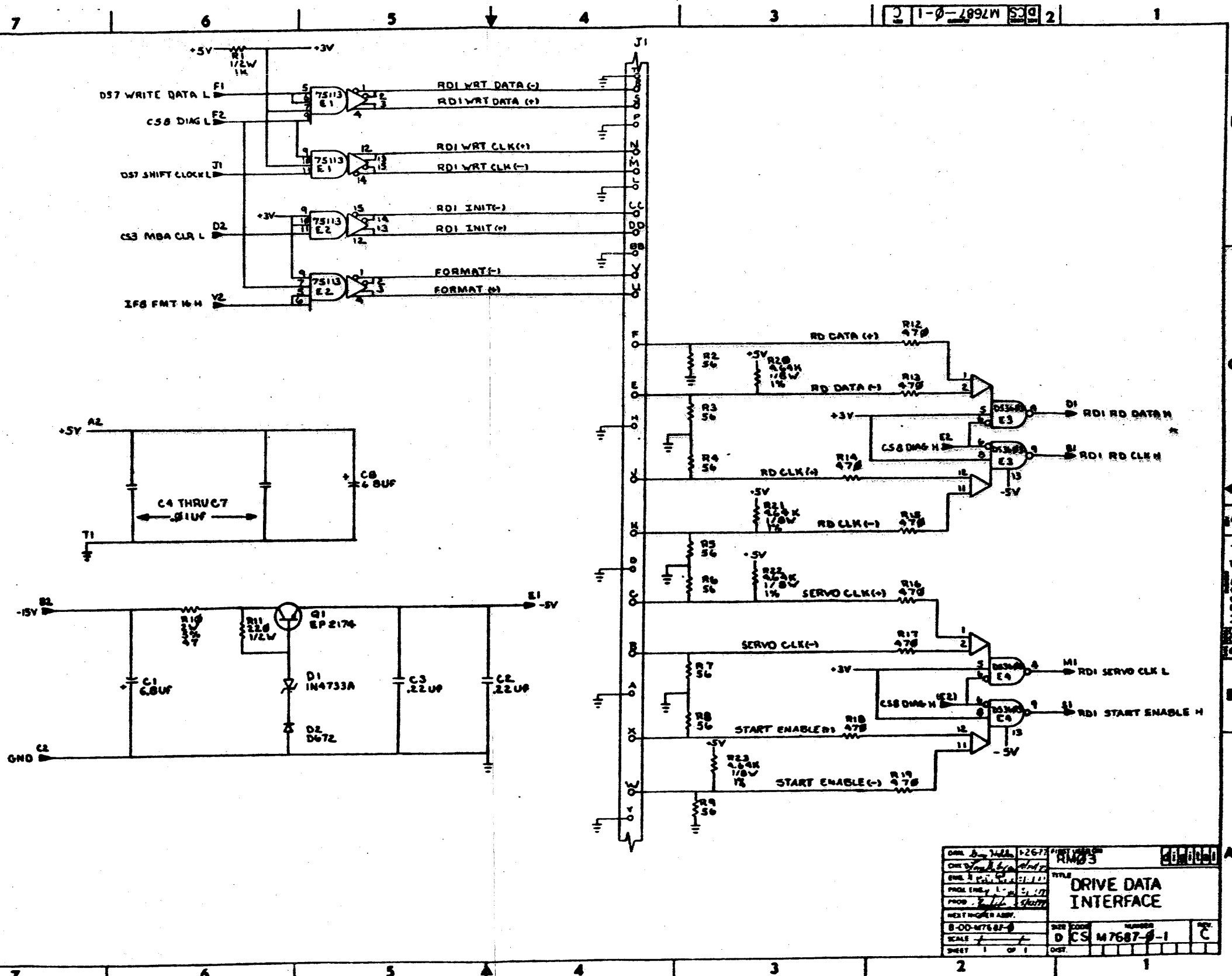
ETCH REV. CP2
P.C. DESIGN DATA BASE REV. CL2

SCALE 2/1
SMT. 1 OF 3
NEXT HIGHER ASSY. B 62-A7621-0

SIZE CODE NUMBER
D U.A. M7687-0-0 C

21 MSB 214FA

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DATE	1/26/78	REV	1
DESIGNED BY	RMG/S	TITLE	DRIVE DATA INTERFACE
CHK'D BY		PROJ ENGR	L. J. S. / 5/77
PROJ ENGR		PROD	
MELTINGER ARMY			
8-00-M7687-0	REV	CODE	
SCALE		D	CS M7687-0-1
SHEET	1	OF	1

REV	DATE	BY
1	1/26/78	RMG/S
2	5/7/77	L. J. S.
3		
4		
5		
6		
7		
8		
9		
10		

DIGITAL EQUIPMENT CORPORATION

PARTS LIST

QUANTITY / VARIATION

NOTES:

MADE BY	JVV	CHECKED	<i>Jim Mistry</i>	SECTION	1
DATE	12/16/76	DATE	2/2/77	ISSUED SECTION	1
ENG	<i>John Bell</i>	PROD	<i>John Bell</i>		
DATE	<i>5/11/77</i>	DATE	<i>5/11/77</i>		

USED ON: OPTION / MODEL
RM03

ITEM NO.	DRAWING NO.	PART NO.	DESCRIPTION
	D-MD-5012487-0-0		DRILL & ETCH DRAWING
	D-UA-M7687-0-0		UNIT ASSY.
	B-DD-M7687-0		DWG. DIRECTORY
	D-CS-M7687-0-1		CIRCUIT SCHEMATIC
1		5012487	ETCHED CIRCUIT BOARD
2		10-05306-00	CAP, 6.8 UF 10% 35V
3		10-10274-00	CAP, 0.22 UF 50V
4		10-01610-01	CAP, .01 UF 100V DISC
5		10-00082-00	CAP, 68UF 15V 10%
6		11-05275-00	DIODE, D672
7		11-09943-00	DIODE, IN4733A
8		13-00274-00	RES, 220 1/2W 5%
9		13-00364-00	RES, 1K 1/2W 5%
10		13-14023-00	RES, 47 2W 5%
11		13-00316-00	RES, 470 1/2W 5%
12		13-02602-00	RES, 56 1/2W 5%
13		13-01802-00	RES 4.64K 1/8K 1% MF
14		15-12589-00	TRANS, PNB, 10W, 60V, 3A
15		19-11341-00	I.C. DEC 75113
16		19-14091-00	I.C. DS3603
17		90-06010-04	SCREW BHM #4-40 x 5-16 LG

REF																				
REF																				
REF																				
REF																				
1																				
2																				
2																				
4																				
1																				
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1																				
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1																				

REF DESIGNATION

C8, C1
C2, C3
C4, C5, C6, C7
~~C1~~
D2
D1
R11
R1
R10
R12 thru R19
R2 thru R9
R20 thru R23
Q1
E1, E2
E3, E4

E.C.O. NO.
000012
M7687-C
CX 0003

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EN-01140A-16-R27013251 DRB 125

TITLE
DRIVE DATA INTERFACE

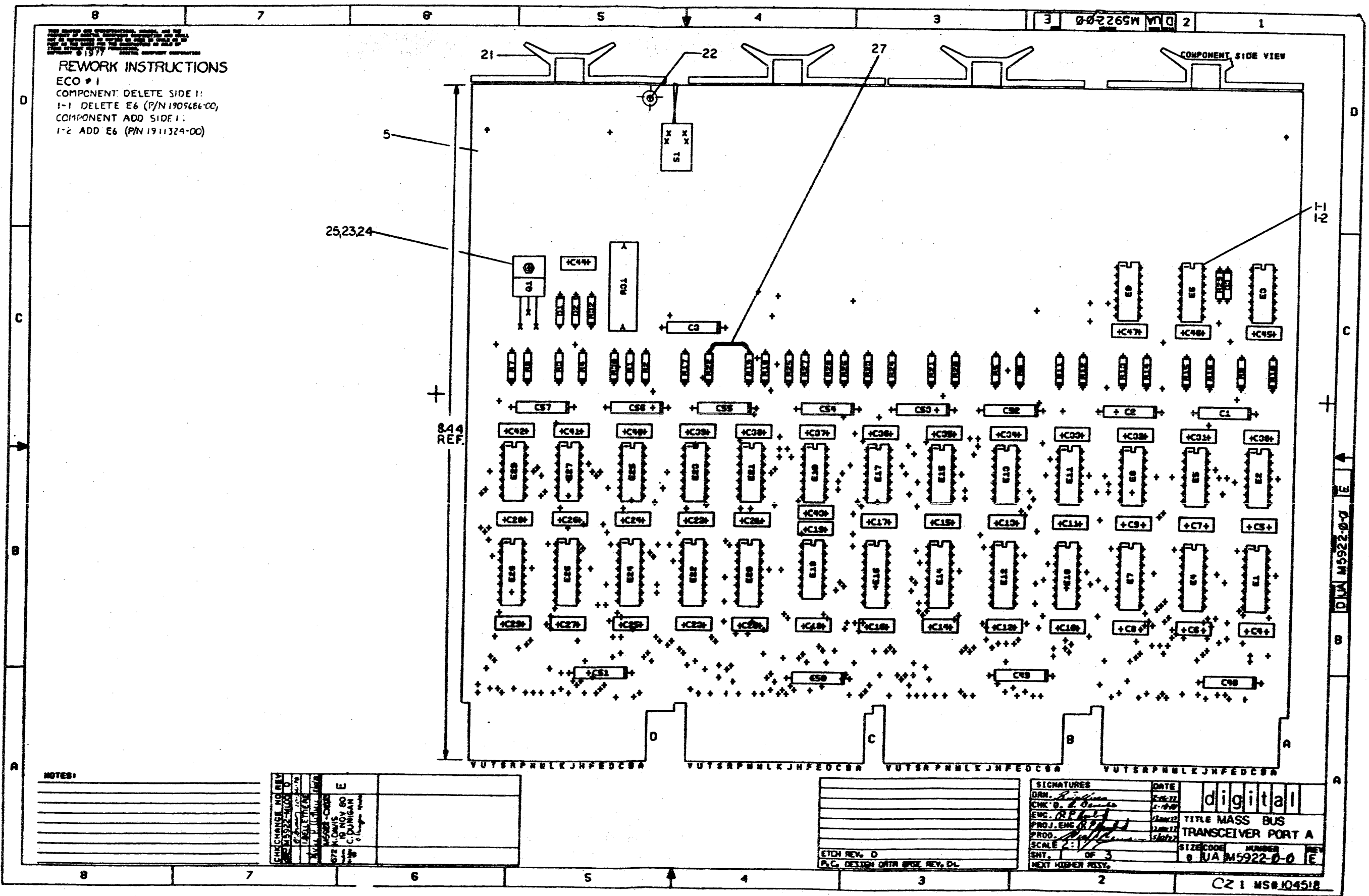
ASSY NO.
D-UA M7687-0-0
SHEET 1 OF 2

SIZE	CODE	NUMBER	REV.
B	PL	M7687-0-0	C

INSERTION PARTS LIST DATA BASE REV 1

REWORK INSTRUCTIONS

ECO # 1
 COMPONENT DELETE SIDE 1:
 1-1 DELETE E6 (P/N 1909686-00),
 COMPONENT ADD SIDE 1:
 1-2 ADD E6 (P/N 1911324-00)



NOTES:

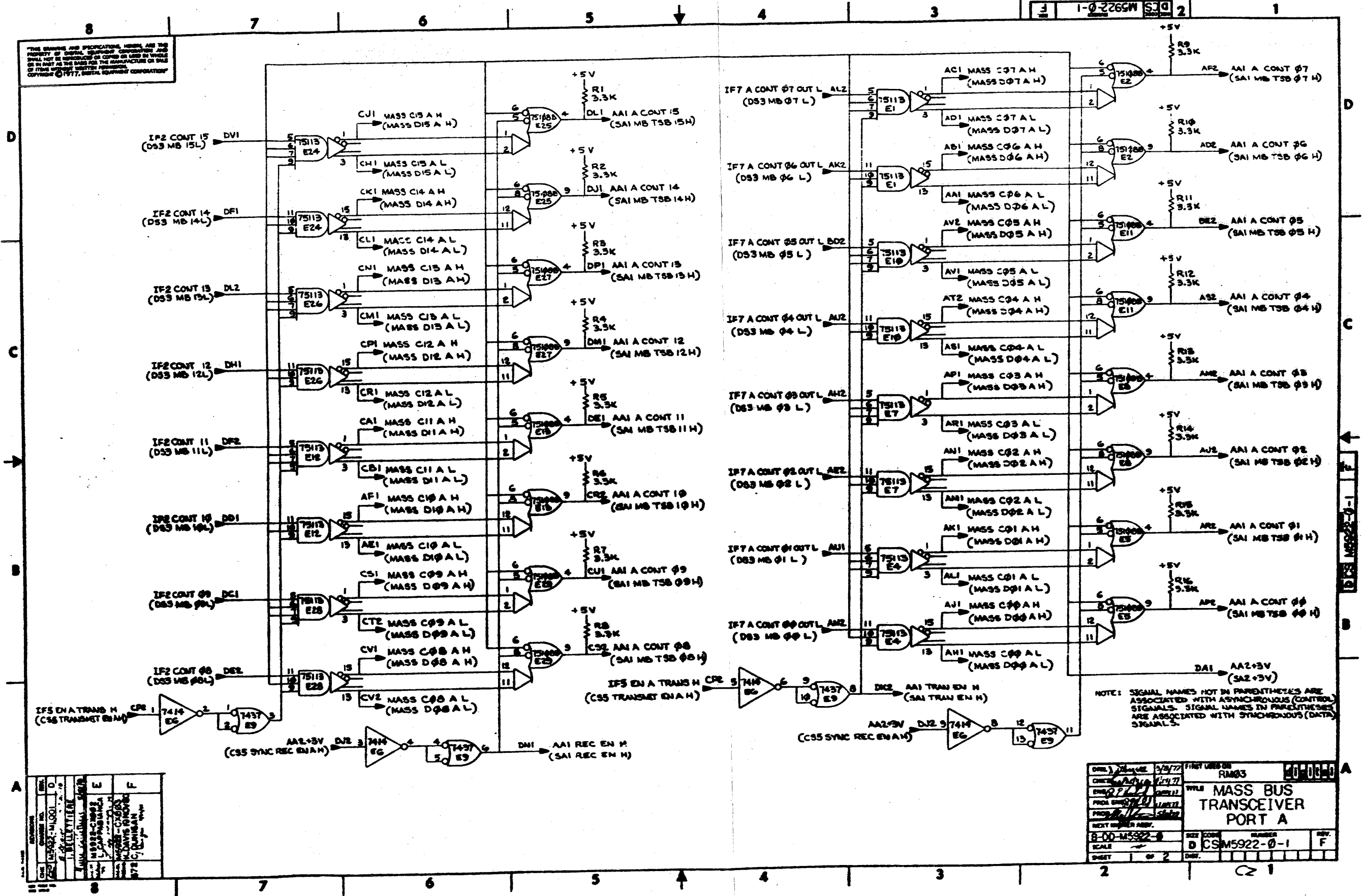
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011332-000A 0	1-80	
011332-000A 0	1-80	
011332-000A 0	1-80	
011332-000A 0	1-80	

SIGNATURES		DATE
DRN. <i>R. P. ...</i>		2-80
CHK. <i>B. ...</i>		1-80
ENG. <i>R. P. ...</i>		2-80
PROJ. ENG. <i>R. P. ...</i>		2-80
PROD. <i>R. P. ...</i>		2-80
SCALE: 2:1		
SNT. OF 3		
NEXT HIGHERassy.		

digital
 TITLE MASS BUS
 TRANSCEIVER PORT A

SIZE CODE NUMBER
 011332-000 E

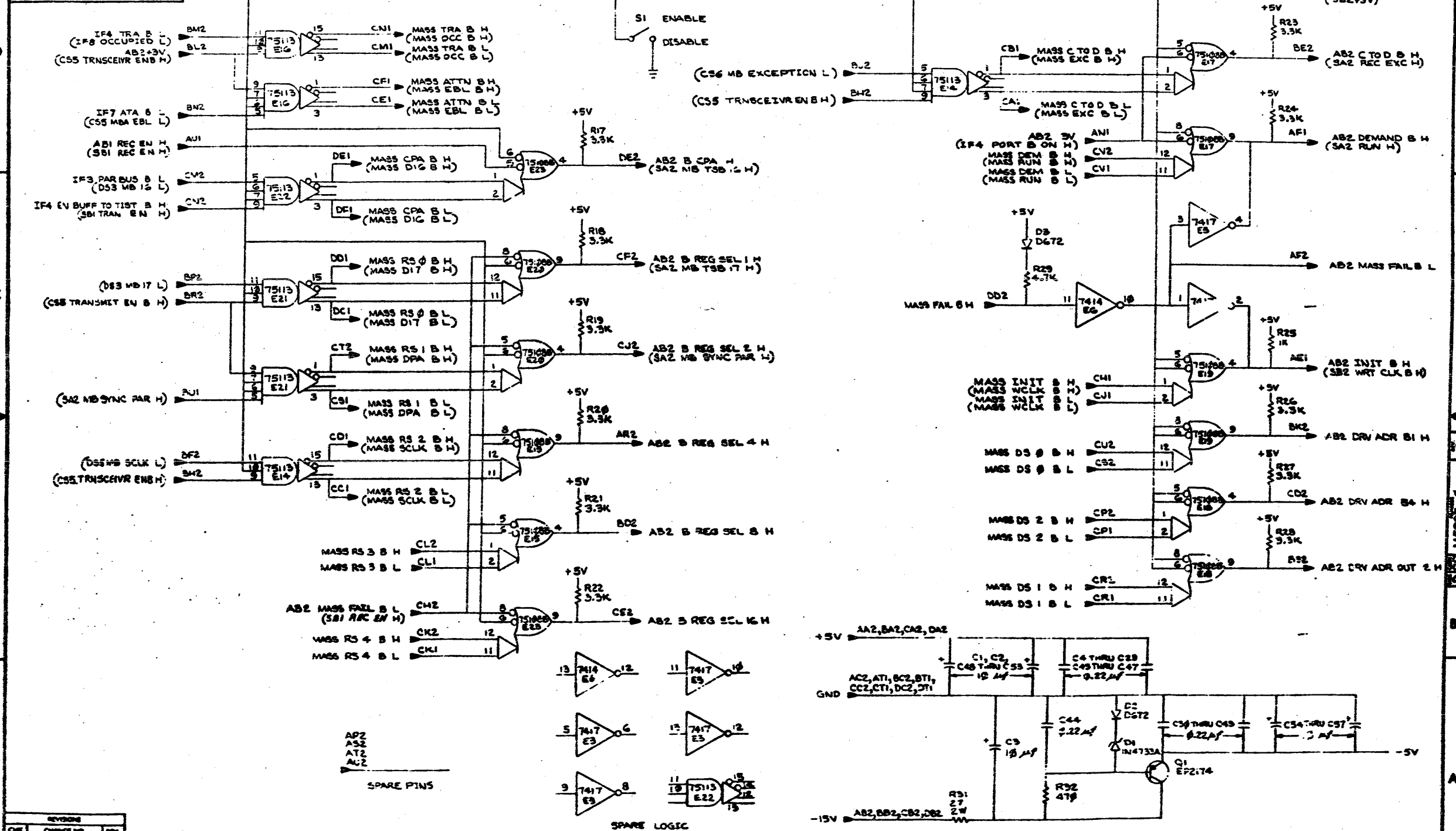
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DESIGNER	DATE	BY	CHKD
W. J. BULLIVANT	5/2/77	W. J. BULLIVANT	W. J. BULLIVANT
DESIGNED BY	DATE	BY	CHKD
W. J. BULLIVANT	5/2/77	W. J. BULLIVANT	W. J. BULLIVANT
DESIGNED BY	DATE	BY	CHKD
W. J. BULLIVANT	5/2/77	W. J. BULLIVANT	W. J. BULLIVANT
DESIGNED BY	DATE	BY	CHKD
W. J. BULLIVANT	5/2/77	W. J. BULLIVANT	W. J. BULLIVANT

DATE	3/2/77	REV	000000
DESIGNER	W. J. BULLIVANT	WFLS	MASS BUS TRANSCEIVER PORT A
PROJ	11/11/77	REV	000000
SCALE	1:1	REV	F
SHEET	2	REV	

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REVISIONS		
CHK	CHANGE NO.	REV.

LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY	PER VARIATION	REFERENCE DESIGNATOR
					00		
1	1	D-CS-M5923-0-1		CIRCUIT SCHEMATIC		REF	
2	2	D-UA-M5923-0-0		UNIT ASSEMBLY		REF	
3	3	B-DD-M5923-0-0		DWG DIRECTORY		REF	
4	4	D-MD-5012533-0-0		DRILL AND ETCH DRAWING		REF	
5	5		5012533-00	M5923	1		
6	6		1017472-00	10 MFD 35V +50-10% AL EL	13		C1,C2,C3,C48-C57
7	7		1010274-00	.22 MFD 50V +80-20% Z5U CER	44		C4-C47
8	8		1105275-00	D 672 TR= 15NS PIV= 60V SI	2		D2,D3
9	9		1109943-00	1N 4733A VZ= 5.1 5% 1W Y	1		D1
10	10		1300316-00	470.0 .25 W 5.0 % CC	1		R32
11	11		1300439-00	3.30 K .25 W 5.0 % CC	27		R1-R24,R26,R27,R28
12	12		1300447-00	4.70 K .25 W 5.0 % CC	1		R29
13	13		1305624-00	27.0 2.0 W10.0 % CC	1		R31
14	14		1300309-00	390.0 .25 W 5.0 % CC	1		R30
15	15		1512589-00	PNP 40W SI 60 25	1		Q1
16	16		1911324-00	7414 INVERTER,HEX 1IN SCH	1		E6
17	17		1909929-00	7417 BUFFER GATE-HEX 1INP	1		E3
18	18		1910091-00	DEC 7437 AND GATE-QUAD 2IN,BU	1		E9
19	19		1910725-00	75108B RECEIVER,LINE,DUAL,	14	CONT	E2,E5,E8,E11,E13,E15,E17,E18, E19,E20,E23,E25,E27,E29
20	20		1911341-00	75113 DRIVER,LINE,DUAL,PA	12	CONT	E1,E4,E7,E10,E12,E14,E16,E21, E22,E24,E26,E28
21	21		9008337-06	HANDLE, FLIP CHIP, MAGENTA	4		
22	22		9006732-00	EYELET, ROLLED FLANGE, .121 OD X	8		
23	23		9006010-01	SCREW,PAN,PHIL 4-40X 5/16 SS	1		
24	24		9006557-00	NUT,KEP , 4-40X 1/4 AF	1		
25	25		9008268-00	COMPOUND, THERMAL JOINT	A/R		
26	26		1210209-00	SW,TOG,SPDT,.01A@6V,ON/ON,SUBMIN	1		S1
27	27		1300365-00	1.0 K .25 W 5.0 % CC	1		R25

REVISION HISTORY			BASIC PART NO: M5923		DRN: R SULLIVAN		DATE: 13-JUL-78		DIGITAL			
ENG	ECO NUMBER	REV	SECTION A OF A	CHK'D:	CB	DATE:	13-JUL-78	TITLE	PARTS LIST			
	INIT	D	SECTION VARIATION INDEX	CHK'D:	CB <td>DATE:</td> <td>13-JUL-78</td> <td colspan="5">HASSBUS TRANCEIVER PORT B</td>	DATE:	13-JUL-78	HASSBUS TRANCEIVER PORT B				
ER	00001	D	[A] 00	DES.ENG:	RE <td>DATE:</td> <td>13-JUL-78</td> <td colspan="5">DOCUMENT NUMBER</td>	DATE:	13-JUL-78	DOCUMENT NUMBER				
LC	M5923-CX002	E	[B]	RESP.ENG.:	RE <td>DATE:</td> <td>13-JUL-78</td> <td>SIZE</td> <td>CODE</td> <td>NUMBER</td> <td>REV</td>	DATE:	13-JUL-78	SIZE	CODE	NUMBER	REV	
CD	M5923-CX003	F	[C]	MFG.ENG.:	MH <td>DATE:</td> <td>13-JUL-78</td> <td>K</td> <td>PL</td> <td>M5923-0-DBP</td> <td>F</td>	DATE:	13-JUL-78	K	PL	M5923-0-DBP	F	
			[D]	ASSEMBLY NUMBER:	D-UA-M5923-0-0	TOP DOCUMENT NUMBER:	RM03	FILE NAME:	Z0847F.PLS			EDIT #
			[E]									6
			[F]									
			[G]									
			[H]									
			[I]									
			[J]									
			[K]									
			[L]									
			[M]									
			[N]									

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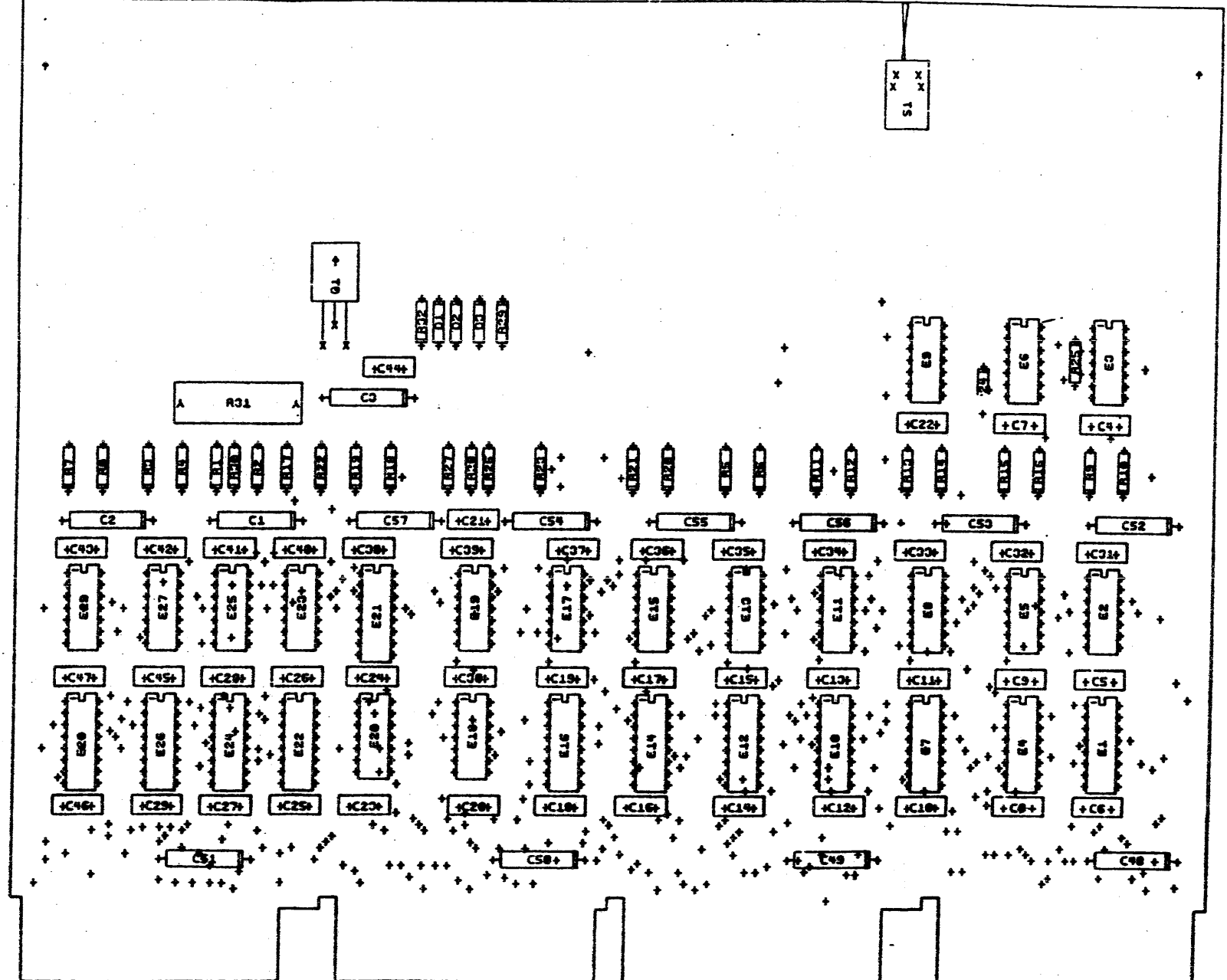
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REWORK INSTRUCTIONS
 COMPONENT DELETE SIDE 1:
 1-1 DELETE E6 (PIN 1909684-00)
 COMPONENT ADD SIDE 1:
 1-2 ADD E6 (PIN 1911324-00)

23,24,25



COMPONENT SIDE VIEW



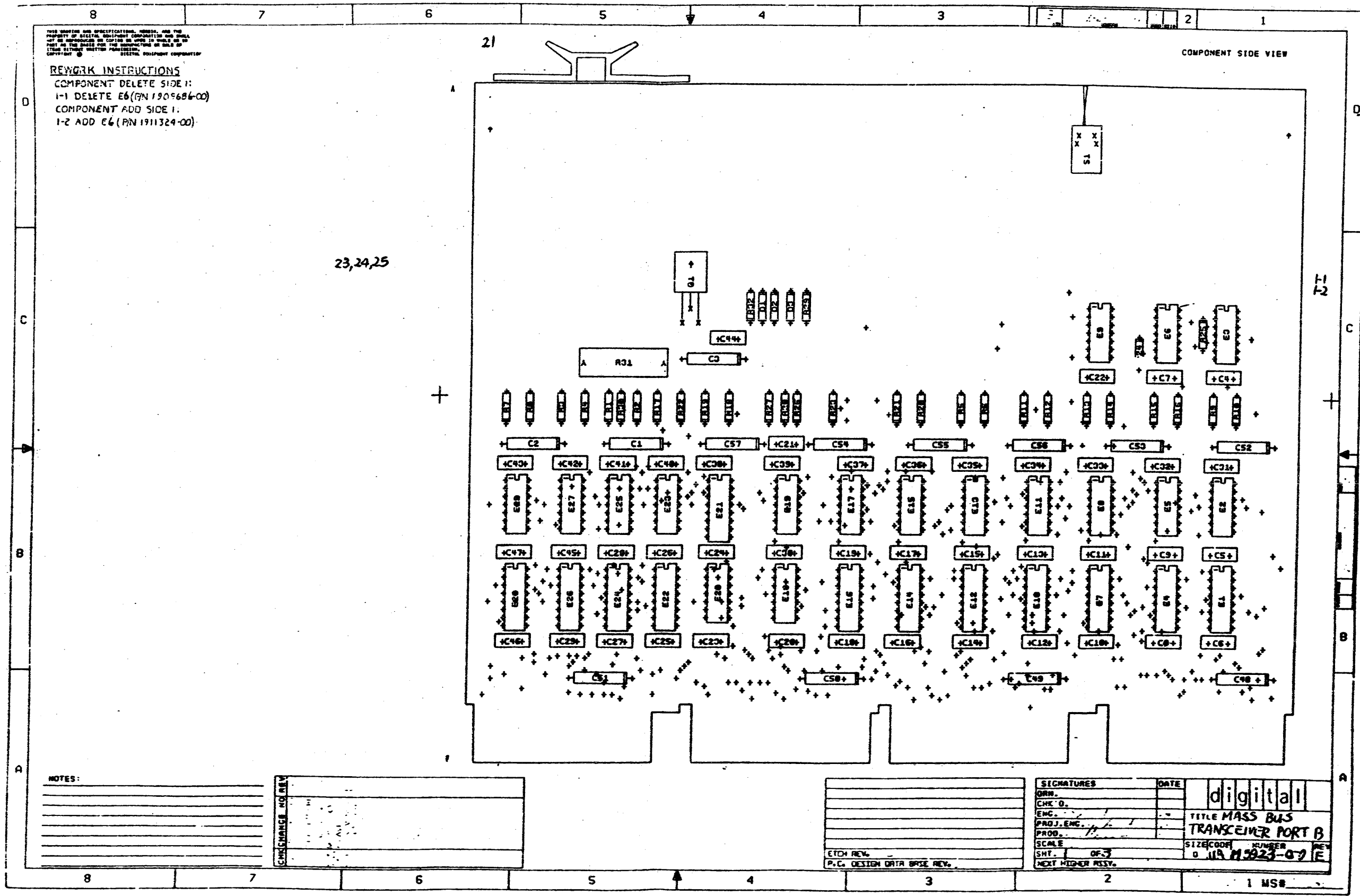
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CH	NO	REV	DATE	BY

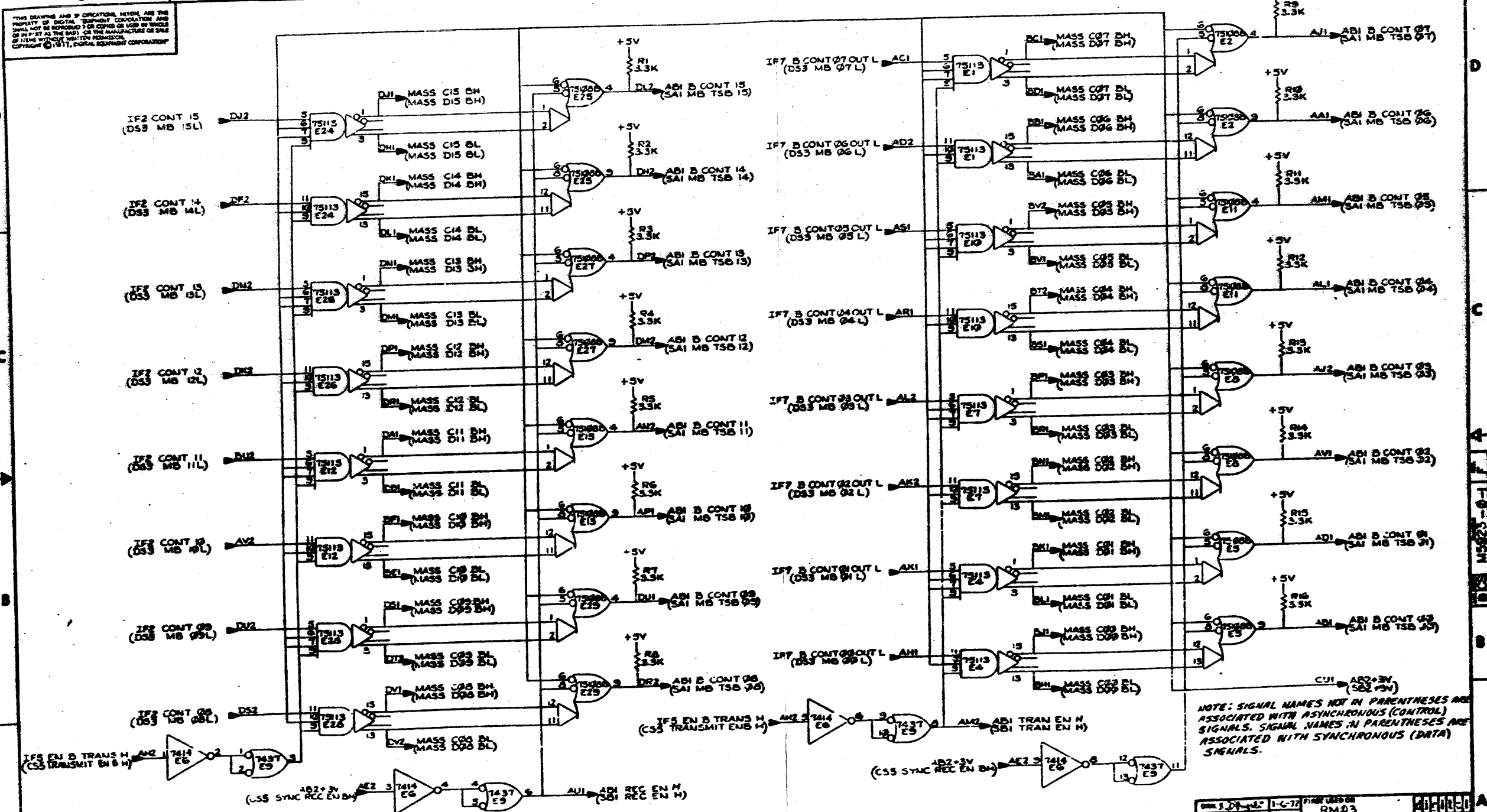
ETCH REV.	
P.C. DESIGN DATA	DATE REV.

SIGNATURES	DATE
DRW.	
CHK'D.	
ENG.	
PROJ. ENG.	
PROD.	
SCALE	
SHT. OF 3	
NEXT HIGHER REV.	

digital
TITLE MASS BUS TRANSCIVER PORT B
SIZE COOP NUMBER 0 1A M5923-07 E



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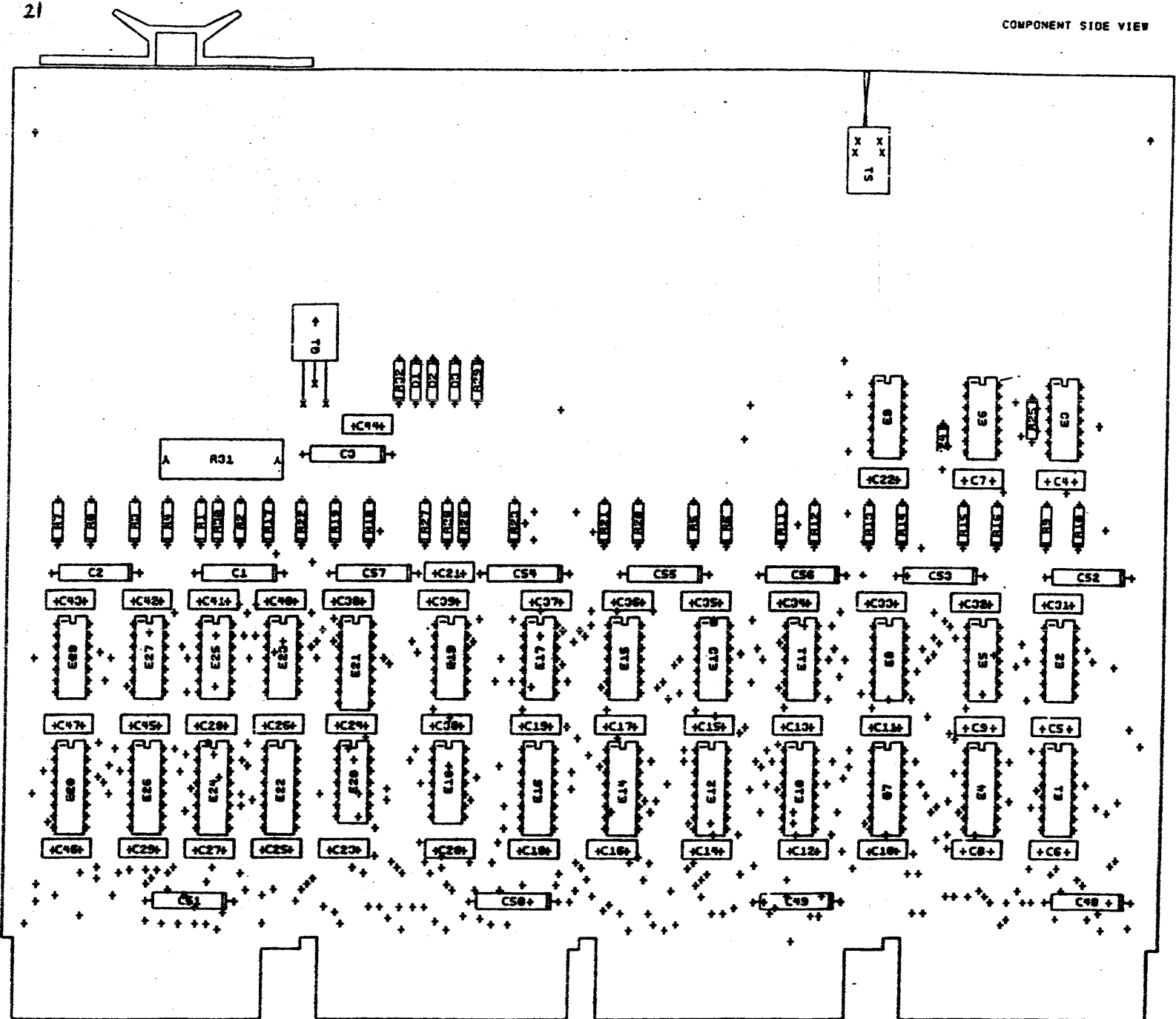
REV.	DATE	BY	CHKD.	DESCRIPTION
1				
2				
3				
4				
5				
6				
7				
8				

DATE: 1-6-77	FIGURE NO: RM03
DESIGNED BY: [Signature]	TITLE: MASS BUS TRANSCEIVER PORT B
PROJ. ENG. [Signature]	SCALE: D CS
PROJ. MGR. [Signature]	NUMBER: M5923-0-1
REV. [Signature]	REV. F
SHEET 1 OF 2	REV. C2 1

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REWORK INSTRUCTIONS
 COMPONENT DELETE SIDE 1:
 1-1 DELETE E6 (P/N 190966-00)
 COMPONENT ADD SIDE 1:
 1-2 ADD C6 (P/N 1911324-00)

23,24,25



NOTES:

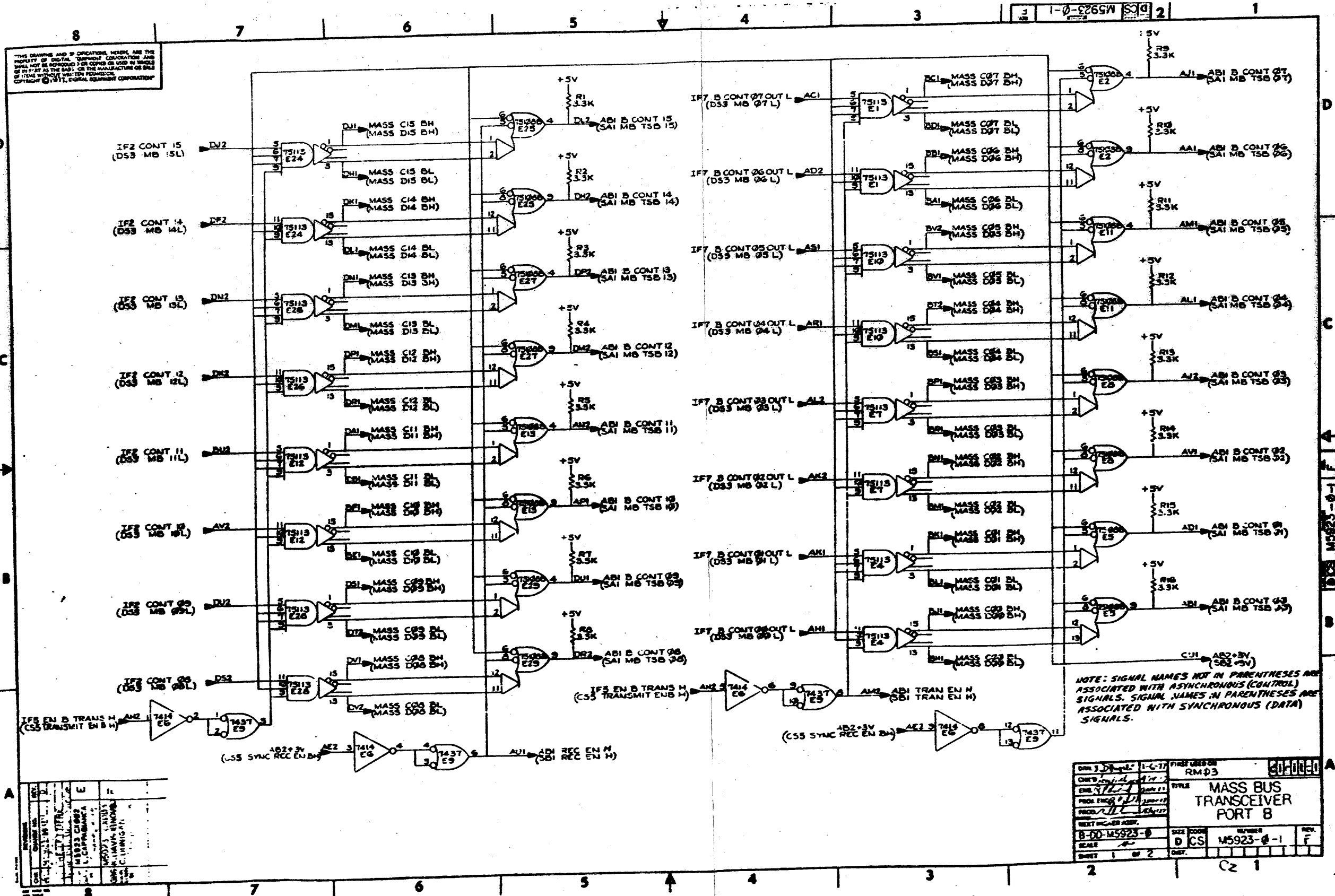
CHANGE NO.	REV.	DATE	BY

ETCH REV. _____
 P.C. DESIGN DATA BASE REV. _____

SIGNATURES _____ DATE _____
 DRN _____
 CHK'D _____
 ENG. _____
 PROJ. ENG. _____
 PROB. _____
 SCALE _____
 SMT. 1 OF 3
 NEXT HIGHER ASSY. _____

digital
 TITLE MASS BUS
 TRANSCEIVER PORT B
 SIZE CODE NUMBER
 D UA M3923-07 E

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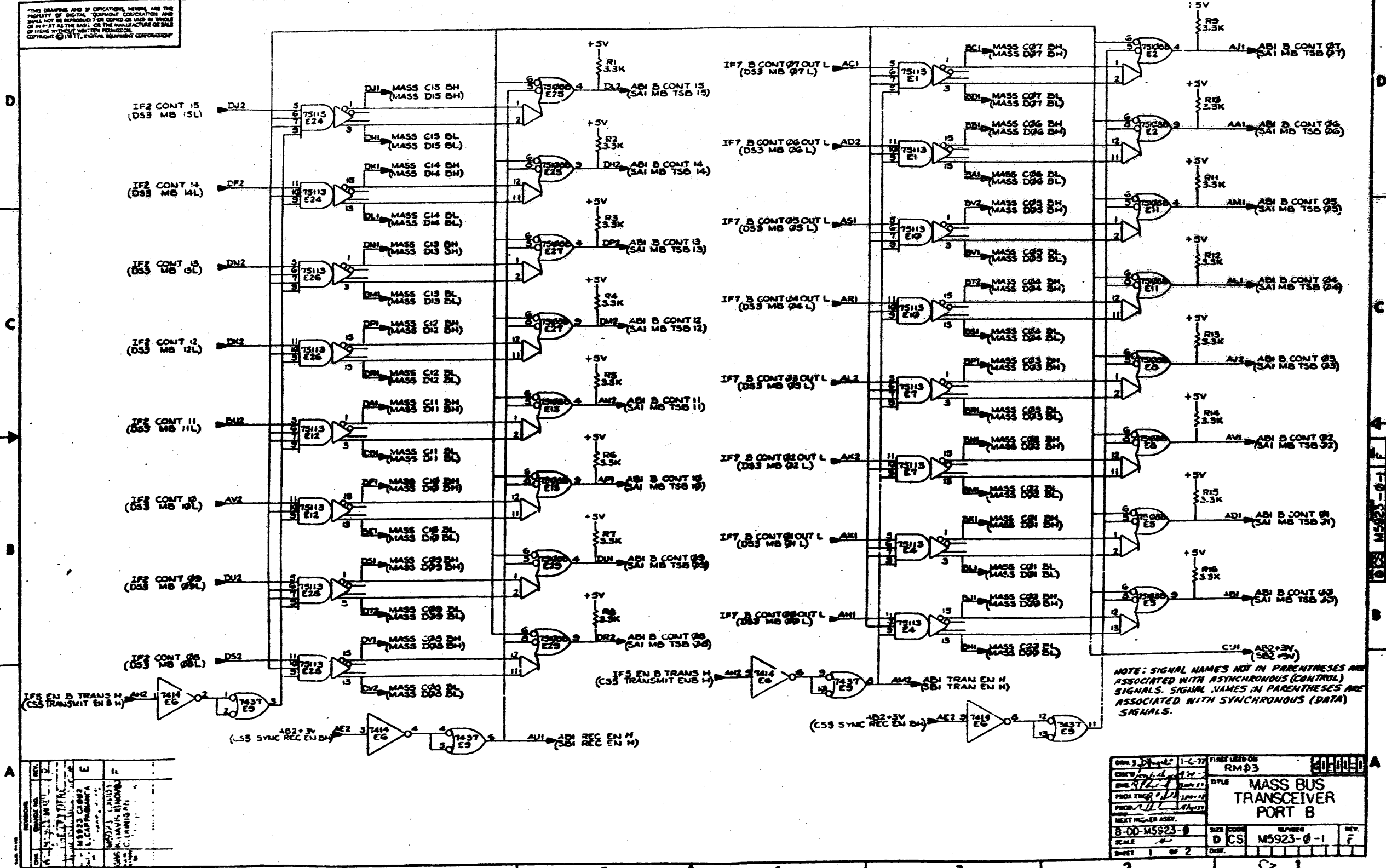


REV	DATE	BY	CHKD	APPD
1	1-6-77
2
3
4
5
6
7
8

NOTE: SIGNAL NAMES NOT IN PARENTHESES ARE ASSOCIATED WITH ASYNCHRONOUS (CONTROL) SIGNALS. SIGNAL NAMES IN PARENTHESES ARE ASSOCIATED WITH SYNCHRONOUS (DATA) SIGNALS.

DATE	1-6-77	FILE NO	RM#3
REV	...	TITLE	MASS BUS TRANSCEIVER PORT B
PROJ	...	SCALE	D CS
DESIGN	...	NUMBER	M5923-0-1
SHEET	1 of 2	REV	F

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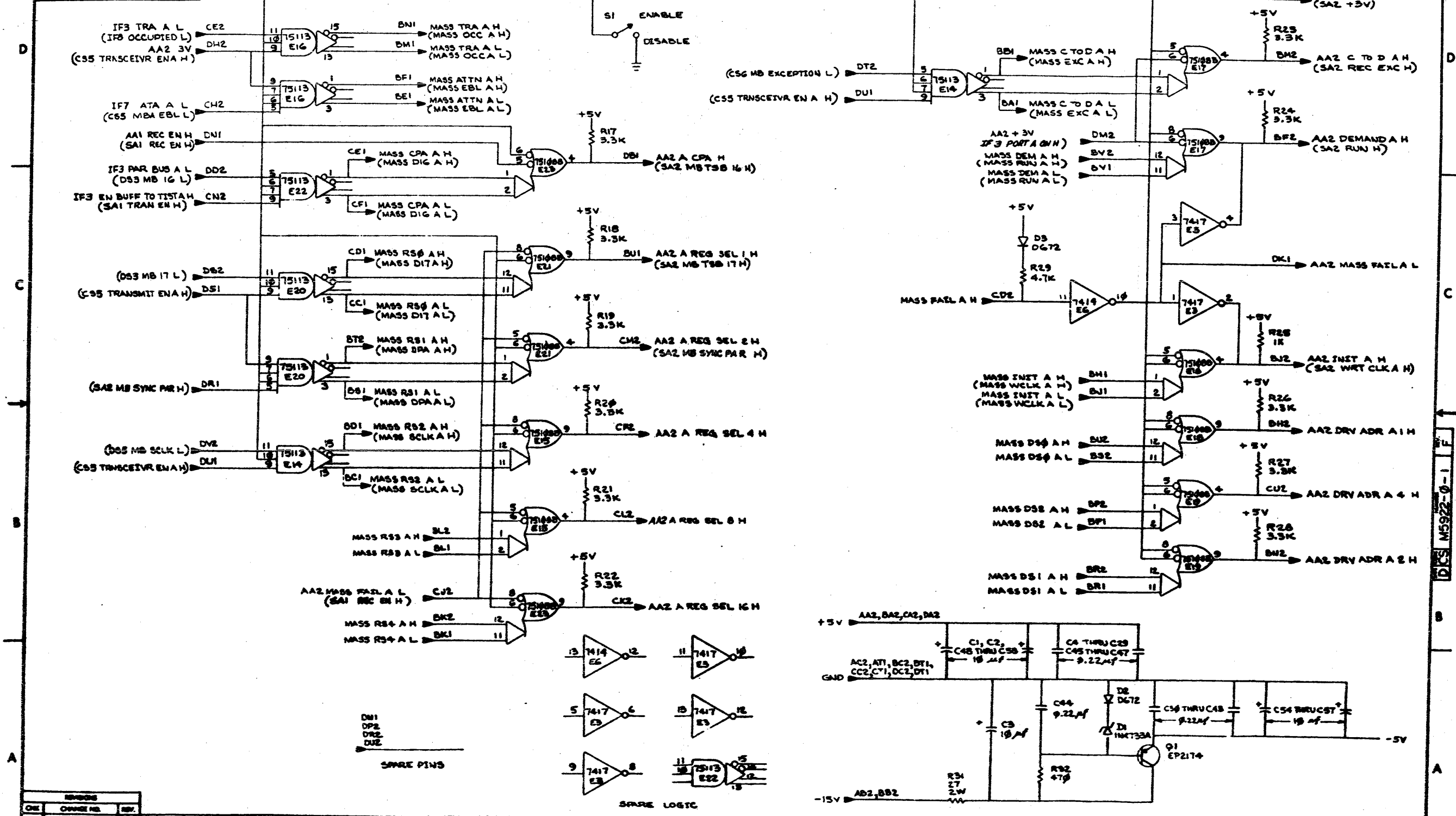
NOTE: SIGNAL NAMES NOT IN PARENTHESES ARE ASSOCIATED WITH ASYNCHRONOUS (CONTROL) SIGNALS. SIGNAL NAMES IN PARENTHESES ARE ASSOCIATED WITH SYNCHRONOUS (DATA) SIGNALS.

REV	DATE	BY	CHKD
1	11/11/77
2
3
4
5
6
7
8

DESIGN NO.	1-C-77	FIRST USED ON	RM 03
DATE	11/11/77	BY	...
PROJ. ENG.
PROJ. MGR.
NEXT HIGHER ASSY.
8-00-M5923-0	SIZE	FORM	NUMBER
SCALE	D	CS	M5923-0-1
SHEET	1	OF	2

C2 1

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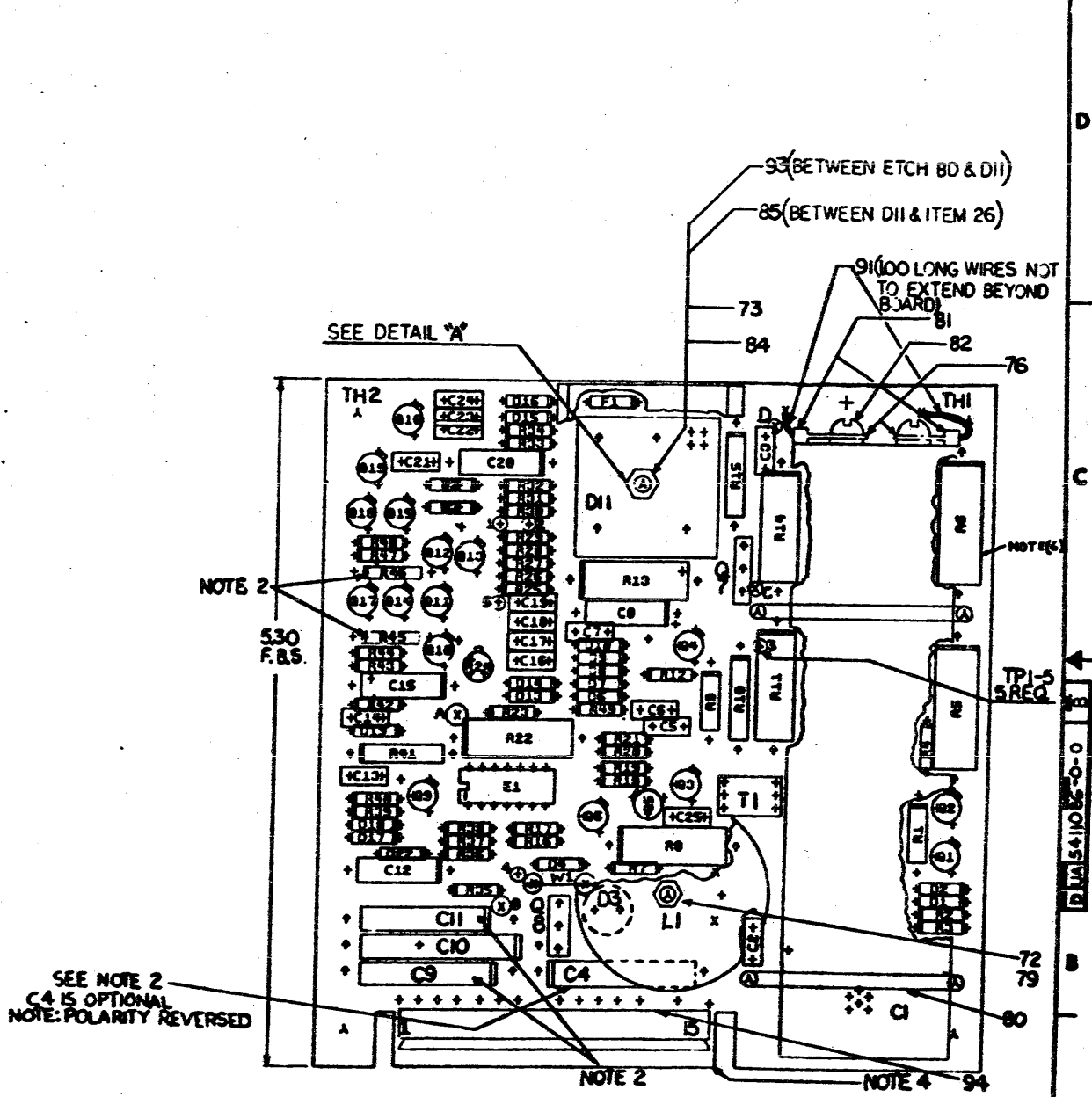
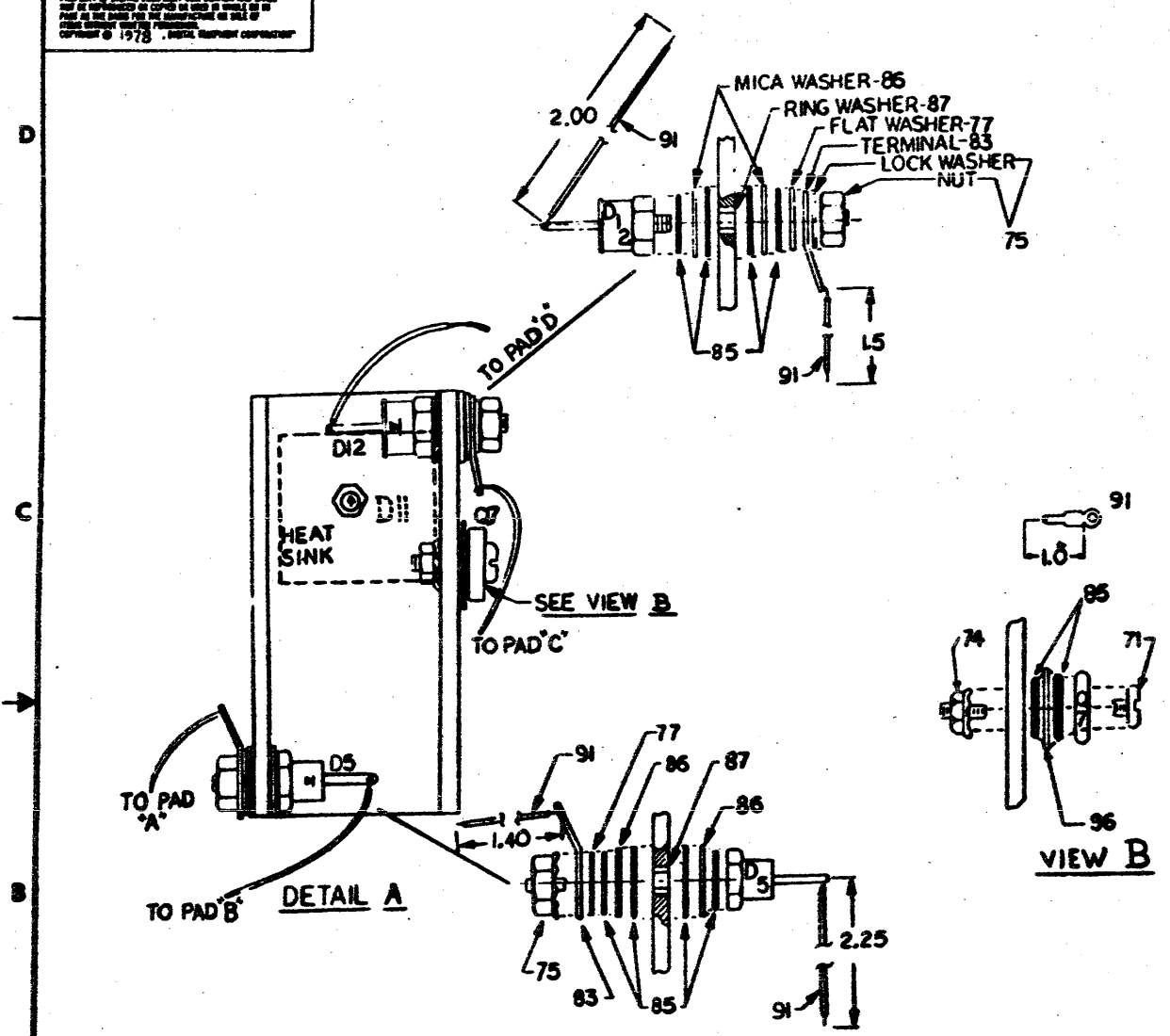
CHK	CHANGE NO.	REV.

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION	REFERENCE DESIGNATOR
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1	D-CS-M5922-0-1		CIRCUIT SCHEMATIC	REF	
2	D-UA-M5922-0-0		UNIT ASSEMBLY	REF	
3	B-DD-M5922-0-0		DWG. DIRECTORY	REF	
4	D-MD-5012462-0-0		DRILL & ETCH DWG.	REF	
5		5012462-00	M5922	1	
6		1017472-00	10 MFD 35V +50-10% AL EL	13	C1,C2,C3,C48-C57
7		1010274-00	.22 MFD 50V +80-20% Z5U CER	44	C4-C47
8		1105275-00	D 672 TR= 15NS PIV= 60V SI	2	D2,D3
9		1109943-00	1N 4733A VZ= 5.1 5% 1W Y	1	D1
10		1300316-00	470.0 .25 W 5.0 % CC	1	R32
11		1300439-00	3.30 K .25 W 5.0 % CC	27	R1-R24,R26,R27,R28
12		1300447-00	4.70 K .25 W 5.0 % CC	1	R29
13		1305624-00	27.0 2.0 W10.0 % CC	1	R31
14		1300309-00	390.0 .25 W 5.0 % CC	1	R30
15		1512589-00	PNP 40W SI 60 25	1	Q1
16		1911324-00	7414 INVERTER,HEX 1IN SCH	1	E6
17		1909929-00	7417 BUFFER GATE-HEX 1INP	1	E3
18		1910091-00	DEC 7437 AND GATE-QUAD 2IN,BU	1	E9
19		1910725-00	75108B RECEIVER,LINE,DUAL,	14	E2,E5,E8,E11,E13,E15,E17,E18,
				CONT	E19,E21,E23,E25,E27,E29
20		1911341-00	75113 DRIVER,LINE,DUAL,MA	12	E1,E4,E7,E10,E12,E14,E16,E20,
				CONT	E22,E24,E26,E28
21		9008337-06	HANDLE, FLIP CHIP, MAGENTA	4	
22		9006732-00	EYELET, ROLLED FLANGE, .121 OD X	8	
23		9006010-01	SCREW,PAN,PHIL 4-40X 5/16 SS	1	
24		9006557-00	NUT,KEP 4-40X 1/4 AF	1	
25		9008268-00	COMPOUND, THERMAL JOINT	A/R	
26		1210209-00	SW,TDB,SPDT,.01A@6V,ON/ON,SUBMIN	1	S1
27		9105740-55	WIRE(WRAP)30AWG UL1423	A/R	
28		1300365-00	1.0 K .25 W 5.0 % CC	1	R25

REVISION HISTORY		BASIC PART NO:	M5922	DRN:	R. SULLIVAN	DATE:	17-JUL-78	D I G I T A L			
ENG	ECO NUMBER	REV	SECTION A OF A	CHK'D:	C. BEVERLIE	DATE:	17-JUL-78	TITLE PARTS LIST			
ER	00001	D	SECTION.VARIATION INDEX	DES.ENG:	RV	DATE:	17-JUL-78	HASS BUS TRANSCEIVER PORT A			
LC	M5922-CX002	E	[A] 00	RESP.ENG.:	RV	DATE:	17-JUL-78	DOCUMENT NUMBER			
CD	M5922-CX003	F	[B]	MFG.ENG.:	MR	DATE:	17-JUL-78	SIZE	CODE	NUMBER	REV
			[C]	ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:		K	PL	M5922-0-DBP	F
			[D]	D-UA-M5922-0-0		RM03				FILE NAME:	EDIT #
			[E]							Z0846F.PLS	6
			[F]								
			[G]								
			[H]								
			[I]								
			[J]								
			[K]								
			[L]								
			[M]								
			[N]								

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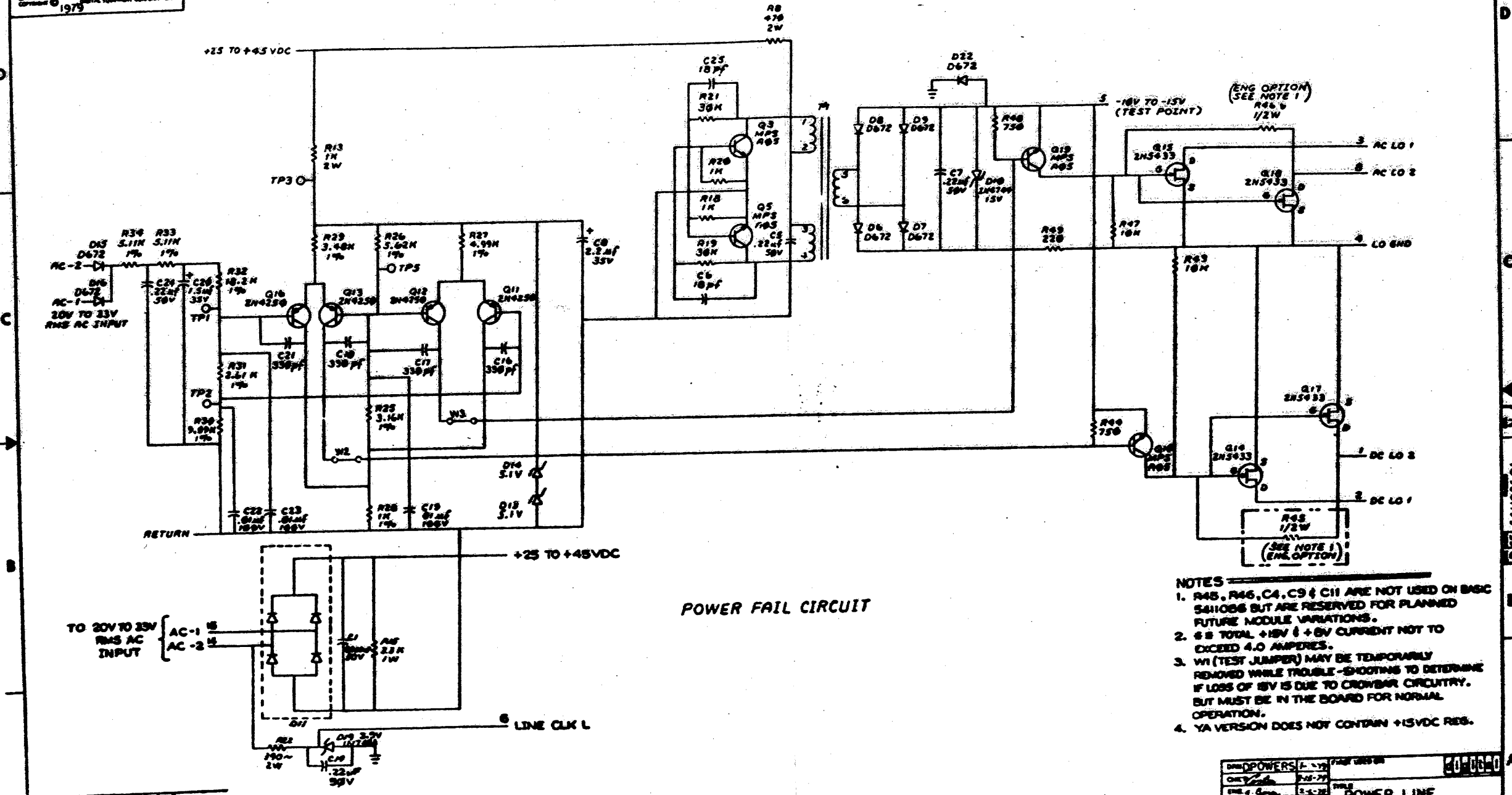
NOTES: 1. HOLES (0465+000-0045) ONLY
 2. 185 F.B.S. C4 IS OPTIONAL
 3. NON-THEMIZED PARTS SUPPLIED WITH D2
 4. USE 5/16 BEVEL AND BREAK TWO CORNERS
 5. D5 TO 7 TO BE SECURED TO ITEM 26
 6. HEAT SINKS SHOWN IN DETAIL A THIS
 UNIT IS ATTACHED TO D11 USING ITEMS
 85, 75, 86
 7. FOR Y4 ADD JUMPER ACROSS R6 PADS

SEE NOTE 2
 C4 IS OPTIONAL
 NOTE: POLARITY REVERSED

REV	DATE	BY	CHKD
1			
2			
3			
4			
5			
6			
7			
8			
9			
10			

DRW	STV	ENGR	DATE	ETCH REV	E-P3
CHKD	DATE	FILE			
ENR	DATE	FILE			
PROJ	DATE	FILE			
PROD	DATE	FILE			
NEXT	HIGHER	ASBY			
SCALE	2/1				
SHEET	1	OF	2		
SIZE CODE			REV		
D UAS41086-0-0			B		
DRW			REV		
TW 1					

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 1979



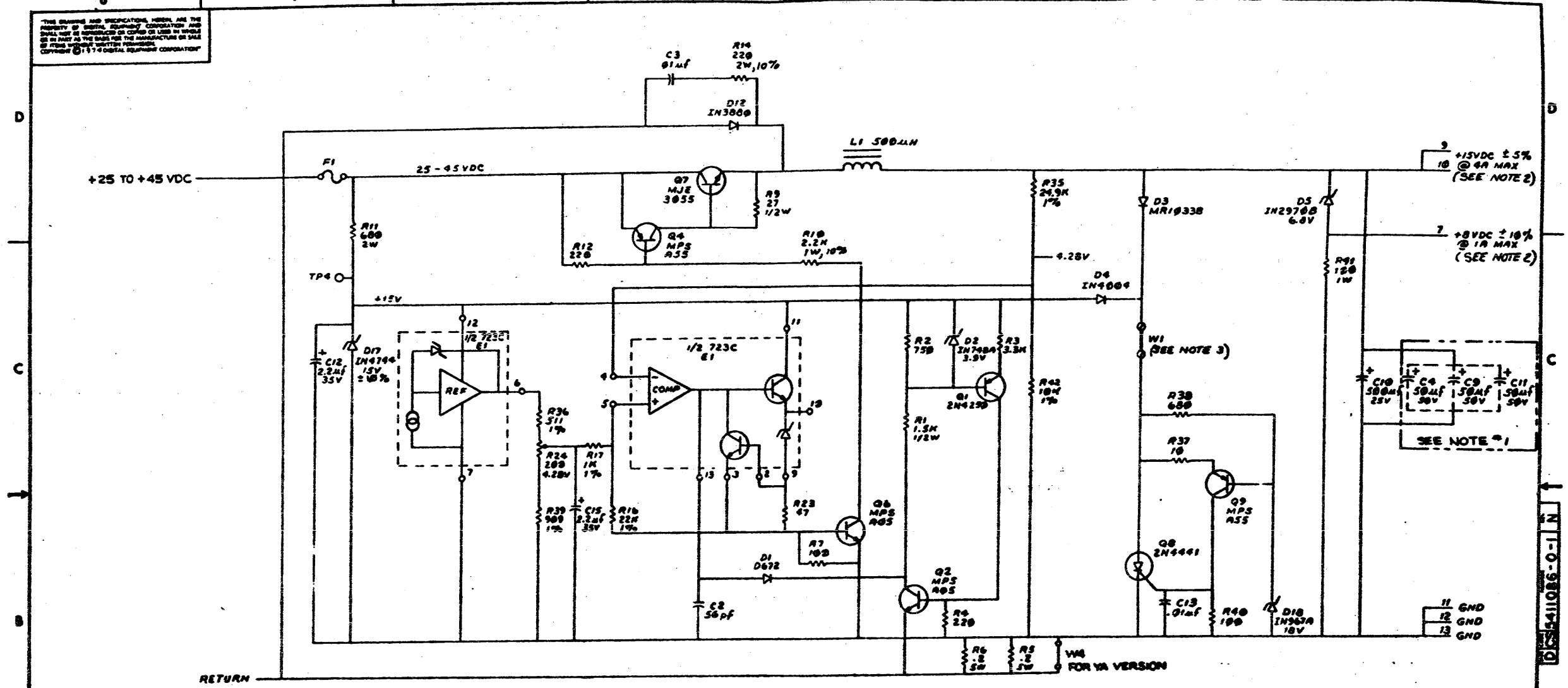
POWER FAIL CIRCUIT

- NOTES
1. R45, R46, C4, C9 & C11 ARE NOT USED ON BASIC 5411086 BUT ARE RESERVED FOR PLANNED FUTURE MODULE VARIATIONS.
 2. 6.8 TOTAL +15V & +5V CURRENT NOT TO EXCEED 4.0 AMPERES.
 3. W1 (TEST JUMPER) MAY BE TEMPORARILY REMOVED WHILE TROUBLE-SHOOTING TO DETERMINE IF LOSS OF 15V IS DUE TO CROWBAR CIRCUITRY, BUT MUST BE IN THE BOARD FOR NORMAL OPERATION.
 4. YA VERSION DOES NOT CONTAIN +15VDC RES.

DESIGNED BY	M
DRAWN BY	C. B.
CHECKED BY	A. BLIGH
DATE	1/10/79

DATE	1-10-79	REV	000000
BY	C. B.	DATE	1-10-79
CHKD	A. BLIGH	DATE	1-10-79
APP'D		DATE	1-10-79
TESTED		DATE	1-10-79
REVISIONS		DATE	1-10-79
B-10 5411086-0		POWER LINE MONITOR 15V REG	
SCALE NONE		D CS 5411086-0-1 N	
SHEET 2		OF 2	
T.W. 1			

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15V REGULATOR
(SEE NOTE #4)

REVISIONS		
CHK	CHANGE NO	REV

TITLE	PWR LINE MONITOR/15V REG.	NUMBER	DCS5411086-0-1	REV	N
SCALE	SHEET 2 OF 2	DATE			

TW

LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY	PER	VARIATION	REFERENCE DESIGNATOR
					00	YA		
1	1	B-DD-5411086-0-0		DRAWING DIRECTORY			REF REF	
2	2	D-UA-5411086-0-0		UNIT ASSEMBLY			REF REF	
3	3	D-CS-5411086-0-1		CIRCUIT SCHEMATIC			REF REF	
4	4		5011085-00	ETCH BRD 5411086	1	1		
5	5		1000012-00	56.0 MMF 100V 5%200PPM DM15S	1	0		C2
6	6		1000023-00	330.0 MMF 100V 5%200PPM DM15S	4	4		C16,C17,C18,C21
7	7		1001610-01	.01 MFD 100V -20+80 250 DISC	5	5		C3,C13,C19,C22,C23
			CONT					C19,C22,C23
8	8		1002431-00	2.2MFD 35V 10% 150S S.TA	3	3		C8,C12,C15
			CONT					C8
9	9		1002608-00	10.0 MMF 100V 5%200PPM DM15S	2	2		C6,C29
10	10		1009725-00	1.5MFD 35V 10% 1500 S.TA	1	1		C20
11	11		1010274-01	.22 MFD 50V -20+80 250 CER	4	4		C5,C7,C14,C24
12	12		1010509-01	500 MFD 25V C9 6010 AL EL	1	0		C10
13	13		1010851-00	8400 MFD 50V C9 360 AL EL	1	1		C1
14	14		1100122-00	1N 748A VZ= 3.9 5% 5% 5%	2	0		D2,D19
			CONT					D19
15	15		1100134-00	1N 2970B VZ= 6.8 5% 10W M	1	0		D5
16	16		1103341-00	MR10338 PIV=300 I= 3A Z44 SM	1	0		D3
17	17		1105275-00	D 672 TR= 15NS PIV= 60V 81	0	0		D1,D6,D7,D8,D9,D19,D16,D22
			CONT					D6,D7,D8,D9,D15,D16,D22
18	18		1105648-00	1N 4744 VZ= 15.0 10% 1W	2	0		D14,D17
			CONT					D14
19	19		1105796-00	1N 4004 PIV=400 I= 1A D041 SP	1	0		D4
20	20		1109440-00	1N 3000 PIV=100 I= 6A D04 SM	1	0		D12
21	21		1110060-00	1N 967A VZ= 10.0 10% .40W Y	1	0		D10
22	22		1110324-00	*** THIS ITEM IS NOT USED ***	0	0		
23	23		1110714-00	N883514 FWR400 I=20A (TRN)	1	1		D11
24	24		1105873-00	4NS-1A21 VZ= 5.1 1% .400W	2	2		D13,D14
25	25		1205747-00	FUSE, SUB-MINI, 5.000A, 125V, A	1	0		F1

REVISION HISTORY		BASIC PART NO: 5411086		DRN:	R. PETERSON	DATE:	04-JAN-79	D I G I T A L				
ENGR	ECO NUMBER	REV	SECTION A. OF A	CHK'D BY	J. FERGUSON	DATE:	04-JAN-79	TITLE PARTS LIST				
C.L.	5411086-ML088	8	SECTION VARIATION INDEX					PWR. LINE MONITOR/15V REG.				
			(A) 00, YA									
			(B)									
			(C)									
			(D)									
			(E)									
			(F)									
			(H)									
			(J)									
			(K)									
			(L)									
			(M)									
			(N)									
								DOCUMENT NUMBER				
								SIZE CODE NUMBER				
								K	PL	5411086-0-088	B	
								ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:	FILE NAME:	EDIT #:
								D-UA-5411086-0-0			Z02J30.PLS	13

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LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY	PER	VARIATION	REFERENCE DESIGNATOR
					NO	YA		
26	26		1211986-00	HEAT SINK FOR H742	1	0		
27	27		1300202-00	47.0 .25 W 5.0 %	1	0		R23
28	28		1300229-00	100.0 .25 W 5.0 %	2	0		R7,R40
29	29		1300232-00	100.0 1.0 W 5.0 %	1	0		R41
30	30		1300271-00	220.0 .25 W 5.0 %	3	0		R4,R12,R49
			CONT			1		R49
31	31		1300278-00	220.0 2.0 W 10.0 %	1	0		R14
32	32		1300348-00	680.0 2.0 W 5.0 %	1	0		R11
33	33		1300365-00	1.0 K .25 W 5.0 %	2	2		R10,R20
34	34		1300394-00	1.50 K .50 W 5.0 %	1	0		R1
35	35		1300420-00	2.20 K 1.0 W 10.0 %	1	0		R10
36	36		1300437-00	3.30 K 1.0 W 10.0 %	1	1		R15
37	37		1300439-00	3.30 K .25 W 5.0 %	1	0		R3
38	38		1300479-00	10.0 K .25 W 5.0 %	2	2		P43,R47
39	39		1301317-00	10.0 .25 W 5.0 %	1	0		R37
40	40		1301401-00	750.0 .25 W 5.0 %	3	0		R2,R44,R48
			CONT			2		R44,R48
41	41		1301424-00	680.0 .25 W 5.0 %	1	0		R30
42	42		1301808-00	22.0 K .25 W 5.0 %	1	0		R16
43	43		1301952-00	1.0 K 2.0 W 5.0 %	1	1		R13
44	44		1302253-00	27.0 .50 W 5.0 %	1	0		R9
45	45		1302394-00	30.0 K .25 W 5.0 %	2	2		R19,R21
46	46		1302411-00	511 1/4W 1% RN550-F 100PPH	1	0		R36
47	47		1302685-00	909 1/4W 1% RN550-F 100PPH	1	0		R39
48	48		1303045-00	3.16 K 1/4W 1% RN550-F 100PPH	1	1		R25
49	49		1303062-00	470.0 2.0 W 5.0 %	1	1		R0
50	50		1303114-00	1.0 K 1/4W 1% RN550-F 100PPH	2	0		R17,R20
			CONT			1		R20
51	51		1303303-00	2.61 K 1/4W 1% RN550-F 100PPH	1	1		R31
52	52		1303312-00	10.0 K 1/4W 1% RN550-F 100PPH	1	0		R42
53	53		1304054-00	5.11 K 1/4W 1% RN550-F 100PPH	2	2		R33,R34
54	54		1304855-00	9.09 K 1/4W 1% RN550-F 100PPH	1	1		R30
55	55		1305114-00	3.40 K 1/4W 1% RN550-F 100PPH	1	1		R29
56	56		1305120-00	5.62 K 1/4W 1% RN550-F 100PPH	1	1		R26
57	57		1305324-00	4.99 K 1/4W 1% RN550-F 100PPH	1	1		R27
58	58		1305405-00	24.9 K 1/4W 1% RN550-F 100PPH	1	0		R35
59	59		1309150-10	200 1/2W20% POT 0 TO 250	1	0		R24
60	60		1309412-00	10.2 K 1/4W 1% RN550-F 100PPH	1	1		R32
61	61		1309884-00	.2 5W 5% NW	2	0		R5,R6
62	62		1510555-00	MJE3055 NPN 90MC 8I 60 20 Y	1	0		Q7
63	63		1510705-00	XA 05 NPN 500MW 8I 60 50 P	6	0		Q2,Q3,Q5,Q6,Q10,Q19
			CONT			4		Q3,Q5,Q10,Q19
64	64		1510706-00	XA 55 PNP 500MW 8I 60 50 P	2	0		Q4,Q9
65	65		1505067-00	2W 4441 SCRD 50V 6 8A	1	0		Q8
66	66		1509142-00	DEC4250 PNP 200MW 8I 40250	5	0		Q1,Q11,Q12,Q13,Q16
			CONT			4		Q11,Q12,Q13,Q16
67	67		1511600-00	DEC5433 PEP N 350MW 10 25 1A	4	0		Q14,Q15,Q17,Q18
68	68		1511860-00	500 0W 20% 5A	1	0		L1

D	I	G	I	T	A	L	TITLE	SECTION A OF A	SIZE	CODE	DOCUMENT NUMBER	REV
							PWR. LINE MONITOR/15V REG.		K	PL	5411006-0-000	B

AUTOMATED BY PRTLST,3G(16)

PARTS LIST

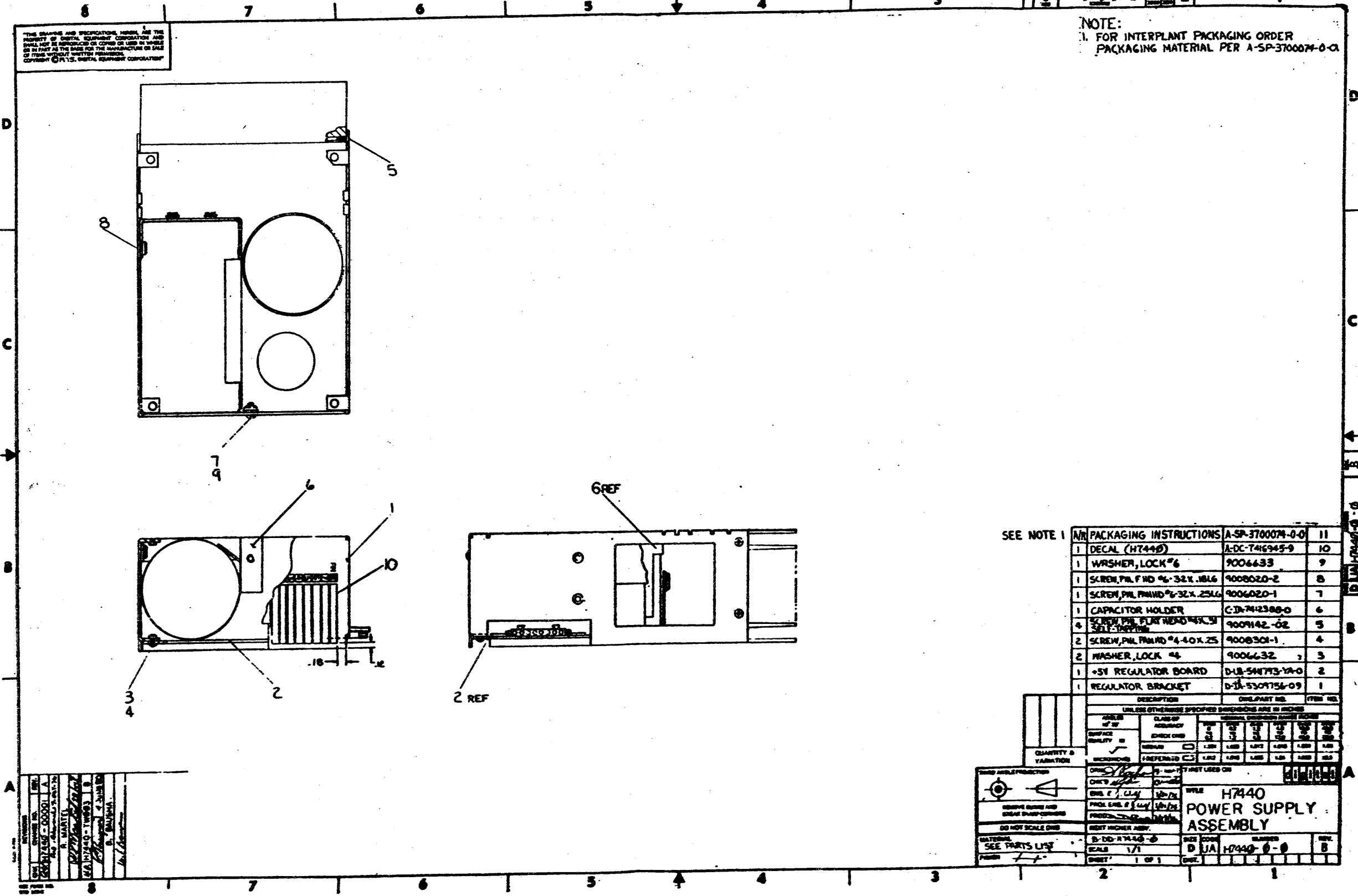
SHEET A3 OF A3

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION		REFERENCE DESIGNATOR
				00	YA	
69	69	1612026-00	PULSE XFMR RATIO 10:1 100UH	1	1	T1
70	70	1910413-00	723 VOLT REG VAR 2/37V	1	0	E1
71	71	9006012-01	SCREW, PAN, PHIL, 4-40X 7/16	1	0	
72	72	9006020-01	SCREW, PAN, PHIL, 6-32X 1/4	1	0	
73	73	9006027-01	SCREW, PAN, PHIL, 6-32X 7/8	1	0	
74	74	9006557-00	NUT, KEP, 4-40X 1/4 AF	1	0	
75	75	9006565-00	NUT, KEP, 10-32X 3/8 AF	2	0	
76	76	9006635-00	WASHER, LOCK, INT, .3100 X .200 ID	2	2	
77	77	9006660-00	WASHER, FLAT, .500 OD X .187 ID	2	0	
78	78	9006735-00	EYELET, FUNNEL FLANGE, .059 OD X	2	0	
79	79	9006851-00	SPACER, HEX, ALUM, 6-32, .250 X	1	0	
80	80	9007880-00	TIE, CABLE, SST-1.5M	4	4	
81	81	9007930-00	TERM RING 1POS INSULATED, 22-16	2	2	
82	82	9008007-01	SCREW, PAN, PHIL, 10-32X 1/4 S	2	2	
83	83	9008150-00	TERM LUG 1POS LOCKING, SOLDER	1	0	
84	84	9008185-00	NUT, KEP, 6-32X 1/4 AF	1	0	
85	85	9008260-00	COMPOUND, THERMAL JOINT	A/R	0	
86	86	9008424-00	WASHER, FLAT, THERMAFILM, .502 O	4	0	
87	87	9008440-00	BUSHING, NYLON, .248 O.D. X .190	2	0	
88	88	BLANK		0	0	
89	89	1301800-00	390.0 2.0 WIRE B CC	1	1	R22
90	90	9009185-00	JUMPER, WIRE, INSULATED, BLACK B	3	0	W1, W2, W3
		CONT		0	0	W4, W2, W3
91	91	9107360-00	WIRE, STRNG, 10AWG, IPVC UL1429	A/R	A/R	
92	92	A-SP-11/45-TA-2	MODULE TEST PROCEDURE	REP	REP	
93	93	9006713-00	WASHER, NYLON, FLAT #8 .437 OD	1	1	
94	94	7413721-00	SHIM .010 IN.	1	1	
95	95	A-SP-5411086-0-3	ENG. SPEC-AND TEST PROC.	REP	REP	
96	96	9009597-00	INSULATOR, THERMA-FILM	1	0	
97	97	9107560-01	*** THIS ITEM IS NOT USED ***	0	0	

D	I	G	I	T	A	L	TITLE	SECTION A OF A	SIZE CODE	DOCUMENT NUMBER	REV
							PWR. LINE MONITOR/15V REG.		K PL	5411086-0-002	B

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NOTE:
1. FOR INTERPLANT PACKAGING ORDER
PACKAGING MATERIAL PER A-SP-3700074-0-0



REV	DATE	BY	CHKD	DESCRIPTION
001	12/15/75	R. MARTEL		ASSEMBLY
002	1/14/76	B. BAUMANN		REVISION

SEE NOTE 1

QTY	DESCRIPTION	PART NO.	REF
11	PACKAGING INSTRUCTIONS	A-SP-3700074-0-0	11
1	DECAL (HT440)	A-DC-7416945-9	10
1	WASHER, LOCK #6	9006633	9
1	SCREW, PHL PANHD #6-32X.18L6	9006020-2	8
1	SCREW, PHL PANHD #6-32X.25L6	9006020-1	7
1	CAPACITOR HOLDER	C-DA-742388-0	6
4	SCREW, PHL FLAT HEAD #4X.51 SELF-TAPPING	9009142-02	5
2	SCREW, PHL PANHD #4-40X.25	9006901-1	4
2	WASHER, LOCK #4	9006632	3
1	+5V REGULATOR BOARD	D-DA-541713-1A-0	2
1	REGULATOR BRACKET	D-DA-530756-09	1

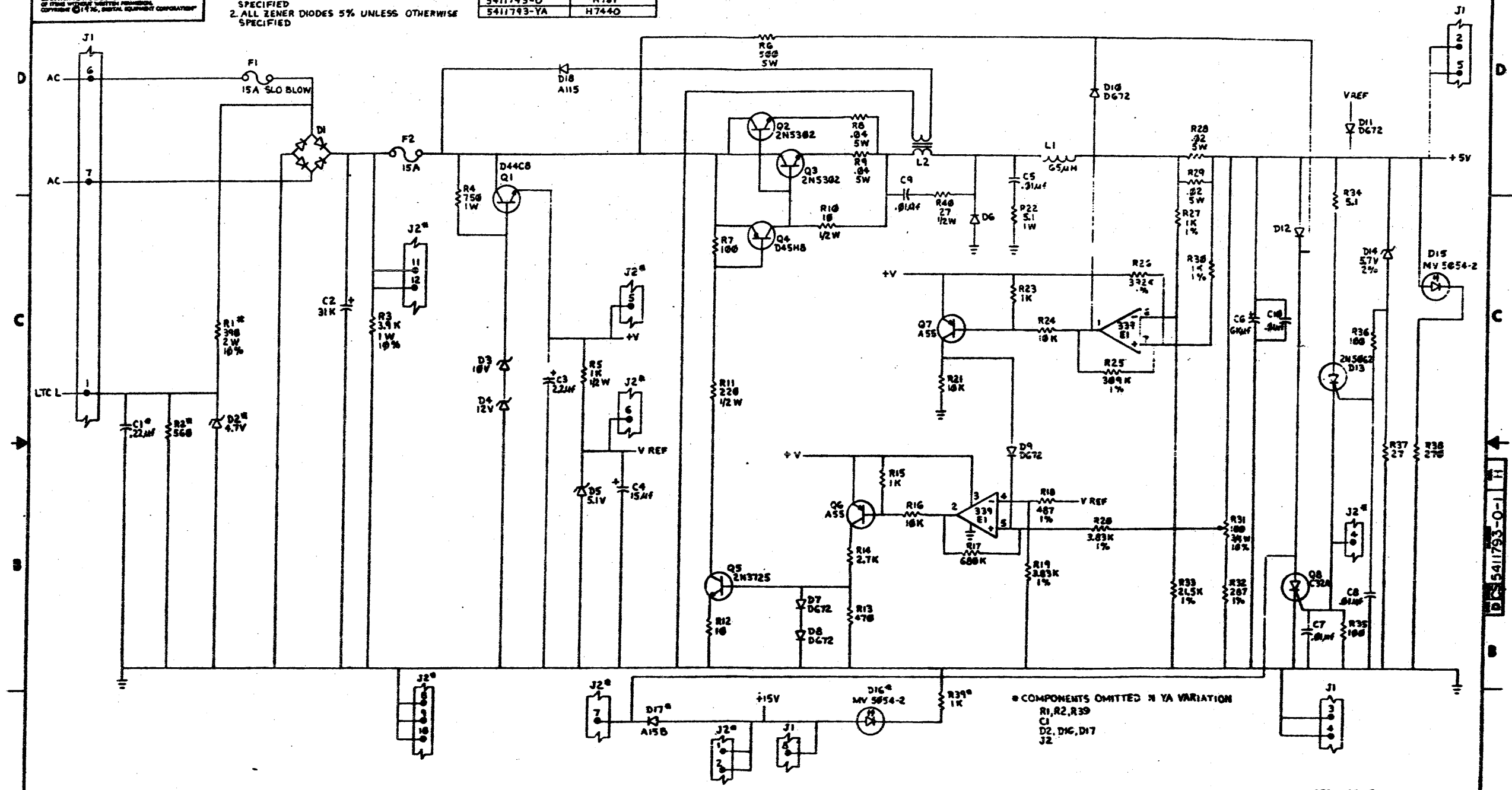
QUANTITY & TOLERANCE		DIMENSIONS		PREFERRED DIMENSIONS		DIMENSIONS	
QTY	TOL	MIN	MAX	MIN	MAX	MIN	MAX
1	±.005	1.00	1.00	1.00	1.00	1.00	1.00

H7440
 POWER SUPPLY
 ASSEMBLY
 SCALE 1/1
 SHEET 1 OF 1

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NOTES:
 1. ALL RESISTORS 1/4 W, 5% UNLESS OTHERWISE SPECIFIED
 2. ALL ZENER DIODES 5% UNLESS OTHERWISE SPECIFIED

LEGEND	
NUMBER	USED ON
5411793-0	H781
5411793-YA	H7440



* COMPONENTS OMITTED IN YA VARIATION
 R1, R2, R39
 C1
 D2, D6, D17
 J2

REV.	DATE	BY	CHKD.	APP'D.	DESCRIPTION
1					
2					
3					
4					
5					
6					
7					
8					

ETCH REV E

DRW. NO. 5411793-0-1	REV. 5	DATE 11/11/73	BY J. J. [Signature]	CHKD. [Signature]	APP'D. [Signature]
TITLE			+5V REGULATOR POWER SUPPLY		
D-UA-5411793-0-0					
SCALE	1	NO. 1	OF 1	REV. H	

DIGITAL EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS				PARTS LIST		ETCH REV E		QUANTITY/VARIATION			
MADE BY BOB WOLF		CHECKED <i>[Signature]</i>		SECTION 1		ISSUED SECT. 1					
DATE 11/SEPT/75		DATE 19 FEB 76									
ENG <i>[Signature]</i>		PROG <i>[Signature]</i>									
DATE 16 MAR 76		DATE 16 MAR 76									
ITEM NO	DWG NO. / PART NO.	DESCRIPTION		5411793-0-0	5411793-1A						
1	D-CS-5411793-0-1	+5V REGULATOR POWER SUPPLY (C.S.)		REF	REF						
2	D-AH-5411793-0-5	ASSY/DRILLING HOLE LAYOUT		REF	REF						
3	B-MH-5411793-0-6	MODULE ECO HISTORY		REF	REF						
4	1301880-00	RES 390, 2%, 10%		1 R1	0						
5	1301890-00	RES 560, 1%, 5%		1 R2	0						
6	1302927-00	RES 3.9K, 1%, 10%		1 R3	1						
7	1302385-00	RES 750, 1%, 5%		1 R4	1						
8	1300164-00	RES 1K, 1%, 5%		1 R5	1						
9	1303169-00	RES 500, 5%		1 R6	1						
10	1300229-00	RES 100, 1/2%, 5%		1 R7 R35 R36	3						
11	1311362-00	RES .04, 5%, 5%		2 R8 R9	2						
12	1300168-00	RES 10, 1%, 5%		1 R10	1						
13	1300274-00	RES 220, 1%, 5%		1 R11	1						
14	1301317-00	RES 10, 1%, 5%		1 R12	1						
15	1300316-00	RES 470, 1%, 5%		1 R13	1						
16	1300426-00	RES 2.7K, 1%, 5%		1 R14	1						
17	1300365-00	RES 1K, 1%, 5%		1 R15 R23 R29	2 R5 R25						
18	1310867-00	RES 680K, 1/4%, 5%		1 R17	1						
19	1303114-00	RES 1K, 1%, 1%		2 R27 R30	2						
20	1311843-00	RES 48K, 1/4%, 1%		1 R18	1						
21	1302413-00	RES 3.9K, 1%, 1%		2 R19 R20	2						
22	1300479-00	RES 10K, 1%, 5%		1 R21 R16 R24	3						
TITLE		ASSY NO. D-UA-5411793-0-0		SIZE CODE A PL	NUMBER 5411793-0-0	REV E	ECO NO H				
-5V REGULATOR POWER SUPPLY		SHEET 3 OF 4									

DEC FORM DEC 16 (175) 1031 N&B
924 110

DIGITAL EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS				PARTS LIST		ETCH REV E		QUANTITY/VARIATION			
MADE BY BOB WOLF		CHECKED <i>[Signature]</i>		SECTION 1		ISSUED SECT. 1					
DATE 11/SEPT/75		DATE 19 FEB 76									
ENG <i>[Signature]</i>		PROG <i>[Signature]</i>									
DATE 16 MAR 76		DATE 16 MAR 76									
ITEM NO	DWG NO. / PART NO.	DESCRIPTION		5411793-0-0	5411793-1A						
23	1302136-00	RES 3.1, 1%, 5%		1 R22	1						
24	1305426-00	RES 300K, 1/4%, 1%		1 R25	1						
25	1305426-00	RES 390K, 1/4%, 1%		1 R26	1						
26	1310876-00	RES .02, 5%, 5%		2 R28 R29	2						
27	1309143-04	RES 100, 3/4%, 10% POT		1 R31	1						
28	1305124-00	RES 287, 1%, 1%		1 R32	1						
29	1309422-00	RES 5.1, 1%, 5%		1 R34	1						
30	1301522-00	RES 27, 1%, 5%		1 R37	1						
31	1301972-00	RES 270, 1%, 5%		1 R38	1						
32	1303155-00	RES 2.2K, 1/4%, 1%		1 R33	1						
33	1010274-00	CAP .22UF, 50V		1 C1	0						
34	1010358-00	CAP 33K, 50V		1 C2	1						
35	1002431-00	CAP 2.2UF, 35V 10%		1 C3	1						
36	1004812-00	CAP 150F, 20V, 10%		1 C4	1						
37	1012794-00	CAP 600UF, 10V		1 C5	1						
38	1001610-01	CAP .01UF 100V		5 C7 C8 C5 C9 C10	5						
39	1110714-00	DIODE RECTIFIER BRIDGE NSS3514		1 D1	1						
40	1110994-00	DIODE 5.1V, .44, 5% ZENER 51A		1 D5	1 D5						
41	1160125-00	DIODE 10V, .44, 5% ZENER 758A		1 D3	1						
42	1110836-00	DIODE 12V, .44, 5% ZENER 759A		1 D4	1						
43	1125275-00	DIODE 0F72		5 D7 D9 D10 D11 D8	5						
44	1110715-00	DIODE RECTIFIER T03		1 D6	1						
TITLE		ASSY NO. D-UA-5411793-0-0		SIZE CODE A PL	NUMBER 5411793-0-0	REV E	ECO NO H				
-5V REGULATOR POWER SUPPLY		SHEET 2 OF 4									

DEC FORM DEC 16 (175) 1031 N&B
924 110

DIGITAL EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS				PARTS LIST		ETCH REV E		QUANTITY/VARIATION			
MADE BY BOB WOLF		CHECKED <i>[Signature]</i>		SECTION 1		ISSUED SECT. 1					
DATE 11/SEPT/75		DATE 19 FEB 76									
ENG <i>[Signature]</i>		PROG <i>[Signature]</i>									
DATE 16 MAR 76		DATE 16 MAR 76									
ITEM NO	DWG NO. / PART NO.	DESCRIPTION		5411793-0-0	5411793-1A						
45	1103341-00	DIODE HR1033B		1 D12	1						
46	1110968-00	DIODE 2N5062 SCR		1 D13	1						
47	1111205-00	DIODE 5.7V, 2% 4d, ZENER		1 D14	1						
48	1110864-00	DIODE LED MV5054-2		2 D15 D16	2						
49	1110420-00	DIODE A15B		1 D17	1						
50	1112594-02	DIODE A15M		1 D18	1						
51	1510421-00	TRANS D44CB		2 Q1	1						
52	1510196-00	TRANS 2B5302		2 Q2 Q3	2						
53	1510708-00	TRANS D45BB		1 Q4	1						
54	1510959-00	TRANS 2N3725		1 Q5	1						
55	1510706-00	TRANS A55		2 O6 O7	2						
56	1510928-00	TRANS C32A		1 O8	1						
57	1912108-00	I.C. LM 339		1 E1	1						
58	1212673-00	XFRM ESUR 10A		1 L1	1						
59	1212672-00	XFRM 1:5		1 L2	1						
60	9007227-00	15A SLOW BLOW FUSE		1 F1	1						
61	1210929-00	15A PICO FUSE		1 F2	1						
62	1302253-00	RES. 27K, 1/2%, 5%		1 R40	1						
63	4011798	ETCHED CIRCUIT BOARD		1	1						
64	1210737-01	HEAT SINK		1	1						
65	1100124-00	DIODE, 4.7V, 4%, 5% ZENER 750A		1 D2	0						
66	1212899	CONN 12 PIN		1 J2	0						
TITLE		ASSY NO. D-UA-5411793-0-0		SIZE CODE A PL	NUMBER 5411793-0-0	REV E	ECO NO H				
-5V REGULATOR POWER SUPPLY		SHEET 3 OF 4									

DEC FORM DEC 16 (175) 1031 N&B
924 110

DIGITAL EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS				PARTS LIST		ETCH REV E		QUANTITY/VARIATION			
MADE BY BOB WOLF		CHECKED <i>[Signature]</i>		SECTION 1		ISSUED SECT. 1					
DATE 11/SEPT/75		DATE 19 FEB 76									
ENG <i>[Signature]</i>		PROG <i>[Signature]</i>									
DATE 16 MAR 76		DATE 16 MAR 76									
ITEM NO	DWG NO. / PART NO.	DESCRIPTION		5411793-0-0	5411793-1A						
67	9007203-00	FUSE CLIP		2	2						
68	1209340-00	CONN MATE-N LOCK 8 PIN SKE		1 J1	1						
69	1209416-01	CONN CONTACT MATE-N-LOCK SKE W/PC 8 TAB		8	8						
70	C-NO-5309759-0-C	CAP STRAPS		2	2						
71	9009000-00	EYELETS GE-4-B		6	6						
72	9006557-00	4-40 KEYPIT		1	1						
73	9007793-01	SCREW 6-32 X 9/16 PHL PAN HD		6	6						
74	9006027-01	SCREW 6-32 X 7/8 PHL PAN HD		1	1						
75	9006010-01	SCREW 4-40 X 1/2 PHL PAN HD		1	1						
76	9006447-01	SCREW 6-32 X 2 PHL PAN HD		1	1						
77	9006653-00	FLAT WASHER 06		5	5						
78	9007201-00	TRANSIPAD		1	1						
79	9006185-00	6-32 KEYPIT		7	7						
80	9107256-11	TUBING, THIN WALL, CLR		AR	AR						
81	9008268-00	COMPOUND, THERMAL		AR	AR						
82	1213071-02	WASHER, INSULATING T02		1	1						
83	1213071-07	WASHER, INSULATING		1	1						
84	9009769	WASHER, RECT. MFG.		1	1						
85	1214074-01	JUMPER MECHANICAL		1	1						
TITLE		ASSY NO. D-UA-5411793-0-0		SIZE CODE A PL	NUMBER 5411793-0-0	REV E	ECO NO H				
-5V REGULATOR POWER SUPPLY		SHEET 4 OF 4									

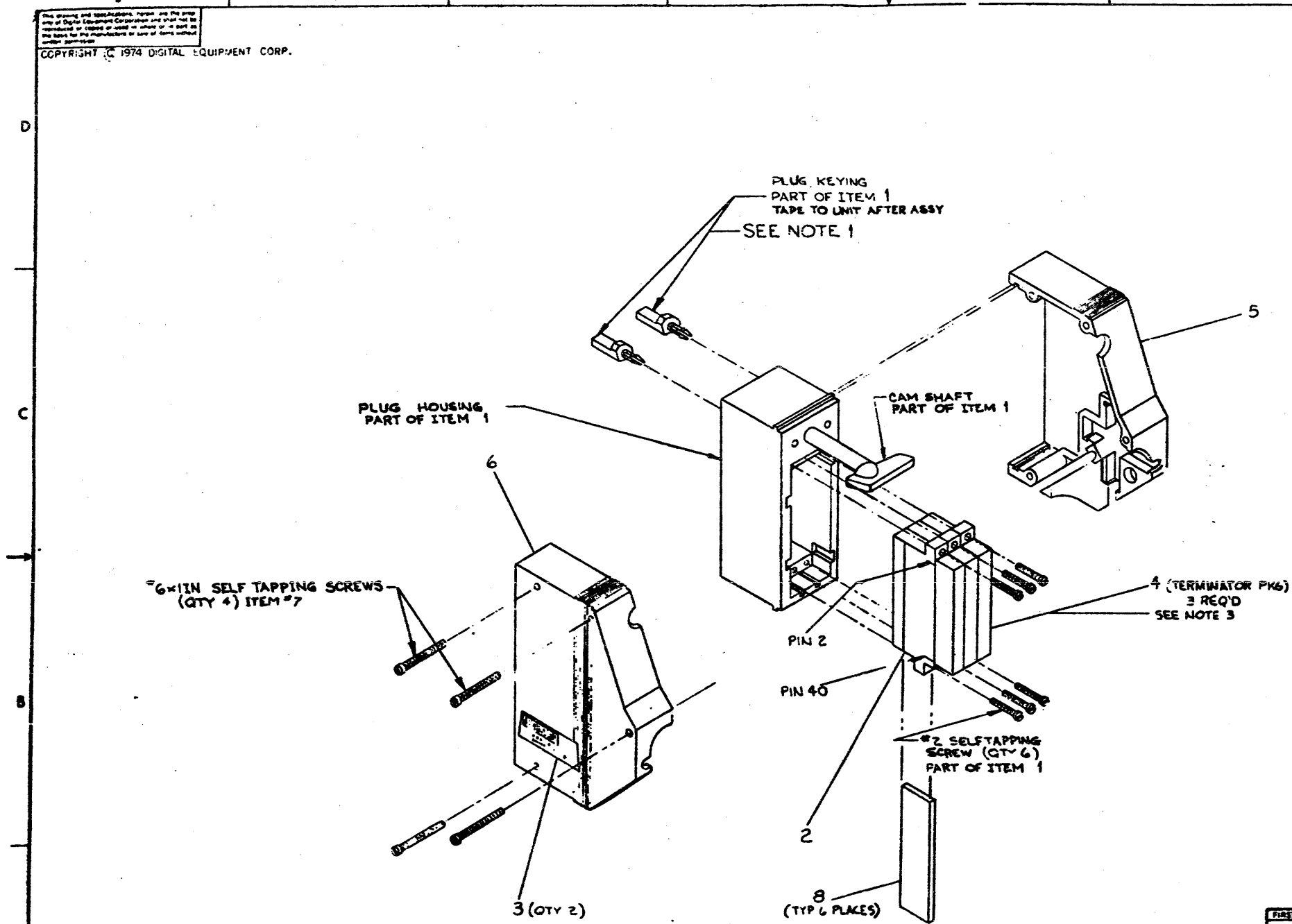
DEC FORM DEC 16 (175) 1031 N&B
924 110

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0-0-8E6600Z 07 2

- NOTES:**
1. PLUG KEYING, PLUG HOUSING, CAM SHAFT & MOUNTING HARDWARE ARE PART OF ITEM 1 (PLUG HOUSING & CAMSHAFT KIT).
 2. CUT JUMPER W2 ON H870 TERMINATOR MODULES.
 3. PRIOR TO ASSY OF ITEM 4 TO ITEM 2, CUT OFF LEAD AT PIN 39. CUT FLUSH WITH TERMINATOR BODY.



QTY	DESCRIPTION	PART NO.	ITEM NO.
6	COVER CONTACT	1211591-11	8
4	SELF THREAD SCREW #21	1211591-18	7
1	STRAIN RELIEF HOUSING R. H.	1211591-13	6
1	STRAIN RELIEF HOUSING L. H.	1211591-14	5
3	BISS BUS TERMINATOR	1313242	4
2	DECAL	A DC 7112208-00	3
3	PLUG ASSY	1211591-07	2
1	PLUG HOUSING & CAM SHAFT	1211591-01	1

FIRST USED OR OPTION-MODEL 2784		DATE 12/15/74		DATE 12/15/74		DATE 12/15/74		DATE 12/15/74	
UNLESS OTHERWISE SPECIFIED DIMENSIONS IN INCHES TOLERANCES		DATE 12/15/74		DATE 12/15/74		DATE 12/15/74		DATE 12/15/74	
DECIMALS	ANGLES	DATE 12/15/74		DATE 12/15/74		DATE 12/15/74		DATE 12/15/74	
AS - .005	10° ±	DATE 12/15/74		DATE 12/15/74		DATE 12/15/74		DATE 12/15/74	
AS - .002		DATE 12/15/74		DATE 12/15/74		DATE 12/15/74		DATE 12/15/74	
AS - .001		DATE 12/15/74		DATE 12/15/74		DATE 12/15/74		DATE 12/15/74	
REWORK BLANK AND BREAK SHARP CORNER SURFACE QUALITY		DATE 12/15/74		DATE 12/15/74		DATE 12/15/74		DATE 12/15/74	
MATERIAL		DATE 12/15/74		DATE 12/15/74		DATE 12/15/74		DATE 12/15/74	
SEE PARTS LIST		DATE 12/15/74		DATE 12/15/74		DATE 12/15/74		DATE 12/15/74	
FINISH		DATE 12/15/74		DATE 12/15/74		DATE 12/15/74		DATE 12/15/74	

REV	BY	DATE	DESCRIPTION
1	D. LIGNON	12/15/74	ISSUE FOR MANUFACTURE
2	J. BUNDAVIS	12/15/74	REVISED TO ADD PARTS LIST
3	S. RAJAT	12/15/74	REVISED TO ADD FINISH

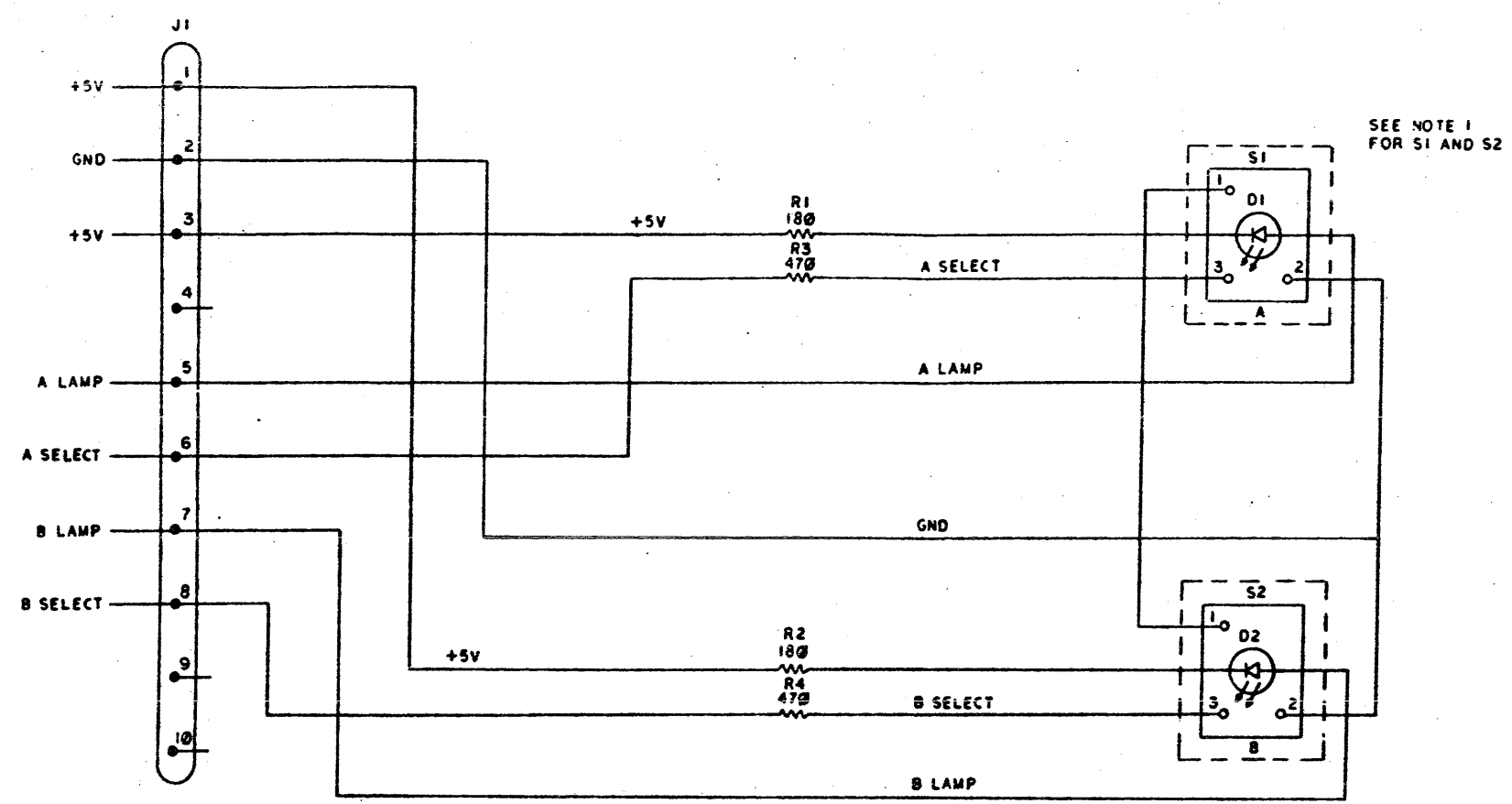
DAD 7009938-0-0

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CS 5413762-0-1 * 2

NOTE:
 1. S1 AND S2 SOLDER TO BOARD AT HIGHER ASSY.



DATE	09/15/79	DESIGNED BY	C. Smead	PRICE GROUP	RM02/03
CHK'D		DATE	10/15/79	TITLE	RM02/03 DUAL PORT SWITCH
ENGR.		PROJ. ENGR.		SIZE	D
PROJ. ENGR.		SCALE	1:1	NUMBER	CS 5413762-0-1 *
NEXT HIGHER ASSY.		SHEET	1 OF 1	REV.	

CS 5413762-0-1 *

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION	REFERENCE DESIGNATOR
1	D-UA-5413762-0-0		UNIT ASSY	REF	
2	D-CS-5413762-0-1		SCHEMATIC DIAGRAM	REF	
3	D-MD-5013761-0-0		DRILL & ETCH DWG	REF	
4		5013761-00	DRILL & ETCH BRD	1	
5		1212965-01	HEADER.100 10POS RT ANGLE	1	J1
6		1301322-00	180.0 .25 W 5.0 %	2	R1,R2
7		1300316-00	470.0 .25 W 5.0 %	2	R3,R4
8	D-EC-5013761-0-0		ETCH CUT DWG	REF	

REVISION HISTORY		BASIC PART NO: 5413762		DRN:	BILL CASSIDY	DATE: 2-FEB-78	D I G I T A L				
ENG:	ECO NUMBER	REV	SECTION A OF A	CHK'D:	P. BOSSMAN	DATE: 19-JUN-79	TITLE	PARTS LIST			
	INITIAL	#	SECTION VARIATION INDEX				RM02/03 DUAL PORT SW				
			[A] 00								
			[B]								
			[C]	DES.ENG:	LEO CAPPABIANCA	DATE: 06-JUN-79					
			[D]								
			[E]	RESP.ENG.:	L.CAPPABIANCA	DATE: 19-JUN-79	DOCUMENT NUMBER				
			[F]				SIZE	CODE	NUMBER	REV	
			[H]								
			[J]	MFG.ENG.:	C.STEINWEG	DATE: 09-AUG-79	K	PL	5413762-0-DBP	*	
			[K]								
			[L]	ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:		FILE NAME:		EDIT #	
			[M]	D-UA-5413762-0-0		#B-DD-5413762-0		Z0213.PLS		5	
			[N]								

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CARD LOCATOR CHART

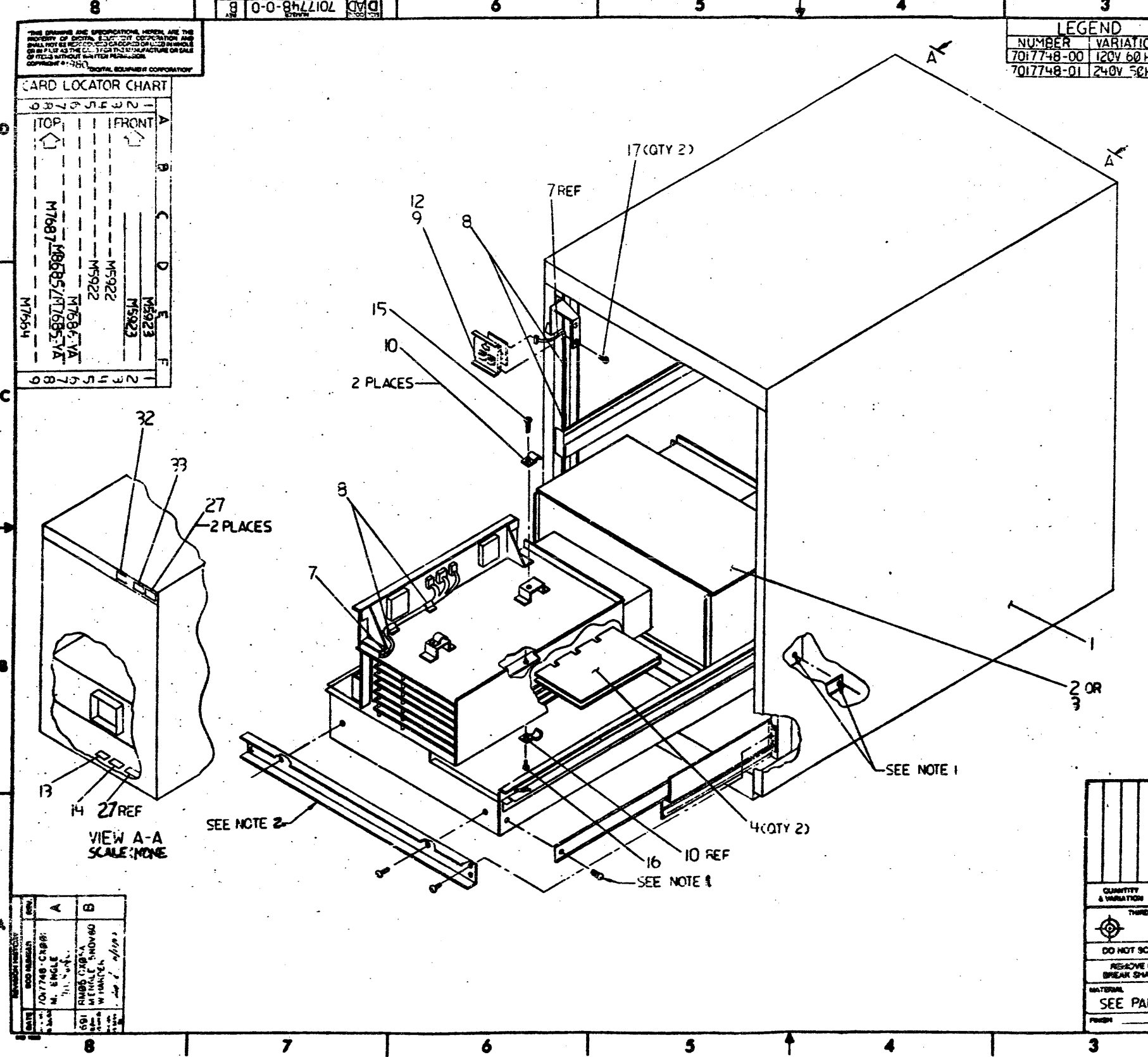
TOP	FRONT
M7687/M8685/A77695-VA	M5922
M7654	M5923

LEGEND

NUMBER	VARIATION
7017748-00	120V 60 HZ
7017748-01	240V 50 HZ

- NOTES:
- AFTER MBA CHASSIS (ITEM 2 OR 3) IS MOUNTED TO SLIDES, USE TWO 8-32 SCREWS (9006076-01) TO HARD MOUNT SLIDES TO FRONT OF CHASSIS. THEN SLIDE CHASSIS OUT REAR OF CABINET (ITEM 1) AND INSTALL REMAINING TWO SCREWS IN REAR MOUNTING HOLES OF SLIDES AND CHASSIS. SCREWS SUPPLIED WITH ITEM 2 OR 3.
 - SHIPPING BRACKET AND SIX 10-32 SCREWS (9006073-03) ARE TO BE INSTALLED AFTER MBA CHASSIS IS INSTALLED IN CABINET. SCREWS SUPPLIED WITH ITEM 2 OR 3.
 - FOR CABLING AND GROUNDING CONFIGURATION SEE D-UA-RM05-0-0.
 - SHIP THE FOLLOWING ITEMS UNASSEMBLED WITH UNIT:

ITEM NO.	QTY	PART NO.	DESCRIPTION
5	2	7419781-25	BC06S REWORK
6	1	7009491-02	POWER SEQUENCE CABLE
18	1	7412827-12	GROUND STRAP
20	1	7017755-05	GROUND STRAP
30	1	9006568-00	5/16 KEP NUT
31	2	9006638-00	5/16 LOCKWASHER



CAUTION: OFF SHEET PARTS LIST EXISTS. SEE K-PL-7017748-G-DBP (Z1200)

REV	DATE	DESCRIPTION
1	10/17/48	CHASSIS
2	11/11/48	REWORK
3	11/11/48	REWORK
4	11/11/48	REWORK
5	11/11/48	REWORK
6	11/11/48	REWORK
7	11/11/48	REWORK
8	11/11/48	REWORK

DESCRIPTION	DRAWING NO.	PART NO.
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES AND THE FOLLOWING TOLERANCES APPLY (PER DBC STD 114L)		
FINISH	UNLESS OTHERWISE SPECIFIED	
QUANTITY & VARIATION		
THIRD ANGLE PROJECTION		
DO NOT SCALE DRAWING		
REMOVE BURRS AND BREAK SHARP CORNERS		
MATERIAL		
SEE PARTS LIST		
DATE	BY	CHECKED
10/17/48	J. K. HUBBARD	J. K. HUBBARD
TITLE: CABINET AND MBA ASSY DUAL PORT		
DOCUMENT NUMBER: D UA-RM05-0-0		

LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION	
					00	01
1	1	D-AD-7017612-0-0	7017612-00	CABINET ASSY	1	1
2	2	D-AD-7018322-0-0	7018322-00	MBA CHASSIS ASSY (60 HZ)	1	-
3	3	D-AD-7018322-0-0	7018322-01	MBA CHASSIS ASSY (50 HZ)	-	1
4	4		M5923-00	MASS BUS TRANSCEIVER, PORT B	2	2
5	5	E-IA-7419781-0-0	7419781-25	BC06S (REWORK) 25 FT.	2	2
6	6	D-IA-7009491-0-0	7009491-02	POWER SEQUENCE CABLE	1	1
7	7	D-IA-7016548-0-0	7016548-01	DUAL PORT HARNESS ASSEMBLY (66 I	1	1
8	8		9008341-00	CLIP,PVC HARNESS, DESKLASP,1" WID	4	4
9	9	C-AD-7017599-0-0	7017599-00	SWITCH PANEL ASSY (UPPER)	1	1
10	10	B-MD-7418607-0-0	7418607-00	CLAMP CABLE	2	2
11	11		7009938-00	*** THIS ITEM IS NOT USED ***	-	-
12	12		3617187-01	LABEL,"PORT SELECT (LEFT)" FOR	1	1
13	13	A-DC-7416197-0-0	7416197-02	SILK SCREEN	1	1
14	14		3613211-00	DECAL,CLEAR PREPRINTED CSA 1-1/4	1	1
15	15		9006418-01	SCREW,TAPPING,TYPE TT,PAN,PHIL,	1	1
16	16		9006036-01	SCREW,PAN,PHIL 8-32X 5/16 SS	1	1
17	17		9008020-01	SCREW,PAN,PHIL 6-32X 3/16 SS	2	2
18	18	D-IA-7412827-0-0	7412827-12	RK06 GROUND CABLE	1	1
19	19	C-IA-7017754-0-0	7017754-12	*** THIS ITEM IS NOT USED ***	-	-
20	20	C-IA-7017755-0-0	7017755-05	STRAP, GROUNDING (4' 0")	1	1
21	21	C-IA-7017756-0-0	7017756-06	*** THIS ITEM IS NOT USED ***	-	-
22	22		9006635-00	*** THIS ITEM IS NOT USED ***	-	-
23	23		9006565-00	*** THIS ITEM IS NOT USED ***	-	-
24	24		1214434-02	*** THIS ITEM IS NOT USED ***	-	-
25	25		1209456-01	*** THIS ITEM IS NOT USED ***	-	-
26	26		1209350-03	*** THIS ITEM IS NOT USED ***	-	-
27	27		3613209-00	LABEL,ELECTRICAL DATA	2	2
28	28		7017610-0J	*** THIS ITEM IS NOT USED ***	-	-
29	29		9006073-03	*** THIS ITEM IS NOT USED ***	-	-
30	30		9006568-00	NUT,KEP ,5/16-18 X1/2 AF	1	1

REVISION HISTORY			BASIC PART NO: 7017748			D I G I T A L		
ENG	ECO NUMBER	REV	SECTION A OF A	DRN:	M. ENGLE	DATE: 12-MAY-80	TITLE	PARTS LIST
ME	7017748-CX001	A	SECTION. VARIATION INDEX	CHK'D:	B. NICHOLS	DATE: 12-MAY-80	CABINET & MBA ASSY DUAL PORT	
WH	RM05-CX05A	B	[A] 00,01	DES.ENG.:	A. CLARK	DATE: 12-MAY-80		
			[B]	RESP.ENG.:	B. MONTERO	DATE: 12-MAY-80		DOCUMENT NUMBER
			[C]	MFG.ENG.:	C. STEINWEG	DATE: 12-MAY-80	SIZE: CODE: NUMBER	REV
			[D]	ASSEMBLY NUMBER:			K PL 7017748-0-DBP	B
			[E]				FILE NAME:	EDIT #
			[F]	D-AD-7017748-0-0			Z1200B.PLS	7

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AUTOMATED BY FRTLST.3P(44)

PARTS LIST

SHEET A2 OF A2

LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION	
					00	01
31	31		9006638-00	WASHER, LOCK-INT, .6000D X .326ID	3	3
32	32	A-DC-7416197-0-0	7416197-01	DECAL	1	1
33	33		3613211-01	DECAL, CLEAR PREPRINTED CSA 1-1/4	1	1

D	I	G	I	T	A	L	TITLE	CABINET & MBA ASSY DUAL PORT	SECTION A OF A	SIZE	CODE	DOCUMENT NUMBER	REV
										K	FL	7017748-0-DBP	B

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY	PER VARIATION
				60	01
1	D-AD-7018322-0-0	7018322-00	MBA CHASSIS ASSY (60 HZ)	1	-
2	L-AD-7018322-0-0	7018322-01	MBA CHASSIS ASSY (50 HZ)	-	1
3		M5923-00	MASS BUS TRANSCEIVER, PORT B	2	2
4	C-MD-7419781-0-0	7419781-04	BC06S (REWORK) 4 FT.	2	2
5	C-IA-7017605-0-0	7017605-00	DUEL, PORT SWITCH ASSY	1	1
6		9008341-00	CLIP,PVC HARNESS, DESKLASP,1'WID	2	2
7	B-MD-7418607-0-0	7418607-00	CLAMP CABLE	2	2
8		7009938-00	*** THIS ITEM IS NOT USED ***	-	-
9	D-IA-7009491-0-0	7009491-00	CABLE	1	1
10		3617187-00	LABEL,"PORT SELECT (RIGHT)" FOR	1	1
11		9006418-01	SCREW,TAPPING,TYPE TT,PAN ,PHIL,	1	1
12		9006036-01	SCREW,PAN,PHIL 8-32X 5/16 SS	1	1
13	C-IA-7017755-0-0	7017755-05	STRAP, GROUNDING (4' 0")	1	1
14	C-IA-7017754-0-0	7017754-12	*** THIS ITEM IS NOT USED ***	-	-
15		9006565-00	*** THIS ITEM IS NOT USED ***	-	-
16		9006635-00	*** THIS ITEM IS NOT USED ***	-	-
17		1214434-02	*** THIS ITEM IS NOT USED ***	-	-
18		1209456-01	*** THIS ITEM IS NOT USED ***	-	-
19		1209350-03	*** THIS ITEM IS NOT USED ***	-	-
20	A-PI-3700589-0-0	3700589-00	PKG ASSY RM05 MASS BUS	REF	REF
21		3613209-02	LABEL,ELECTRICAL DATA	2	2

22 NOTE: -
 23 NOTE: -
 24 NOTE: -
 25 NOTE: -

LEGEND
 PART NUMBER VARIATION
 7017747-00 120V 60HZ
 7017747-01 240V 50HZ

REVISION HISTORY			BASIC PART NO: 7017747			DRN: M. ENGLE DATE: 12-MAY-80			D I G I T A L		
ENG	ECO NUMBER	REV	SECTION A OF A	CHK'D:	B. NICHOLS	DATE:	12-MAY-80	TITLE	PARTS LIST		
	INITIAL	*	SECTION. VARIATION INDEX					MBA ASSY,DUAL PORT			
GN	7017747-CX001	A	[A] 00,01								
WH	RM05-CX05A	B	[B]	DES.ENG.:	A. CLARK	DATE:	12-MAY-80				
	<i>P. Clark 11/3/80</i>		[C]	RESP.ENG.:	B. MONTERO	DATE:	12-MAY-80	DOCUMENT NUMBER			
			[D]					SIZE CODE NUMBER REV			
			[E]	MFG.ENG.:	C. STEINWEG	DATE:	12-MAY-80	K PL 7017747-0-DBP	B		
			[F]	ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:		FILE NAME:	EDIT #		
				D-AD-7017747-0-0		#B-DD-RM05-0		Z1199B.PLS	14		

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